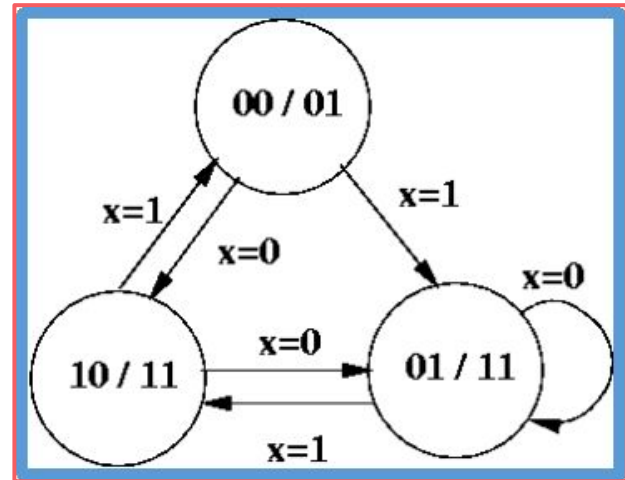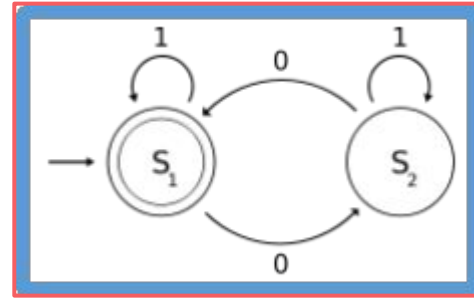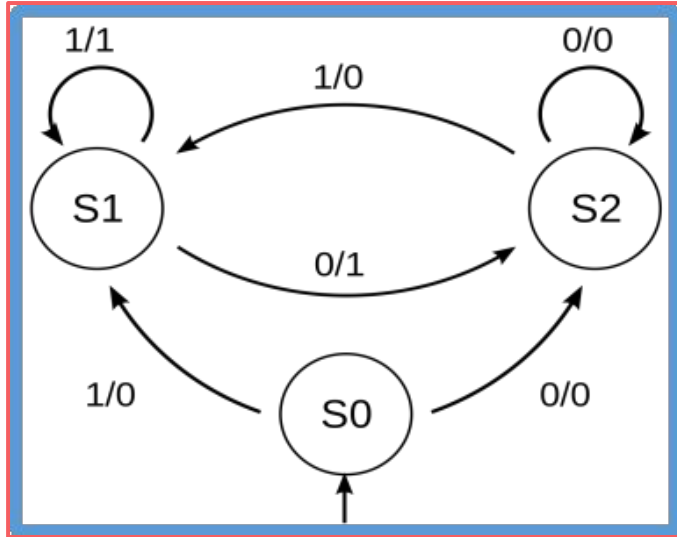# Work Package #6

Work Package #7

1. Implement and simulate a BDC-to-Excess3 converter.
2. Research, implement and simulate a Carry Skip and a Carry Lookahead 16-bit adder, and their respective full testbenches.
3. Implement and simulate the following multiplier (http://nanocad.ee.ucla.edu/pub/Main/Publications/JOLPE_v3.pdf)
4. Implement the appropriate FSM controlling the following traffic lights: