

Dr. D. Y. Patil Pratishthan's

DR. D. Y. PATIL INSTITUTE OF ENGINEERING, MANAGEMENT & RESEARCH

Approved by A.I.C.T.E, New Delhi, Maharashtra State Government, Affiliated to Savitribai Phule Pune University Sector No. 29, PCNTDA, Nigidi Pradhikaran, Akurdi, Pune 411044. Phone: 020–27654470, Fax: 020-27656566 Website: www.dypiemr.ac.in Email: principal.dypiemr@gmail.com

COMPUTER ENGINEERING DEPARTMENT

GUEST LECTURE



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GUEST LECTURE On TURING MACHINE – THEORY OF COMPUTATION





Participants : TE students of DYPIEMR Venue : DYPIEMR, Seminar Hall Dr. D. Y. Patil Educational Complex, Akurdi, Pune – 44 **Date :** 18/08/17 **Organizing Team** : Ms. Ketaki Bhoyar, Subject Teacher TOC. Mrs. Abha Jain , CSI Coordinator

1. Objectives

- Understand the Overview of Theory of computation(TOC)
- Discuss Turing Machine model in detail
- Applications of Turing Machine
- Applications and research areas of TOC

. Vivek Kulkarni				
ork Experience:	Cl.'sfsl.'44	Design of Control	DA IAL E	
Currently Working as	Cmei architect,	Persistent Syste	ms Pvt. Lta., Er	andwana

3. Report

Name of the Speaker: Mr Vivek Kulkarni

Designation: Chief architect,

Company: Persistent Systems Pvt. Ltd., Erandwana, Pune.

Title: Turing Machine – Theory of Computation

Day & Date: 18/08/2017

Highlights of the Talk:

- Overview of Theory of computation(TOC)
- Turing Machine model in detail
- Applications of Turing Machine
- Applications and research areas of TOC

The Session was organized at the Seminar Hall at 09.00am for the TE Computer Engineering Students as a part of covering the contents beyond syllabus.

Details of the session:

Vivek Kulkarni, author of book Theory of Computation published by Oxford University, gave an insight to the students of third year to correlate object oriented programming and theory of computation. Following topics were covered in the lecture.

1. Set theory

Set theory is a branch of mathematical logic that studies sets, which informally are collections of objects. Although any type of object can be collected into a set, set theory

is applied most often to objects that are relevant to mathematics. The language of set theory can be used in the definitions of nearly all mathematical objects.

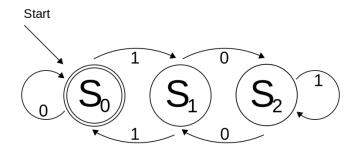
The modern study of set theory was initiated by Georg Cantor and Richard Dedekind in the 1870s. After the discovery of paradoxes in naive set theory, such as the Russell's paradox, numerous axiom systems were proposed in the early twentieth century, of which the Zermelo–Fraenkel axioms, with or without the axiom of choice, are the best-known.

Set theory is commonly employed as a foundational system for mathematics, particularly in the form of Zermelo–Fraenkel set theory with the axiom of choice. Beyond its foundational role, set theory is a branch of mathematics in its own right, with an active research community. Contemporary research into set theory includes a diverse collection of topics, ranging from the structure of the real number line to the study of the consistency of large cardinals.

2. Deterministic Finite Automata

In the theory of computation, a branch of theoretical computer science, a deterministic finite automaton (DFA)—also known as a deterministic finite acceptor (DFA) and a deterministic finite state machine (DFSM)—is a finite-state machine that accepts and rejects strings of symbols and only produces a unique computation (or run) of the automaton for each input string. Deterministic refers to the uniqueness of the computation. In search of the simplest models to capture finite-state machines, McCulloch and Pitts were among the first researchers to introduce a concept similar to finite automata in 1943.

The figure illustrates a deterministic finite automaton using a state diagram. In the automaton, there are three states: S0, S1, and S2 (denoted graphically by circles). The automaton takes a finite sequence of 0s and 1s as input. For each state, there is a transition arrow leading out to a next state for both 0 and 1. Upon reading a symbol, a DFA jumps deterministically from one state to another by following the transition arrow. For example, if the automaton is currently in state S0 and the current input symbol is 1, then it deterministically jumps to state S1. A DFA has a start state (denoted graphically by an arrow coming in from nowhere) where computations begin, and a set of accept states (denoted graphically by a double circle) which help define when a computation is successful.



3. Push Down Automata

In computer science, a pushdown automaton (PDA) is a type of automaton that employs a stack.

Pushdown automata are used in theories about what can be computed by machines. They are more capable than finite-state machines but less capable than Turing machines. Deterministic pushdown automata can recognize all deterministic context-free languages while nondeterministic ones can recognize all context-free languages, with the former often used in parser design.

The term "pushdown" refers to the fact that the stack can be regarded as being "pushed down" like a tray dispenser at a cafeteria, since the operations never work on elements other than the top element. A stack automaton, by contrast, does allow access to and operations on deeper elements. Stack automata can recognize a strictly larger set of languages than pushdown automata.[1] A nested stack automaton allows full access, and also allows stacked values to be entire sub-stacks rather than just single finite symbols.

4. Turing Machine

A Turing machine is a mathematical model of computation that defines an abstract machine which manipulates symbols on a strip of tape according to a table of rules. Despite the model's simplicity, given any computer algorithm, a Turing machine can be constructed that is capable of simulating that algorithm's logic.

The machine operates on an infinite memory tape divided into discrete cells. The machine positions its head over a cell and "reads" (scans) the symbol there. Then, as per the symbol and its present place in a finite table of user-specified instructions, the machine (i) writes a symbol (e.g. a digit or a letter from a finite alphabet) in the cell (some models allowing symbol erasure or no writing), then (ii) either moves the tape one cell left or right (some models allow no motion, some models move the head), then (iii) (as determined by the observed symbol and the machine's place in the table) either proceeds to a subsequent instruction or halts the computation.



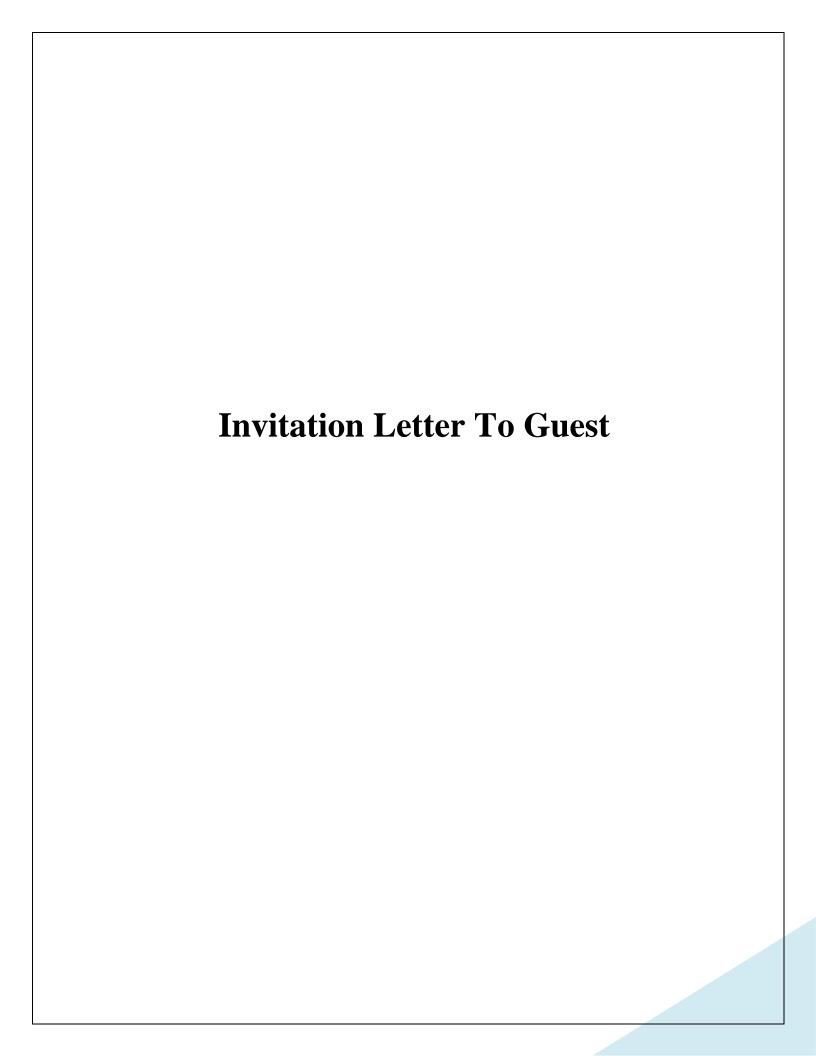
Snapshot 1: Students Listening Guest Lecture

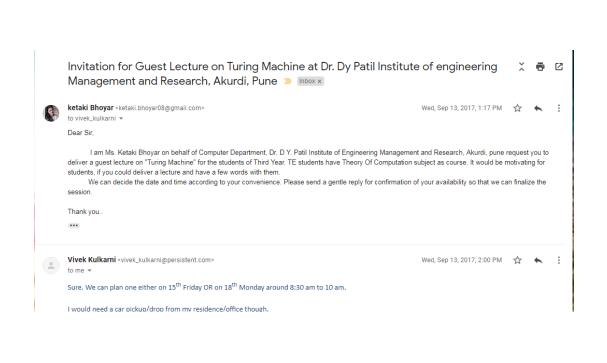


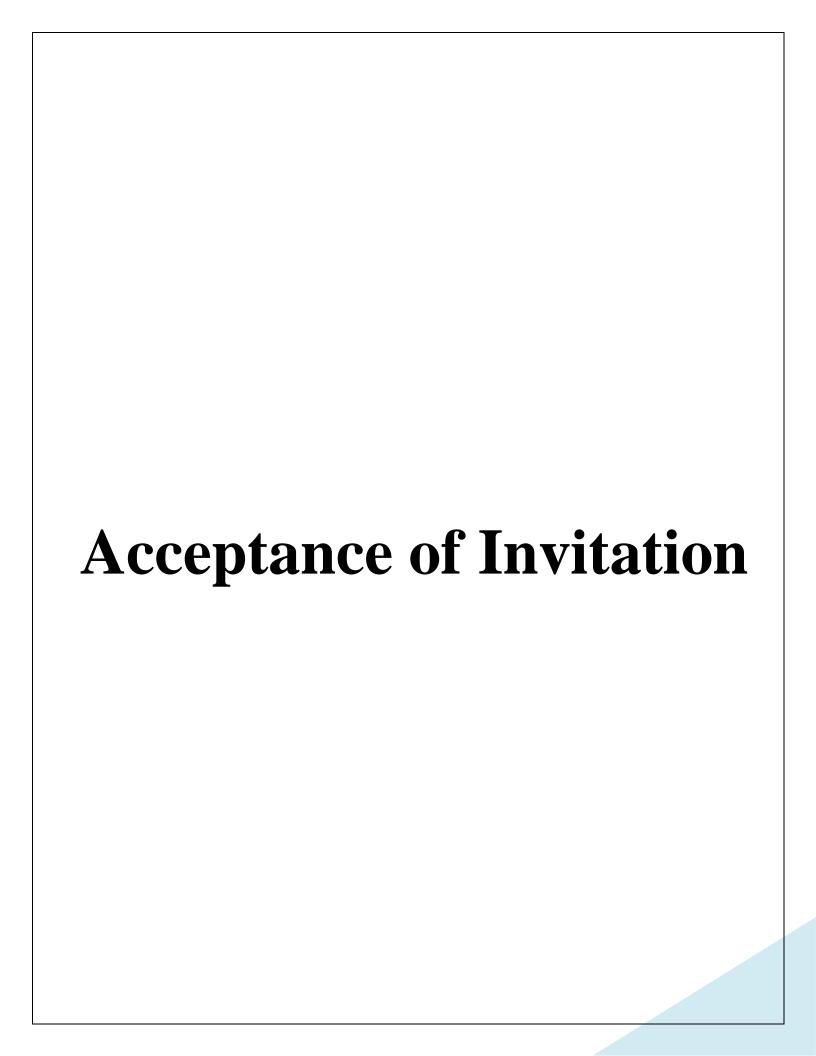
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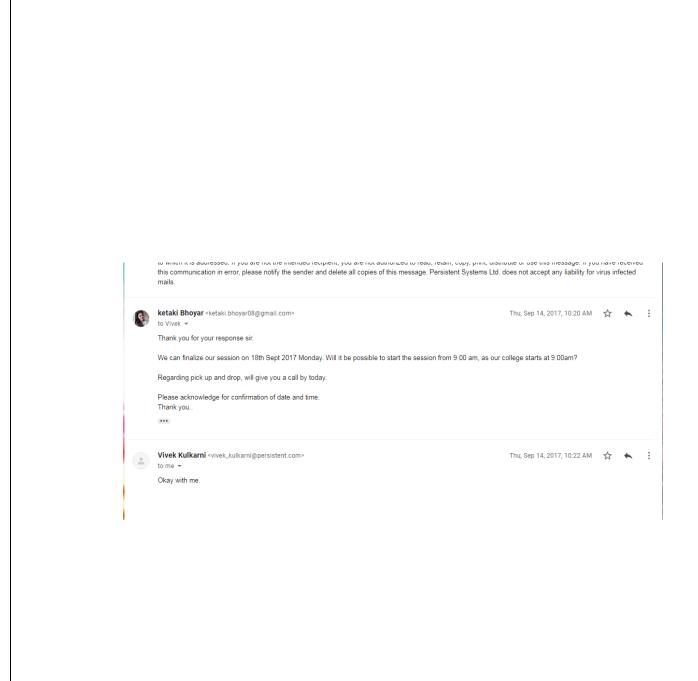


Snapshot 3: Students Listening Guest Lecture



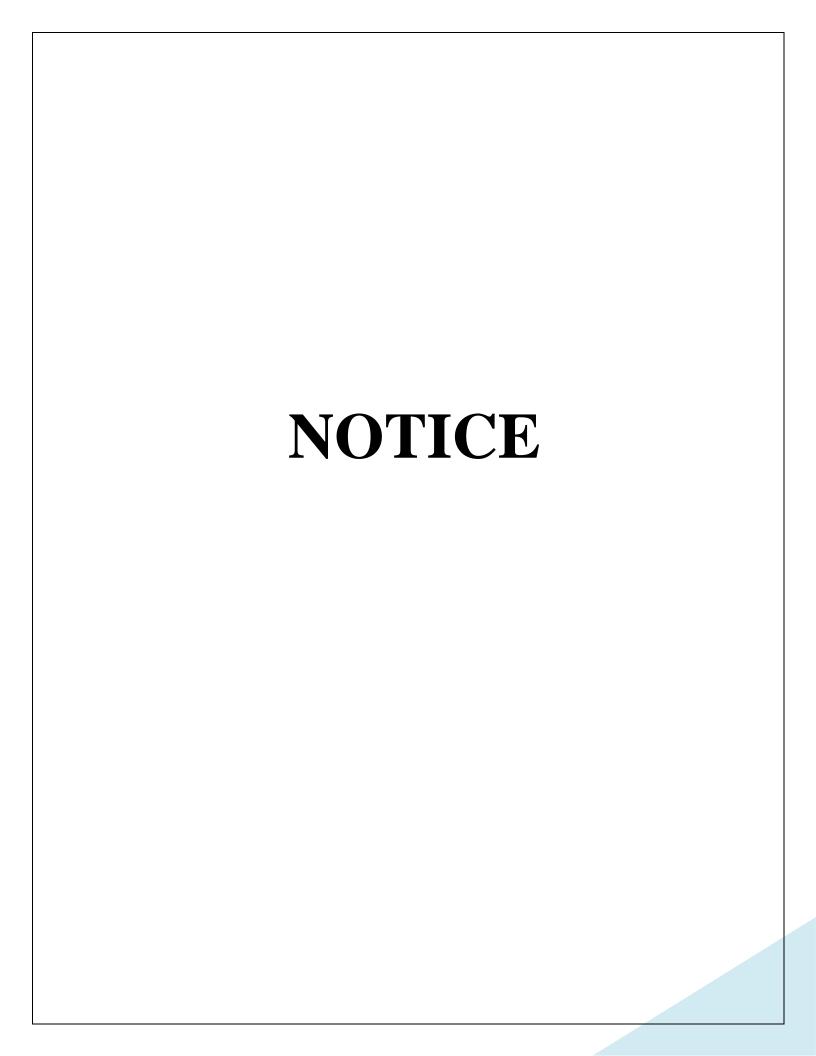






Learning Outcomes

- Students are able to understand the Overview of Theory of computation(TOC)
- Students get information about Turing Machine model in detail
- Students have understood applications of Turing Machine
- Students came to know Applications and research areas of TOC







NOTICE

Academic Year: 2017-18

Semester: 1

Date: 14/08/2017 Year: T.E.

All the students of TE A and B div are informed that a guest session is organized on the concepts of Turing Machine from Theory of computation on 18th August 2017 at 9.00 am sharp. The speaker is Mr. Vivek Kulkarni, Chief architect, Persistent Systems Pvt. Ltd., Erandwana, who is a proper of the book "Theory of Computation".

author of the book "Theory of Computation".

This session will be helpful to clear more concepts about Turing Machine along with its applications.

Time: 09:00 am – 11:00am Venue: Seminar Hall

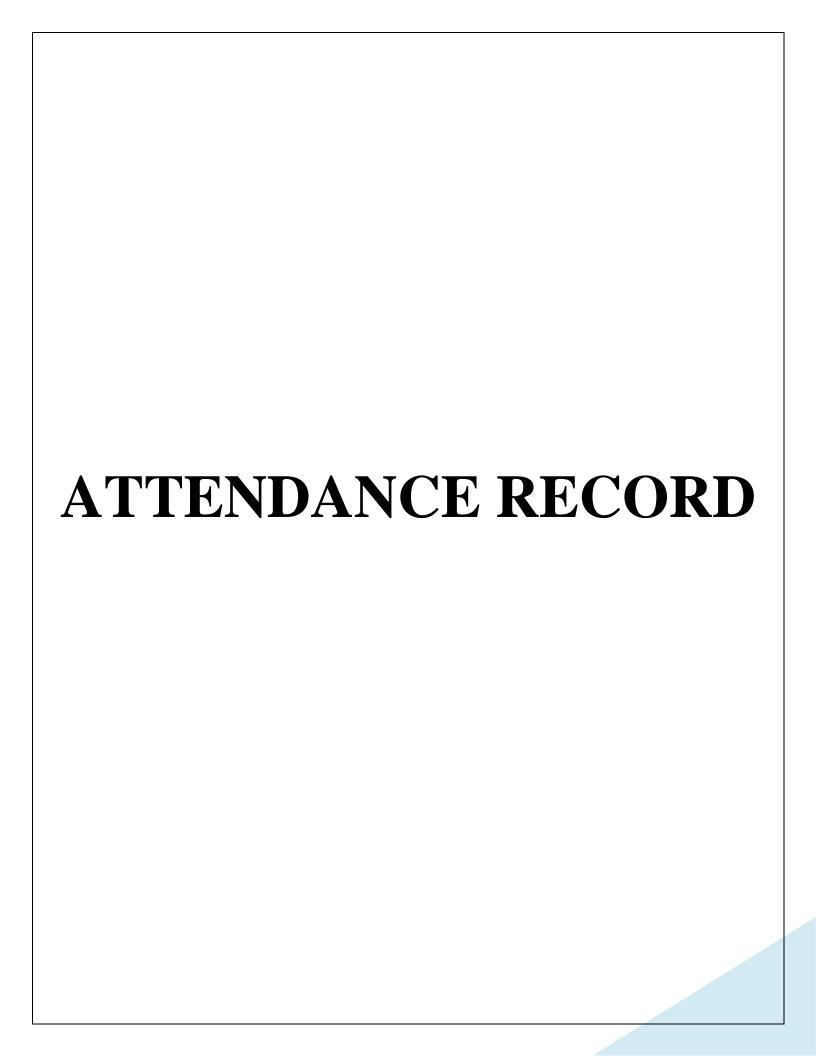
Note: It is mandatory for all to attend this lecture.

Ms. Netaki bhoyar ACM Student Chapter Coordinator Prof. P. P. Shevatekar Head of the Department

Department of Computer Engineering

Or. D.Y. Paul Institute of Engineering.

***enegament & Research, Alunt. Puris - 411 044



Dr. D. Y. Patil Institute of Engineering, Management and Department of Computer Engineering Turing Machine-TOC Guest Lecture Attendance TE (A)

Date: 18/09/17

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Mrs. Abha Jain

Mrs. Shivganga Gavhane

Mrs. Suvarna Patil

Dr. D. Y. Patil Institute of Engineering, Management and Research, Department of Computer Engineering Turing Machine-TOC Guest Lecture Attendance TE (B)

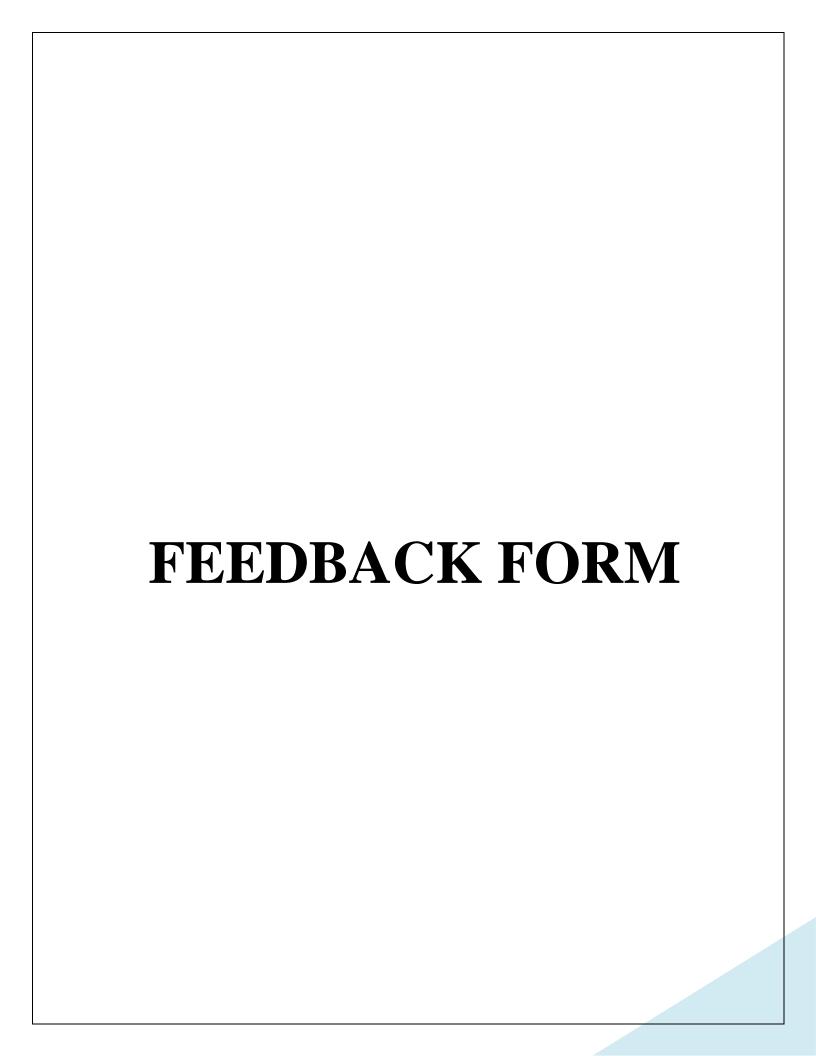
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Ms. Ketaki Bhoyar Mrs. Tanuja Lonhari Coordinators

Mrs. Abha Jain Mrs. Shivganga Gavhane Mrs. Suvarna Patil Coordinators





Theory of Computation Guest Lecture Feedback Form

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Sr.No.	Questions	Strongly Agree	Agree	Neutral	Disagree	Strongly Disagree
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02	Guest Lecture was well organized.	~				
03	I will be able to apply the acquired knowledge in my day to day life.	L-				
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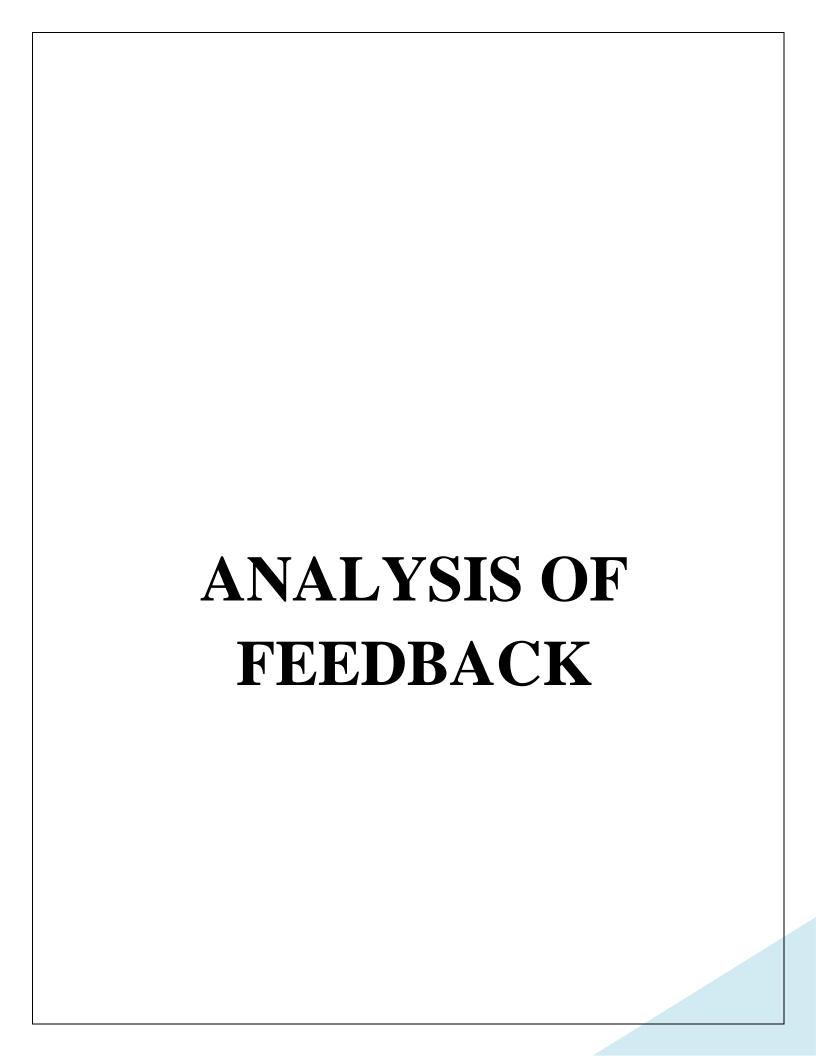
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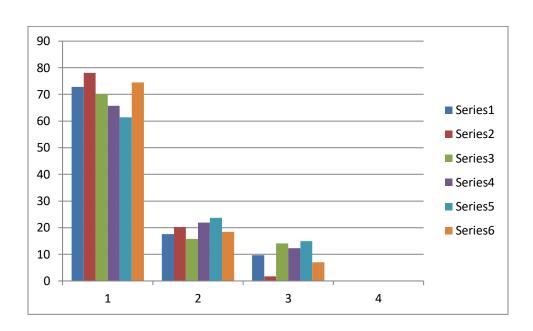
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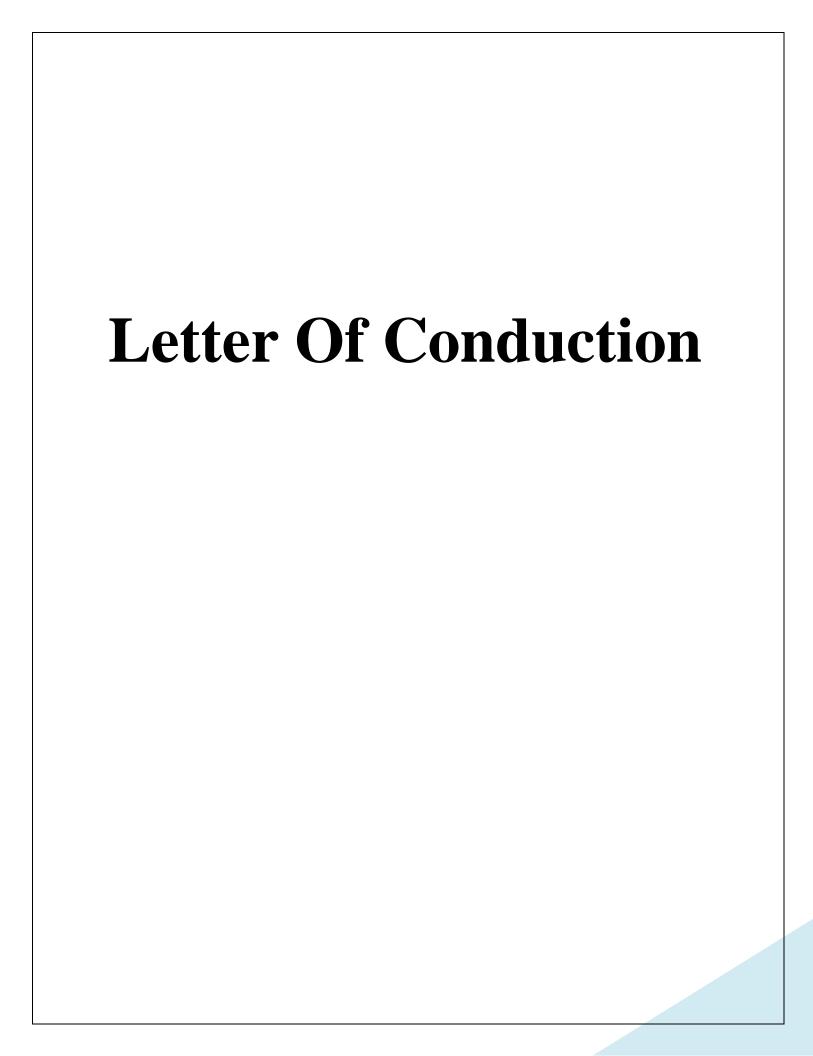


Theory of Computation Guest Lecture Feedback Form

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CONDUCTION CERTIFICATE

This is to certify that Mr. Vivek Kulkarni, Chief Architect – Persistent-LABS, Persistent Systems Ltd. his conducted Guest Lecture on "Turing Machine – Theory of Computation" at Dr. D. Y. Patil Institute of Engineering, Management and Research, Akurdi on 18/09/17 for the students of Third Year.

We thank you for your valuable inputs.

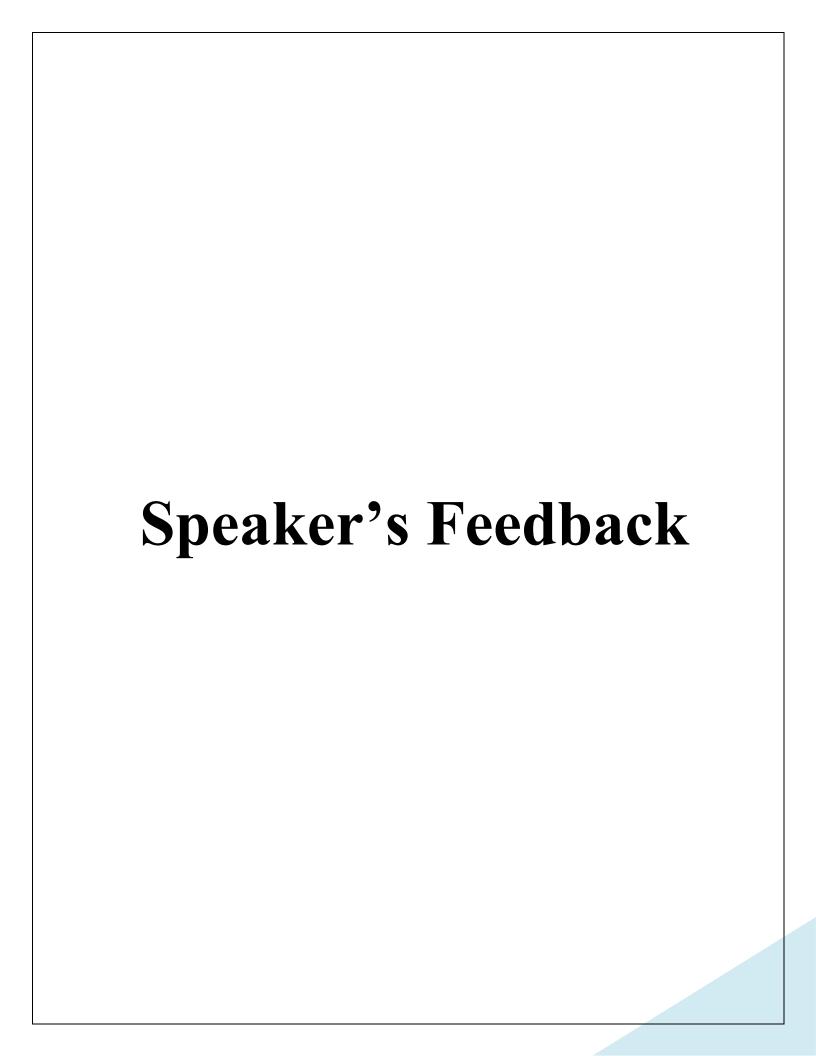
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Prof. P. P. Shevatekov Prof. P. P. Shevatekov Head of the Department Department of Computer Engineering

Department of Computer of Engineering

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Management & Research, Akurdi, Pune - 414 044





Year: 2017-18

Date: 18/09/2017

Feedback

All lethours & students were very receptive. Enjoyed the session.

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