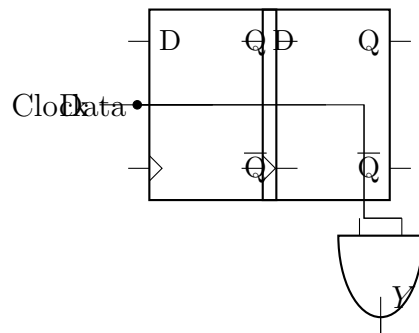


# GATE-EE 2011

## Digital Logic Question

### Q.6

When the output  $Y$  in the circuit below is “1”, it implies that data has:



(A) changed from “0” to “1”

(C) changed in either direction

(B) changed from “1” to “0”

(D) not changed

Correct Answer

Option (C):