DNN Model Placement for Edge Intelligence

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Abstract—**Edge Intelligence (EI) has emerged as a promising area in recent years. In order to mitigate some of the challenges associated with using AI in cloud servers, EI utilizes edge computing to deliver AI services to users. The dominant approach to creating AI services is deep neural networks (DNN) which have shown significant results in computer vision, language processing, robotics, etc., tasks. In the cloud, these models run on powerful servers, but they are slow to respond, have privacy concerns, and have low reliability. EI can help to overcome these obstacles. However, edge servers are typically constrained in terms of processing power and energy. Therefore, special care must be taken to deploy DNN models on edge servers. Our purpose in this project is to partition DNN models and place each part either on edge servers or the user device. We use optimization approaches to analyze this problem.**

Index Terms—**Edge intelligence, Model placement, Neural network, Convex optimization**

I. INTRODUCTION

Deep Neural Networks (DNNs) have become increasingly popular as the core machine learning technique in many fields, such as speech recognition, image classification, translation, language modeling, and video captioning. DNNs are widely used to perform these tasks due to their high accuracy and adaptability. The training of model parameters requires massive amounts of data that can only be achieved with powerful hardware and adequate time. Additionally, since neural networks are widely used, it is essential to research how to utilize and train them optimally.

One solution to tackle this issue is cloud computing, which undoubtedly poses real challenges to network capacity and the computing power of cloud computing infrastructures. Also, many new applications such as cooperative autonomous driving, are sensitive to delay requirements that the cloud infrastructures would have difficulty meeting since they may be far from the users.

Another solution that has become a hot topic in the computation context is edge computing. Edge computing has many benefits, such as alleviating the network traffic load as less data is exchanged with the cloud compared to the cloud-only scenario. Furthermore, services hosted at the edge can substantially reduce the delay time of data transmissions and improve the response time. The users' privacy is also enhanced as the private data is stored locally on the edge or user devices instead of cloud servers. The hierarchical computing architecture provides more reliable computation, and finally, edge computing can promote the pervasive application of DNNs.

Cloud computing and edge computing aren't mutually exclusive, and that's crucial to understand. In other words, edge computing complements and extends cloud computing. High processing power, giant storage, and data backup are some of the capabilities of cloud servers that edge servers lack. On the contrary, low latency, privacy assurance, and real-time processing are some of the capabilities of edge servers that cloud servers lack. The combination of these computation paradigms enables cumulating advantages of each and reduces the disadvantages.

Edge intelligence (EI) refers to the data analysis and development of solutions at the site where data is generated. Indeed, edge intelligence reduces latency, costs, and security risks, thus making the associated business more efficient. In other words, edge devices help DNN models to be trained and deployed more efficiently.

EI is used for optimizing the costs associated with fleet management. Industrial manufacturers use EI to improve real-time data processing. Software companies use EI to develop personalized data-driven experiences to attract their customers. Multiple System Operators (MSOs) use EI to maximize the lifetime value of their subscribers.

II. RELATED WORK

Throughout this section, we review some research related to mobile edge intelligence, server selection, and model placement.

Edge intelligence (EI), the integration of mobile edge computing (MEC) and AI technologies, has recently emerged as a promising paradigm to support computation-intensive AI applications at the network edge [1]. An edge-based MEC network with multiple edge servers is studied in [2] to maximize the number of computing offloading requests under edge storage, computation, and communication constraints. To cope with the unknown and fluctuating service demand, [3] proposed an online learning algorithm to optimize spatial-temporal dynamic service placement decisions among multiple edge servers to minimize the computation delay. Considering parallel computing at both cloud and edge servers, [4] and [5] studied collaborative service placement and computation offloading to minimize the computation latency.

Due to resource limitations on the edge server, server selection has received considerable attention in recent years [6]. Researchers have proposed techniques to achieve optimal goals when selecting servers, such as minimizing the average delay [7], maximizing the resources utilization efficiency [8], minimizing energy consumption [9] and etc.

In some research like [10], the continuous server selection problem is modeled as a Markov Decision Process (MDP). Deep Reinforcement Learning (DRL) techniques are proposed to address the issue of dealing with highly dynamic environments modeled by MDP. The time and computations necessary to train DRL models are substantial, even though these methods can reach good solutions. Additionally, since
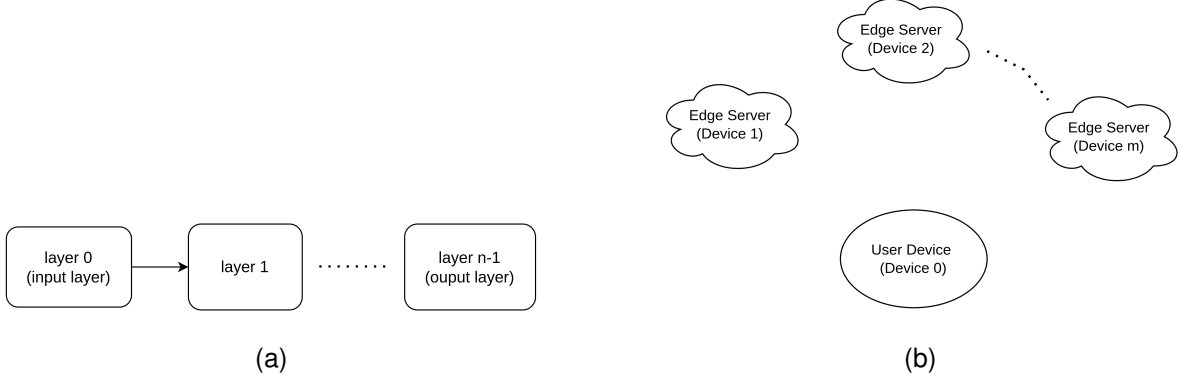
Fig. 1. System model. (a) Neural network architecture. (b) Computation recources.

DRL models need to be trained periodically, this method cannot be used in many situations, especially in real-time applications.

III. METHODOLOGY

A. introduction

We analyze this problem from an optimization point of view. This technique is used in [11], [12], and [13] articles. Every DNN model can be thought of as a directed graph in which nodes are operations and edges are data (tensor in software terms). The direction of the edge shows the dependency between nodes. One can model this graph with three possible granularities: (a) neuron, (b) operation, and (c) layer. With neuron granularity, the dimension of the problem is quite high and the number of connections between neurons prevents partitioning the graph effectively. Layer abstraction, however, results in a small graph, which makes parallelization difficult. Therefore, we use operation granularity in this project.

Placement problems consist of two subproblems: (a) graph partitioning and (b) task assignment. Graph partitioning requires segmentation points that enable parallel execution of each subgraph and minimizes dependency between subgraphs. When subgraphs are created, they must be processed on the user device or an edge server. This task assignment problem can be solved via optimization techniques.

Let $G(V, E)$ to be the model graph and $\mathcal{P} = \{p_1, p_2, \cdots p_k\}$ be the list of k processors. A model placement means finding the set of $\{(p_1, s_1), (p_2, s_2), \cdots, (p_k, s_k)\}$, where each s_i is a subgraph and all s_i form a partition of graph. In other words, $\bigcup s_i = V$, and $\bigcap s_i = \emptyset$. Among all feasible points, we choose one that minimizes total latency of model execution. The objective function depends on how latency is defined and will be the subject of project itself.

B. Formulation

System model is described in figure 1. We assume that neural network architecture is composed of n layers. The data flow between layers is shown by a directed edge connecting the layers. Each directed edge transfers the output of one layer to the next one. Also, the output of a layer is a tensor, a

generalization of matrices in a higher dimensional space. Each directed edge transfers the output of one layer to the next one. Also, the output of a layer is a tensor, a generalization of matrices in a higher dimensional space.

Note that we assume the neural network is already partitioned, and each segment is a layer. Many algorithms can be used to partition the model graph, such as the algorithm proposed in [12]. Thus, our purpose is to place the layers on computation resources to optimize the objective function.

Computation resources consist of m edge servers and a user device. A device can run one or more layers of the model and transfer the results to another device, running the next layer of the model. However, these devices have memory limitations and a maximum number of operations per second. Furthermore, due to physical distance, transmission cost is applied between devices. Therefore, we must optimize the objective function and satisfy the constraints.

Not that, this model doesn't hold for all neural networks, as there could be multiple connections between layers. For instance, Resnet models use skip connection to deepen the network, therefore, cannot be represented by our system model.

Our objective is to minimize total inference time that consists of computation and transmission delay. First let's define input parameters of our problem:

$$\mathbf{C} = [c]_{n(m+1)} : \text{computation matrix (operations)} \qquad (1)$$

$$\mathbf{R} = [r]_{(n-1)(m+1)} : \text{transmission matrix} \, (s) \qquad (2)$$

$$\boldsymbol{m_l} = [m_l]_n : \text{layer memory requirement (unit)} \qquad (3)$$

$$\boldsymbol{m_d} = [m_d]_{m+1} : \text{device available memory (unit)} \qquad (4)$$

$$\boldsymbol{\alpha} = [\alpha]_{m+1} : \text{processing power} \, (\frac{\text{operations}}{s}) \qquad (5)$$

$$K : \text{maximum acceptable delay ratio} \qquad (6)$$

c_{ij} element of the computation matrix corresponds to the computation cost of layer i when it is placed on device j. Also, r_{ij} element of transmission matrix denotes the transmission cost of layer i output to device j. Note that we defined the transmission cost for each device to be independent of the layer itself. This might seem restrictive in practice, but we only consider the transmission cost that is caused by physical

distance between computation devices. $\boldsymbol{m_l}$ and $\boldsymbol{m_d}$ are layer and memory requirement parameters respspectively. $\boldsymbol{\alpha}$ denotes maximum computation power of each device in number of operations per second. If the total number of layer operations in a layer is greater than its device operations, then the delay will increase to infinity. Therefore, we defined K as the maximum rate of acceptable delay to limit this delay.

Define optimization variable (X) as:

$$X = [x]_{n(m+1)}, x_{ij} = \begin{cases} 1 \,, \text{layer i is placed in device j} \\ 0 \,, \text{otherwise} \end{cases} \quad (7)$$

X is a binary variable that makes the optimization problem a non-convex problem. Generally, these problems are NP-hard and challenging to solve.

The objective function can be expressed as:

$$f_0(X) = \sum_{j=0}^{m} \sum_{i=0}^{n-1} x_{ij} \frac{c_{ij}}{\alpha_j} + \frac{1}{2} \sum_{i=0}^{n-2} \mathbf{card}(\boldsymbol{x}_{i+1} - \boldsymbol{x}_i) \boldsymbol{r}_i^T \boldsymbol{x}_{i+1} \quad (8)$$

Note that in the objective function, x_i and r_i denote the row i of matrix $\boldsymbol{X}$ and $\boldsymbol{R}$ respectively. First term shows the inference delay of each layer in its device. Second term is responsible for the transmission delay from one device to another. If layer i and layer $i+1$ are both in the same device, then no transmission cost is applied (**card** will be zero). On the other hand, if two consecutive layers are in two different devices, then a transmission cost will be considered. In this case, **card** will be equal to 2 and $\boldsymbol{r}_i^T \boldsymbol{x}_{i+1}$ is the transmission cost of layer i to layer $i+1$. We also assume that there is no transmission cost between output layer and final user. This is the reason of iterating i from 0 to $n-2$ not $n-1$.

The objective function is a non-convex as it contains cardinality operation and binary variables. Later we will satisfy these conditions and convert the objective function to a proper form.

The objective function must be minimized while satisfying the constraints. Next, each constraint is mentioned and discussed in detail. First constraint is:

$$\sum_{i=0}^{n-1} c_{ij} x_{ij} \leq K\alpha_j \,, j = 0, \cdots, m \quad (9)$$

This constraint is responsible for limiting the maximum delay of inference on each device. Also, each layer must be assigned to only one device. Thus,

$$\sum_{j=0}^{m} x_{ij} = 1 \,, i = 0, \cdots, n-1 \quad (10)$$

Finally, device memory limitations must be satisfied. Therefore,

$$\sum_{i=0}^{n-1} m_{li} x_{ij} \leq m_{dj}, j = 0, \cdots, m \quad (11)$$

By combining equation 8 to 11, complete form of our problem can be expressed as:

$$minimize \sum_{j=0}^{m} \sum_{i=0}^{n-1} x_{ij} \frac{c_{ij}}{\alpha_j} + \frac{1}{2} \sum_{i=0}^{n-2} \mathbf{card}(\boldsymbol{x}_{i+1} - \boldsymbol{x}_i) \boldsymbol{r}_i^T \boldsymbol{x}_{i+1} \quad (12)$$

$$s.t. \quad \sum_{i=0}^{n-1} c_{ij} x_{ij} \leq K\alpha_j \,, j \in [0, m]$$

$$\sum_{j=0}^{m} x_{ij} = 1 \,, i \in [0, n-1]$$

$$\sum_{i=0}^{n-1} m_{li} x_{ij} \leq m_{dj}, j \in [0, m]$$

$$x_{ij} \in \{0, 1\} \,, i \in [0, n-1]; \, j \in [0, m]$$

C. Approximation and Conversion

In this section, problem 12 will be approximated and converted to a simpler form, so that it can be solved by general solvers. First of all, we approximate **card** function with L1 norm. Therefore, $\mathbf{card}(\boldsymbol{x}_{i+1} - \boldsymbol{x}_i)$ can be replaced with $\sum_{j=0}^{m} |x_{(i+1)j} - x_{ij}|$ function. Also, this term can be replaced with another binary variable and adding two sets of constraints to the original problem. The main idea is to define a new binary variable y_{ij} to replace $|x_{(i+1)j} - x_{ij}|$ while it satisfies $x_{(i+1)j} - x_{ij} \leq y_{ij}$ and $x_{(i+1)j} - x_{ij} \geq -y_{ij}$. Therefore, the approximated form of optimization problem becomes:

$$minimize \sum_{j=0}^{m} \sum_{i=0}^{n-1} x_{ij} \frac{c_{ij}}{\alpha_j} + \frac{1}{2} \sum_{i=0}^{n-2} \sum_{j=0}^{m} y_{ij} r_{ij} x_{(i+1)j} \quad (13)$$

$$s.t. \quad \sum_{i=0}^{n-1} c_{ij} x_{ij} \leq K\alpha_j \,, j \in [0, m]$$

$$\sum_{j=0}^{m} x_{ij} = 1 \,, i \in [0, n-1]$$

$$\sum_{i=0}^{n-1} m_{li} x_{ij} \leq m_{dj}, j \in [0, m]$$

$$x_{(i+1)j} - x_{ij} \leq y_{ij} \,, i \in [0, n-2]; \, j \in [0, m]$$

$$x_{(i+1)j} - x_{ij} \geq -y_{ij} \,, i \in [0, n-2]; \, j \in [0, m]$$

$$x_{ij} \in \{0, 1\} \,, i \in [0, n-1]; \, j \in [0, m]$$

$$y_{ij} \in \{0, 1\} \,, i \in [0, n-2]; \, j \in [0, m]$$

The approximated problem is clearly a quadratic integer (binary) programming problem. Quadratic form can be expressed as $z^T Q z$ which Q is a symmetric matrix with all zero on its main diagonal. Due to the non-positive semi-definite nature of this matrix, we cannot use a more general solver. Furthermore, most solvers can deal with mixed-integer linear programming problems but fail to solve an integer problem with the quadratic form. Hence, we simplify this problem further by converting it to a binary (integer) programming problem, which can be solved by various optimization techniques.

The main idea is to introduce more binary variables that replace the $y_{ij} x_{(i+1)j}$ product. The product of two binary

variables is also a binary variable. Therfore, by defining $w_{ij} = x_{(i+1)j}y_{ij}, i = 0, \cdots, n-2, j = 0, \cdots, m$, objective function can be converted to a linear form. Also two set of constraints are added to the approximated problem. If $x_{(i+1)j} = 0$, then $w_{ij} = 0$ too. This is a forcing constraint which is a linear constraint. The same relationships hold for y_{ij}, too. Also, if both $x_{(i+1)j}$ and y_{ij} equal to 1, then $w_{ij} = 1$ too. Finally, by combining these modifications, our optimization problem can be expressed as:

$$minimize \sum_{j=0}^{m}\sum_{i=0}^{n-1} x_{ij}\frac{c_{ij}}{\alpha_j} + \frac{1}{2}\sum_{j=0}^{m}\sum_{i=0}^{n-2} w_{ij}r_{ij} \quad (14)$$

$$s.t. \quad \sum_{i=0}^{n-1} c_{ij}x_{ij} \le K\alpha_j, j \in [0, m]$$

$$\sum_{j=0}^{m} x_{ij} = 1, i \in [0, n-1]$$

$$\sum_{i=0}^{n-1} m_{li}x_{ij} \le m_{dj}, j \in [0, m]$$

$$x_{(i+1)j} - x_{ij} \le y_{ij}, i \in [0, n-2]; j \in [0, m]$$

$$x_{(i+1)j} - x_{ij} \ge -y_{ij}, i \in [0, n-2]; j \in [0, m]$$

$$z_{ij} \le y_{ij}, i \in [0, n-2]; j \in [0, m]$$

$$z_{ij} \le x_{(i+1)j}, i \in [0, n-2]; j \in [0, m]$$

$$z_{ij} \ge x_{(i+1)j} + y_{ij} - 1, i \in [0, n-2]; j \in [0, m]$$

$$x_{ij} \in \{0, 1\}, i \in [0, n-1]; j \in [0, m]$$

$$y_{ij}, w_{ij} \in \{0, 1\}, i \in [0, n-2]; j \in [0, m]$$

We ended up having a binary linear programming problem that most solvers can solve. This conversion involves a trade-off between increasing the optimization space's dimension and simplifying the problem. We increased the number of variables with the cost of solving the problem easily and having a simpler form.

IV. SIMULATION

To solve this problem and simulate it, problem 14 is represented in matrix and vector format. The detail of this representation is not discussed in this section, but you can refer to simulation code for more information. Equivalent form in matrix format is,

$$minimize \; \boldsymbol{p}^T\boldsymbol{z} + \boldsymbol{q}^T\boldsymbol{w} \quad (15)$$

$$s.t. \; \boldsymbol{S_1}\boldsymbol{z} \le 0, \quad -\boldsymbol{S_2}\boldsymbol{z} \le 0$$

$$\boldsymbol{L}\boldsymbol{z} \le \boldsymbol{h}$$

$$\boldsymbol{E}\boldsymbol{z} = \boldsymbol{1}_n$$

$$\boldsymbol{M}\boldsymbol{z} \le \boldsymbol{M_d}$$

$$\boldsymbol{U}\boldsymbol{z} + \boldsymbol{w} \le 0, \boldsymbol{V}\boldsymbol{z} + \boldsymbol{w} \le 0$$

$$\boldsymbol{G}\boldsymbol{z} - \boldsymbol{w} \le \boldsymbol{1}_{(m+1)(n-1)}$$

$$\boldsymbol{z} \in \{0, 1\}^{(m+1)(2n-1)}, \boldsymbol{w} \in \{0, 1\}^{(m+1)(n-1)}$$

In this problem, $\boldsymbol{z}$ and $\boldsymbol{w}$ are optimization variables. Other parameters are derived from problem parameters and equation 14.

We simulated this problem with the following input parameters:

$$m = 2, n = 3, K = 2, \boldsymbol{C} = \begin{bmatrix} 100 & 200 & 200 \\ 100 & 200 & 200 \\ 100 & 200 & 200 \end{bmatrix} \quad (16)$$

$$\boldsymbol{R} = \begin{bmatrix} 10 & 20 & 30 \\ 10 & 20 & 30 \end{bmatrix}, \boldsymbol{m_l} = \begin{bmatrix} 100 & 100 & 300 \end{bmatrix},$$

$$\boldsymbol{m_d} = \begin{bmatrix} 500 & 500 & 500 \end{bmatrix}, \boldsymbol{\alpha} = \begin{bmatrix} 50 & 60 & 100 \end{bmatrix}$$

We choose a simple problem to show the concept of our approach. We used cvxpy and MOSEK to solve this problem. MOSEK is a commersial optimization tool that can handle large scale problems efficiently. The simulation code is sent along with this report. Also, whole content of project can be accesed thorugh this github repository. To run the simulation. you need cvxpy, numpy and MOSEK license.

It's obvious that all 3 layers must be placed on last device (edge server 2) as it eliminates the transmission cost and can handle all layers simultaneously. The total delay in this case is delay $= \frac{200}{100} + \frac{200}{100} + \frac{200}{100} = 6$ and is optimal. When simulation is run, the following result is achieved:


```
Status: optimal
Optimal value 6.0
Optimal variable:
[[0. 0. 0.]
 [0. 0. 0.]
 [1. 1. 1.]]
```

Fig. 2. Simulation result

Which proves our observation. Note that our main objective in this project was to formulate this problem in a simple form not simulation. However, we run the simulation to show the correctness of our formulation.

V. CONCLUSION

In this project, we investigated a DNN model placement problem with an optimization approach. DNN models are at the heart of edge intelligence and running these models efficiently is an important problem. We formulated the problem as a linear binary programming problem that can be solved with various optimization techniques, such as branch and bound. Also, a simulation of a simple scenario was done with MOSEK, and the results were discussed. Our contributions can be summarized as follows:

1) Optimization approaches were applied to an industrial problem
2) Defining a hyperparameter to control maximum inference time of the model in total (K)
3) Approximation of the original problem and converting it to a linear form
4) Simulation of the problem with MOSEK

Some of the assumptions in this project can be relaxed in future works. DNN model can be generalized to include more sophisticated architectures. Transmission cost between devices must also be a function of the current layer. More simulations must be done and timing results must be compared.

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