Computer Architecture

Assignment - 2

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Question 1

Suppose that a task makes extensive use of floating-point operations, with 40% of the time consumed by floating-point operations. With a new hardware design, the floating-point module is sped up by a factor of K. Then what is the overall speedup?

Solution

We know,

$$T_{original} = T_p + T_{1-p}$$

$$T_{new} = rac{T_p}{N} + T_{1-p}$$

$$speedup(new, original) = rac{T_{original}}{T_{new}}$$

$$rac{T_p}{p} = rac{T_{1-p}}{1-p} = T_{original}$$

Given,

$$p = 0.4$$

$$N = K$$

Plugging values,

$$speedup = rac{T_p + T_{1-p}}{rac{T_p}{N} + T_{1-p}}$$
 $= rac{p * T_{original} + (1-p)T_{original}}{rac{p * T_{original}}{N} + (1-p)T_{original}}$
 $= rac{1}{rac{p}{N} + (1-p)}$
 $= rac{1}{rac{0.4}{K} + (1-0.4)}$
 $= rac{K}{0.4 + 0.6 * K}$

Question 2

Assume that a benchmark program executes in 480 seconds on a reference machine A. The same program executes on systems B, C, and D in 360, 540, and 210 seconds, respectively. Show the speedup of each of the three systems under test relative to A.

Solution

We know,

$$SpeedUp(A,B) = \frac{Executiontime(A)}{Executiontime(B)}$$

Execution Time
$$(A) = 480s$$

Plugging in values we have,

System	Execution time	SpeedUp Calculation	SpeedUp
В	360s	480/360	1.33
С	540s	480/540	0.88
D	210s	480/210	2.28

The table shows the required results.

Question 3

Consider a system that has a single bottleneck which occupies 20% of the total execution time. Suppose we add 4 more processors to the system. What would be the speedup achieved?

Solution

Given, 20% is bottleneck, therefore this is the serial part and can't be parralelized.

$$1 - p = 0.2$$
$$N = 4$$

Solving, p=0.8, using the equation derived in Question 1,

$$speedup = rac{1}{rac{p}{N} + (1-p)} = rac{1}{rac{0.8}{4} + (0.2)} = extbf{2.5}$$

Question 4

A system is composed of 4 components:

- a). The performance of 5% of the system can be doubled. We will call this part component 1
- b). The performance of 20% of the system can be improved by 80%. We will call this part component 2
- c). The performance of 45% of the system can be improved by 50%. We will call this part component 3
- d). The performance of the remaining part of the system cannot be improved. We will call this part component 4.

Which component is most worthy to work on to get the maximum overall improvement?

Solution

We can only improve three components: Component 1, 2 and 3. We compute speedup for each of the cases and report component with highest speedup most worky to work on.

We know,
$$speedup = rac{1}{rac{p}{N} + (1-p)}$$

Case	Component	р	N	speedup calculation	speedup
I	1	0.05	2	$\frac{1}{\frac{0.05}{2} + (0.95)}$	1.02
II	2	0.2	1.8	$\frac{1}{\frac{0.2}{1.8} + (0.8)}$	1.09
III	3	0.45	1.5	$\frac{1}{\frac{0.45}{1.5} + (0.55)}$	1.17

We can see, speedup is maximum for case III. So, it is most worthy to work on **component 3**.

Question 5

Assume a machine is enhanced, making all floating-point instructions 7 times faster. Originally, these floating-point instructions accounted for 65% of the total execution

time.

- a). What will be the overall speedup of the machine after this enhancement? Additionally, calculate the percentage increase in speedup due to this change.
- b). If the execution time of some benchmark program before the floating-point enhancement is 29 seconds, what will the speedup be if only two-third of the 29 seconds is spent executing floating-point instructions.

Solution

(a) We know,

$$speedup = rac{1}{rac{p}{N} + (1-p)}$$

Given,

$$p = 0.65$$

$$N = 7$$

Plugging values, we have,

$$SpeedUp = \frac{1}{\frac{0.65}{7} + (0.35)} =$$
2.26

Assuming percentage increase in speedup refers to percentage increase in performance,

$$Performance_{new} = 2.26 \times Performance_{original}$$

 $Performance_{new} - Performance_{original} = 1.26 imes Performance_{original}$

$$rac{(Performance_{new} - Performance_{original})}{Performance_{original}} imes 100 = 1.26 imes 100$$

$$Percentage\ Increase = \mathbf{126}\%$$

(b) Given,

$$T_{original}=29s$$

$$T_{new}=rac{1}{7} imesrac{2}{3} imes29+rac{1}{3} imes29=rac{9}{21} imes29s$$

$$speedup = rac{T_{original}}{T_{new}} = rac{29}{rac{9}{21} imes 29} = rac{21}{9} = extbf{2.33}$$

Question 6

When parallelizing an application, the ideal speedup is speeding up by the number of processors. This is limited by two things: percentage of the application that can be parallelized and the cost of communication. Amdahl's law takes into account the former but not the latter.

- a. What is the speedup with N processors if 80% of the application is parallelizable, ignoring the cost of communication?
- b. What is the speedup with 8 processors if, for every processor added, the communication overhead is 0.5% of the original execution time.
- c. What is the speedup with 8 processors if, for every time the number of processors is doubled, the communication overhead increases by 0.5% of the original execution time?
- d. What is the speedup with N processors if, for every time the number of processors is doubled, the communication overhead increases by 0.5% of the original execution time?
- e. Write the general equation that solves this question: What is the number of processors with the highest speedup in an application in which P% of the original execution time is parallelizable, and, for every time the number of processors is doubled, the communication is increased by 0.5% of the original execution time?

Solution

(a) We know,

$$speedup = rac{1}{rac{p}{N} + (1-p)}$$

Given,

$$p = 0.8$$

 $N = N$

Plugging values, we have,

$$SpeedUp = rac{1}{rac{0.8}{N} + (0.2)} = rac{\mathbf{N}}{\mathbf{0.8} + \mathbf{0.2} imes \mathbf{N}}$$

(b) We know,

$$T_{original} = T_p + T_{1-p}$$

$$T_{new} = rac{T_p}{N} + T_{1-p} + N imes 0.005 imes T_{original}$$

$$speedup(new, original) = rac{T_{original}}{T_{new}}$$

$$rac{T_p}{p} = rac{T_{1-p}}{1-p} = T_{original}$$

Given,

$$p = 0.8 \; (assuming \; given \; same \; as \; (a))$$

 $N = 8$

For N=8, we add 7 processors therefore penalty gets added for 7 processors, Plugging values,

$$speedup = rac{T_p + T_{1-p}}{rac{T_p}{N} + T_{1-p} + (N-1) imes 0.005 imes T_{original}}$$
 $= rac{p * T_{original} + (1-p) T_{original}}{rac{p * T_{original}}{N} + (1-p) T_{original} + (N-1) imes 0.005 imes T_{original}}$
 $= rac{1}{rac{p}{N} + (1-p) + (N-1) imes 0.005}$
 $= rac{1}{rac{0.8}{8} + 0.2 + 7 imes 0.005}$
 $= rac{1}{0.335}$
 $= 2.98$

(c) Given,

$$p=0.8 \ (assuming \ given \ same \ as \ (a))$$
 $N=8$

We can note, N=8 implies that number of processors is doubled 3 times. The question statement is ambiguous, we can assume that the communication overhead increases by 0.5 % of the oldest original execution time instead immediate original.

$$T_{original} = T_p + T_{1-p}$$

$$T_{new} = rac{T_p}{8} + T_{1-p} + 3 imes 0.005 imes T_{original}$$
 $speedup = rac{T_p + T_{1-p}}{rac{T_p}{8} + T_{1-p} + 3 imes 0.005 imes T_{original}}$ $= rac{p * T_{original} + (1-p)T_{original}}{rac{p * T_{original}}{8} + (1-p)T_{original} + 3 imes 0.005 imes T_{original}}$ $= rac{1}{rac{p}{8} + (1-p) + 3 imes 0.005}$ $= rac{1}{rac{0.8}{8} + 0.2 + 3 imes 0.005}$ $= rac{1}{0.315}$ $= 3.17$

(d) Given,

$$p=0.8 \ (assuming \ given \ same \ as \ (a))$$
 $N=N$

We can note, N = N implies that number of processors is doubled $\lfloor \log_2(N) \rfloor$ times. The question statement is ambiguous, we can assume that the communication overhead increases by 0.5 % of the oldest original execution time instead immediate original. Also, the overhead only increases upon doubling not addition of non doubling processors.

$$T_{original} = T_p + T_{1-p}$$
 $T_{new} = rac{T_p}{N} + T_{1-p} + \lfloor \log_2(N)
floor imes 0.005 imes T_{original}$ $speedup = rac{T_p + T_{1-p}}{rac{T_p}{N} + T_{1-p} + \lfloor \log_2(N)
floor imes 0.005 imes T_{original}}$ $= rac{p * T_{original} + (1-p) T_{original}}{rac{p * T_{original}}{N} + (1-p) T_{original} + \lfloor \log_2(N)
floor imes 0.005 imes T_{original}}$ $= rac{1}{rac{p}{N} + (1-p) + \lfloor \log_2(N)
floor imes 0.005}$ $= rac{1}{rac{0.8}{N} + 0.2 + \lfloor \log_2(N)
floor imes 0.005}$

(e) Given parallelizable portion is P %, so fraction of parallizable part is p = P/100. As derived in (d),

$$\mathbf{speedup(p,N)} = \frac{1}{\frac{p}{N} + (1-p) + \lfloor \log_2(N) \rfloor \times 0.005}$$