功率放大器

RFX2401C: 2.4 GHz Zigbee®/ISM 前端模块

Applications

- ZigBee extended range devices
- ZigBee smart power
- Wireless sound and audio systems
- Home and industrial automation
- Wireless sensor networks
- Custom 2.4 GHz radio systems

Features

- 2.4 GHz ZigBee high-power single-chip, single-die RF front-end IC
- Single-ended 50 Ω input and output ports
- Integrated PA with up to +22 dBm output power
- Integrated LNA with 2.5 dB noise figure
- Transmit/receive switch circuitry
- High transmit signal linearity meeting standards for OQPSK modulation
- Low voltage (1.2 V) CMOS control logic
- ESD protection circuitry on all ports
- DC decoupled RF ports
- Full on-chip matching and decoupling circuitry
- Market proven CMOS technology
- Small QFN, 16-pin (3 x 3 x 0.55 mm) package with exposed ground pad (MSL1, 260 °C per JEDEC J-STD-020)

Skyworks Green™ products are compliant with all applicable legislation and are halogen-free. For additional information, refer to *Skyworks Definition of Green™*, document number SQ04-0074.
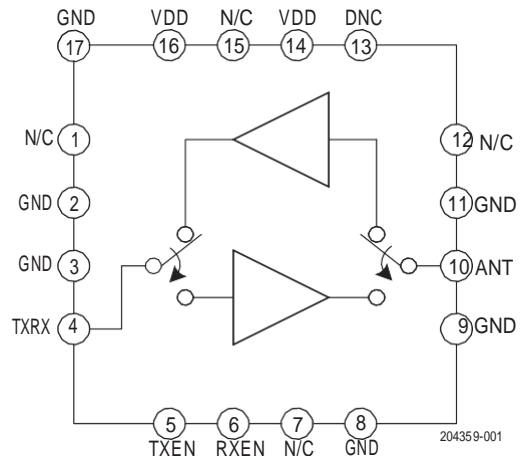

Figure 1. RFX2401C Block Diagram

Description

RFX2401C 是一个完全集成的前端模块, 它包含 IEEE 802.15.4/无线传感器网络以及 2.4 GHz ISM 波段中的任何其他无线系统所需的所有 RF 功能。RFX2401C 体系结构集成了 PA、低噪声、传输和接收开关电路以及相关的匹配网络, 所有这些都在一个软件包中。典型的大功率应用包括家用和工业自动化、智能电源和 RF4CE 等。RFX2401C 的性能优越、灵敏度高、效率低、噪音小、外形小巧、成本低廉, 是需要扩展范围和带宽的应用的完美解决方案。RFX2401C 具有简单、低压 CMOS 控制逻辑, 并有广泛的操作电源电压范围。该设备是在紧凑的, 16 针 3 x 3 毫米四扁无铅 (QFN) 封装。功能框图如图1所示。pin 配置和包如图2所示。信号针脚分配和功能...。
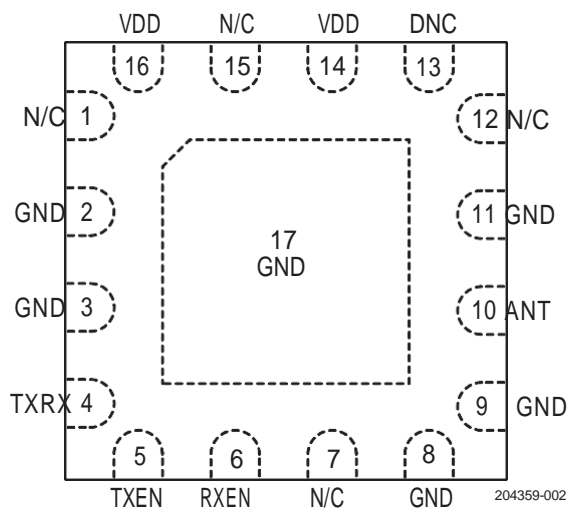
Figure 2. RFX2401C Pinout
(Top View)

Table 1. RFX2401C Signal Descriptions

Pin	Name	Description	Pin	Name	Description
1	N/C	Not connected internally	9	GND	Ground
2	GND	Ground	10	ANT	Connect to 50 Ω antenna (DC shorted to GND)
3	GND	Ground	11	GND	Ground
4	TXRX	RF signal to/from the transceiver (DC shorted to GND)	12	N/C	Not connected internally
5	TXEN	CMOS input to control TX enable	13	DNC	Do not connect
6	RXEN	CMOS input to control RX enable	14	VDD	Alternate supply pin, internally connected to pin 16
7	N/C	Not connected internally	15	N/C	Not connected internally
8	GND	Ground	16	VDD	Voltage supply connection

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Electrical and Mechanical Specifications

The absolute maximum ratings of the RFX2401C are provided in Table 2. The recommended operating conditions are specified in Table 3.

The electrical specifications are provided in Tables 4 and 5. The state of the RFX2401C is determined by the logic provided in Table 6.

Table 2. RFX2401C Absolute Maximum Ratings[1]

Parameter	Conditions	Minimum	Maximum	Units
DC VDD voltage supply		0	4.0	V
DC control pin voltage	Through 1 kΩ resistor	0	3.6	V
DC VDD current consumption	Through VDD pins when TX is "ON"		350	mA
DC control pin current consumption			1	µA
TX RF input power	All operating modes		+5	dBm
ANT RF input power	When RX is "ON"		+5	dBm
Junction temperature			150	°C
Storage ambient temperature	No RF and DC voltages applied Appropriate care required according to JEDEC Standards	-50	+150	°C

[1] Exposure to maximum rating conditions for extended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their nominal value. Exceeding any of the limits listed here may result in permanent damage to the device. All maximum RF input power ratings assume 50 Ω terminal impedance.

ESD HANDLING: Although this device is designed to be as robust as possible, electrostatic discharge (ESD) can damage this device. This device must be protected at all times from ESD when handling or transporting. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD handling precautions should be used at all times.

Table 3. RFX2401C Recommended Operating Conditions[1]

Parameter	Conditions	Minimum	Typical	Maximum	Units
DC VDD voltage supply	All VDD pins	2.0	3.3	3.6	V
Control voltage "high"	Through 1 kΩ resistor	1.2		VDD	V
Control voltage "low"		0		0.3	V
Operating ambient temperature	Note 2	-40		+125	°C
θja			35		°C/W

[1] During production test, devices will be tested at 5 V.

[2] For operation above +85 °C, use the θja as guidance for system design to assure the junction temperature will not exceed the maximum of +150 °C.

Table 4. RFX2401C Electrical Specifications[1] (V$_{DD}$ = 3.3 V, All Unused Ports Terminated with 50 Ω, T$_A$ = 25 °C, Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
Frequency range	f		2.4		2.525	GHz
Transmit Mode						
Saturated output power				+22		dBm
Small-signal gain			21.5	25	26.5	dB
Second harmonic		P$_{OUT}$ = +20 dBm, IEEE 802.15.4 OQPSK modulation signal		−10		dBm/MHz
Third harmonic		P$_{OUT}$ = +20 dBm, IEEE 802.15.4 OQPSK modulation signal		−20		dBm/MHz
Input return loss				−10		dB
Output return loss				−6		dB
Input / output impedance single-ended				50		Ω
TX quiescent current		No RF applied		17		mA
TX high-power current		P$_{OUT}$ = +20 dBm		90		mA
Load VSWR for stability (P$_{OUT}$ = +20 dBm)		All non-harmonically related spurs less than −43 dBm/MHz		6:1		N/A
Load VSWR for ruggedness (P$_{OUT}$ = +20 dBm)		No damage		10:1		N/A
Receive Mode						
Gain				12		dB
Noise figure				2.5		dB
Input return loss				−10		dB
Output return loss				−12		dB
Rf port impedance				50		Ω
Rx quiescent current		No RF applied		8		mA
Input p_{1db}		At ANT pin		−8		dBm

[1] Performance is guaranteed only under the conditions listed in this table.

Table 5. RFX2401C Standby Mode Technical Parameters

Parameter	Symbol	Test Condition	Min	Typical	Max	Units
DC shutdown current					1	µA
TXRX-ANT insertion loss (S21)		Pin < -20 dBm		−50		dB
ANT-TXRX insertion loss (S21)				−50		dB
Return loss (S11)		From TXRX port		−1.5		dB
Transmit-receive switching time				800		nsec
Shutdown and "ON" State switching time				800		nsec

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Table 6. RFX2401C Control Logic[1]

Mode	TXEN	RXEN
TX active	1	x
RX active	0	1
Shutdown	0	0

[1] "1" denotes high voltage state (> 1.2 V)
　"0" denotes low voltage stage (< 0.3 V) at control pins
　"X" denotes do not care: either "1" or "0" can be applied

Application Schematic Board Description

A suggested RFX2401C FEM application schematic diagram is shown in Figure 3.

Circuit Design Considerations

The following design considerations are general in nature and must be followed regardless of final use or configuration:

- Paths to ground should be made as short as possible.

- If the transceiver TXRX port has DC present, use a capacitor to block this voltage from reaching the RFX2401C.

- The ground pad of the RFX2401C has special electrical and thermal grounding requirements. This pad is the main thermal conduit for heat dissipation. Because the circuit board acts as the heat sink, it must shunt as much heat as possible from the device.

  Multiple vias to the grounding layer are required. Use thermal vias to assure efficient heat dissipation.

- Locate the bypass capacitors as close as possible to the ground pad. Use two ground vias.

- The VDD (pin 14) is an optional VDD pin, internally connected to pin 16.

- The N/C pins 1, 7, 12, and 15 may be left open or connected to GND.

- If the antenna circuits have DC present, use a capacitor to block this voltage from reaching the RFX2401C.

NOTE: *A poor connection between the ground pad and ground increases junction temperature (T_J), which reduces the life of the device..*
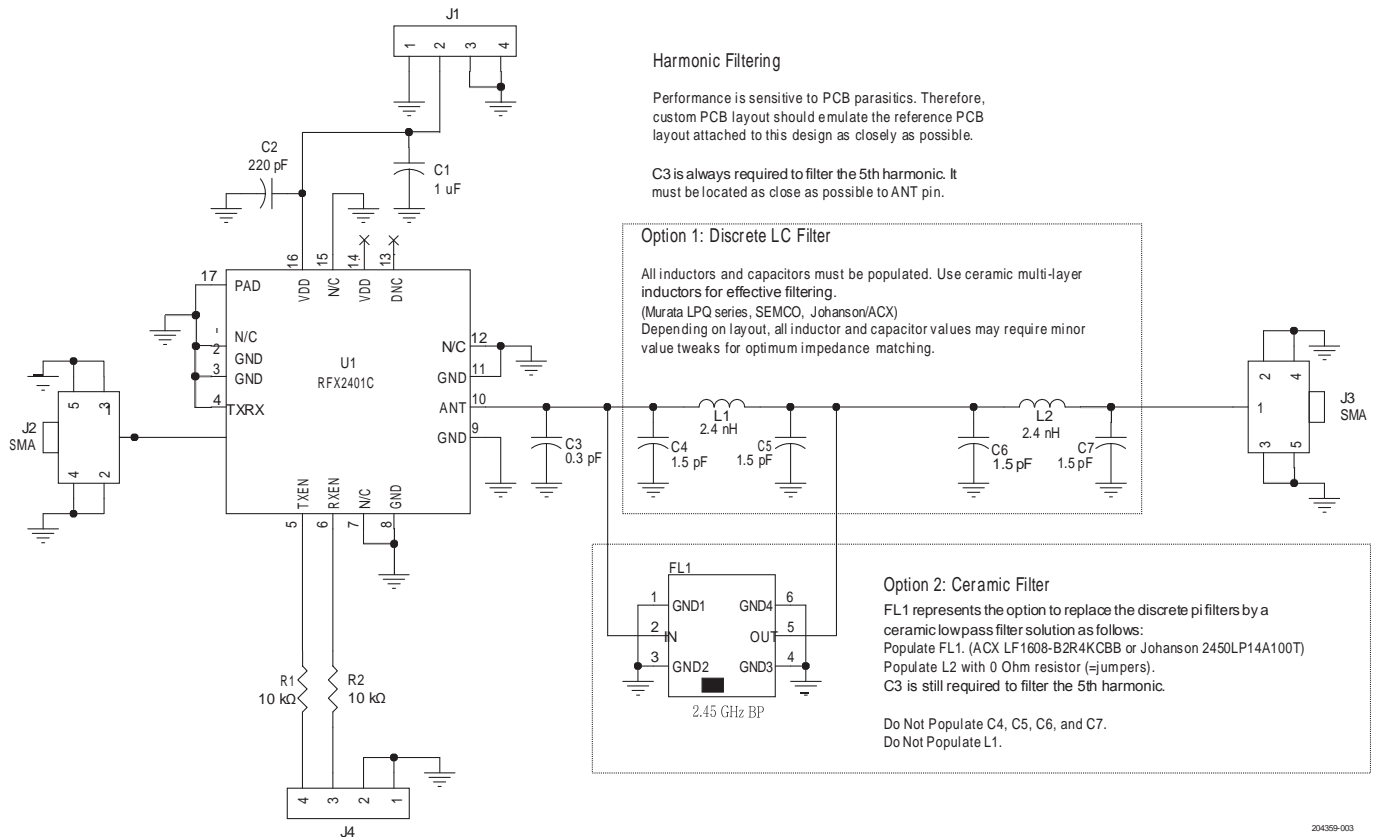
Harmonic Filtering

Performance is sensitive to PCB parasitics. Therefore, custom PCB layout should emulate the reference PCB layout attached to this design as closely as possible.

C3 is always required to filter the 5th harmonic. It must be located as close as possible to ANT pin.

Option 1: Discrete LC Filter

All inductors and capacitors must be populated. Use ceramic multi-layer inductors for effective filtering.
(Murata LPQ series, SEMCO, Johanson/ACX)
Depending on layout, all inductor and capacitor values may require minor value tweaks for optimum impedance matching.

Option 2: Ceramic Filter

FL1 represents the option to replace the discrete pi filters by a ceramic lowpass filter solution as follows:
Populate FL1. (ACX LF1608-B2R4KCBB or Johanson 2450LP14A100T)
Populate L2 with 0 Ohm resistor (=jumpers).
C3 is still required to filter the 5th harmonic.

Do Not Populate C4, C5, C6, and C7.
Do Not Populate L1.

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Figure 3. RFX2401C Application Schematic

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Package Dimensions

The PCB layout footprint for the RFX2401C is shown in Figure 4. Typical part markings are shown in Figure 5. Package dimensions are shown in Figure 6, and tape and reel dimensions are provided in Figure 7.

Package and Handling Information

Instructions on the shipping container label regarding exposure to moisture after the container seal is broken must be followed. Otherwise, problems related to moisture absorption may occur when the part is subjected to high temperature during solder assembly.

The RFX2401C is rated to Moisture Sensitivity Level 1 (MSL1) at 260 °C. It can be used for lead or lead-free soldering. For additional information, refer to the Skyworks Application Note, *Solder Reflow Information*, document number 200164.

Care must be taken when attaching this product, whether it is done manually or in a production solder reflow environment. Production quantities of this product are shipped in a standard tape and reel format.
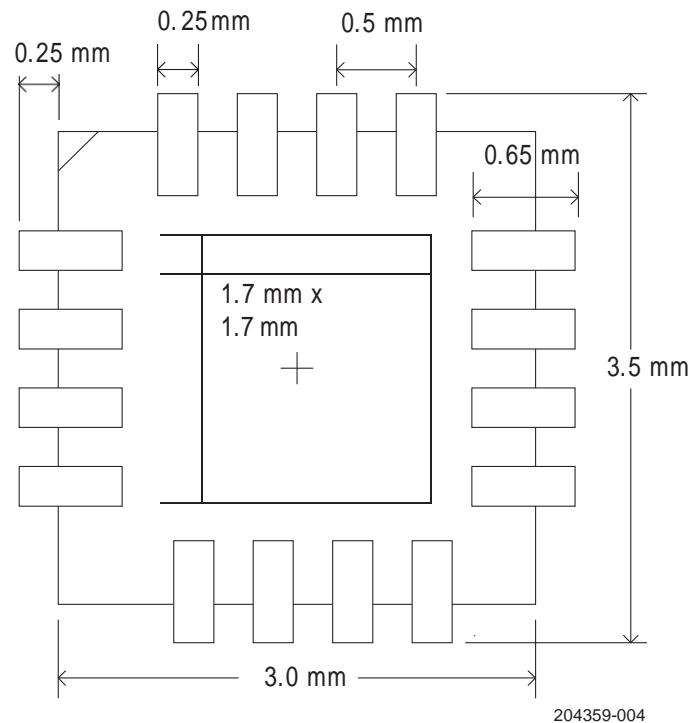
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**Figure 4. RFX2401C PCB Layout Footprint
(Top View)**

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Pin 1 Indicator

2401C — Skyworks Part Number
LLLL.L — Lot Code
UHYYWW — Date Code
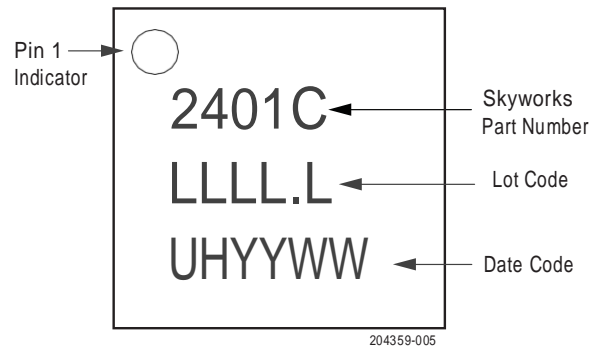
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Figure 5. Typical Part Markings
(Top View)

3.00 ± 0.05

Pin 1 Indicator

3.00 ± 0.05

0.55 ± 0.05

0.00 − 0.05

1.70 ± 0.05

0.25 ± 0.05

0.50 ± 0.05

1G

Pin 1 Indicator

0.40 ± 0.05   1.70 ± 0.05

1

Top View   Side View   Bottom View   204359-006
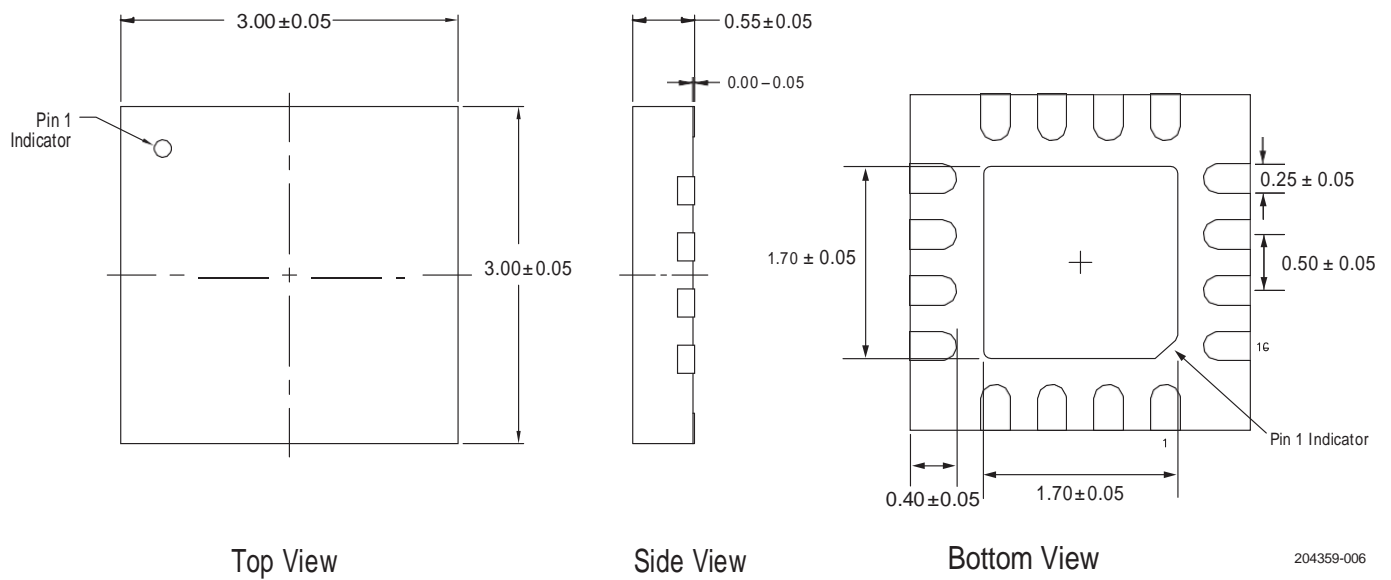
All dimensions are in millimeters.

Figure 6. RFX2401C Package Dimensions

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All measurements are in millimeters.
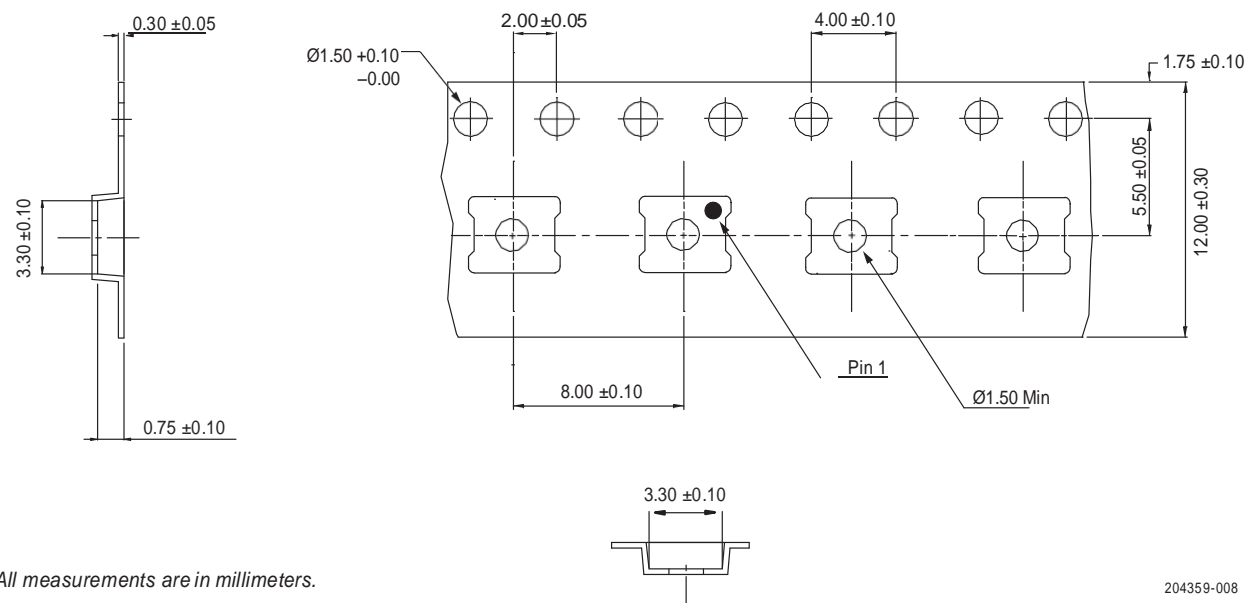
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Figure 8. RFX2401C Tape and Reel Dimensions

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Ordering Information

Model Name	Manufacturing Part Number	Evaluation Board Part Number
RFX2401C: 2.4 GHz Zigbee/ISM Front-End Module	RFX2401C	RFX2401C-EK1

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