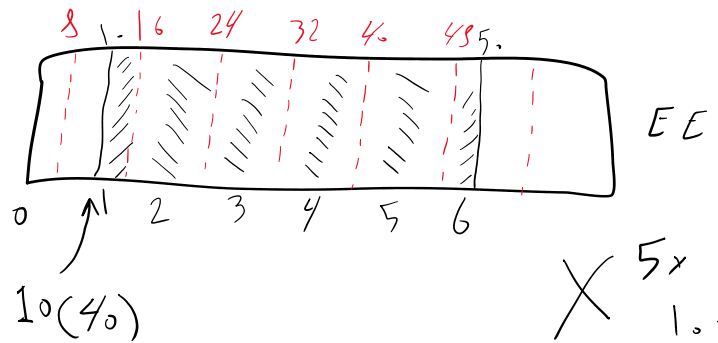


* 01 }
 02 } 8-bit mem Addr
 04 } multiple (C02)
 08 }
 16 }

32 }
 64 } 16-bit mem Addr
 128 }
 256 }
 512 }

1,024 } 16-bit mem Addr
 2,048 } multiple (C512)

Page write → 8, 32, 64, 128



X 5x 8 byte
 1.0ms delay

✓ 6x write
 1.0ms delay