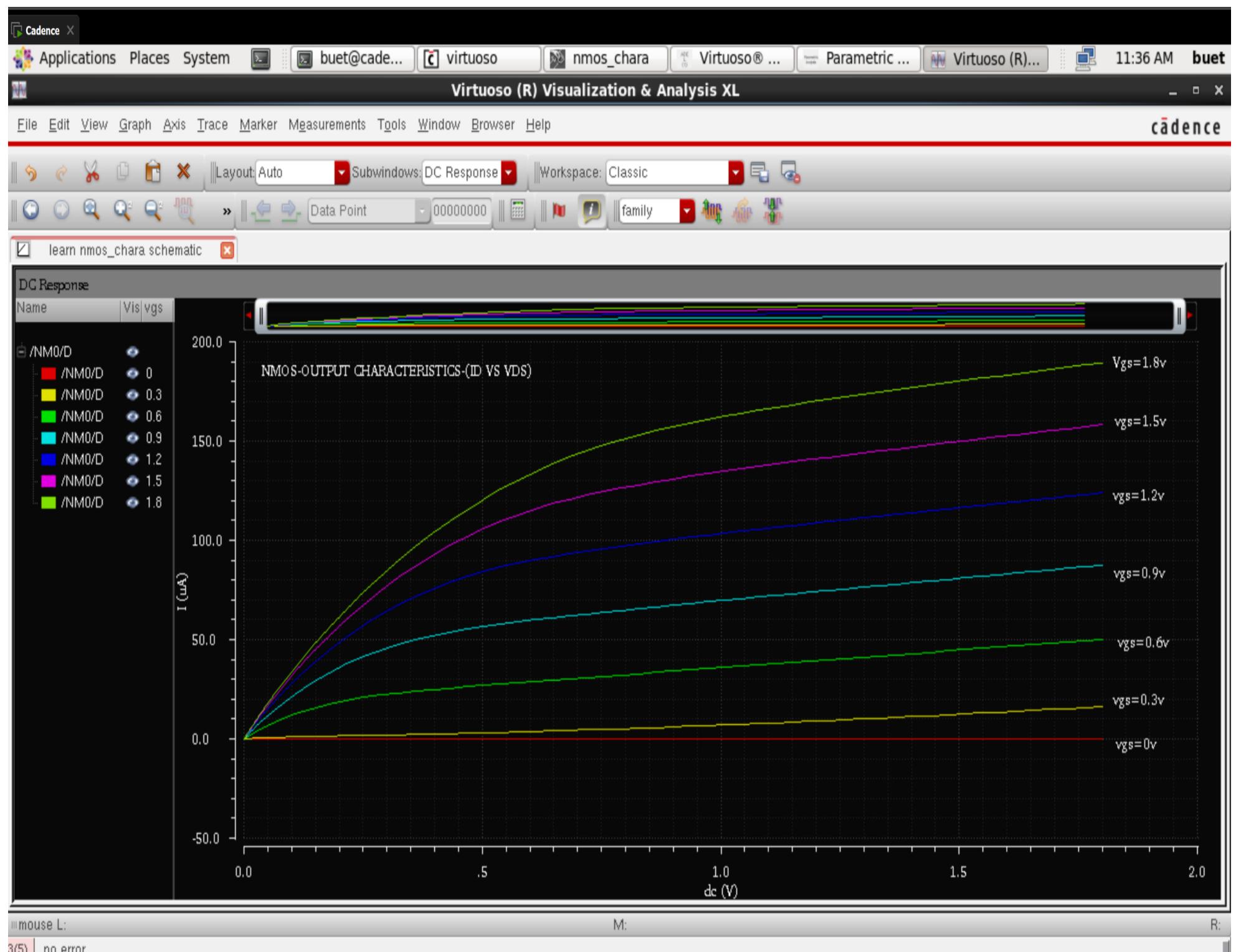
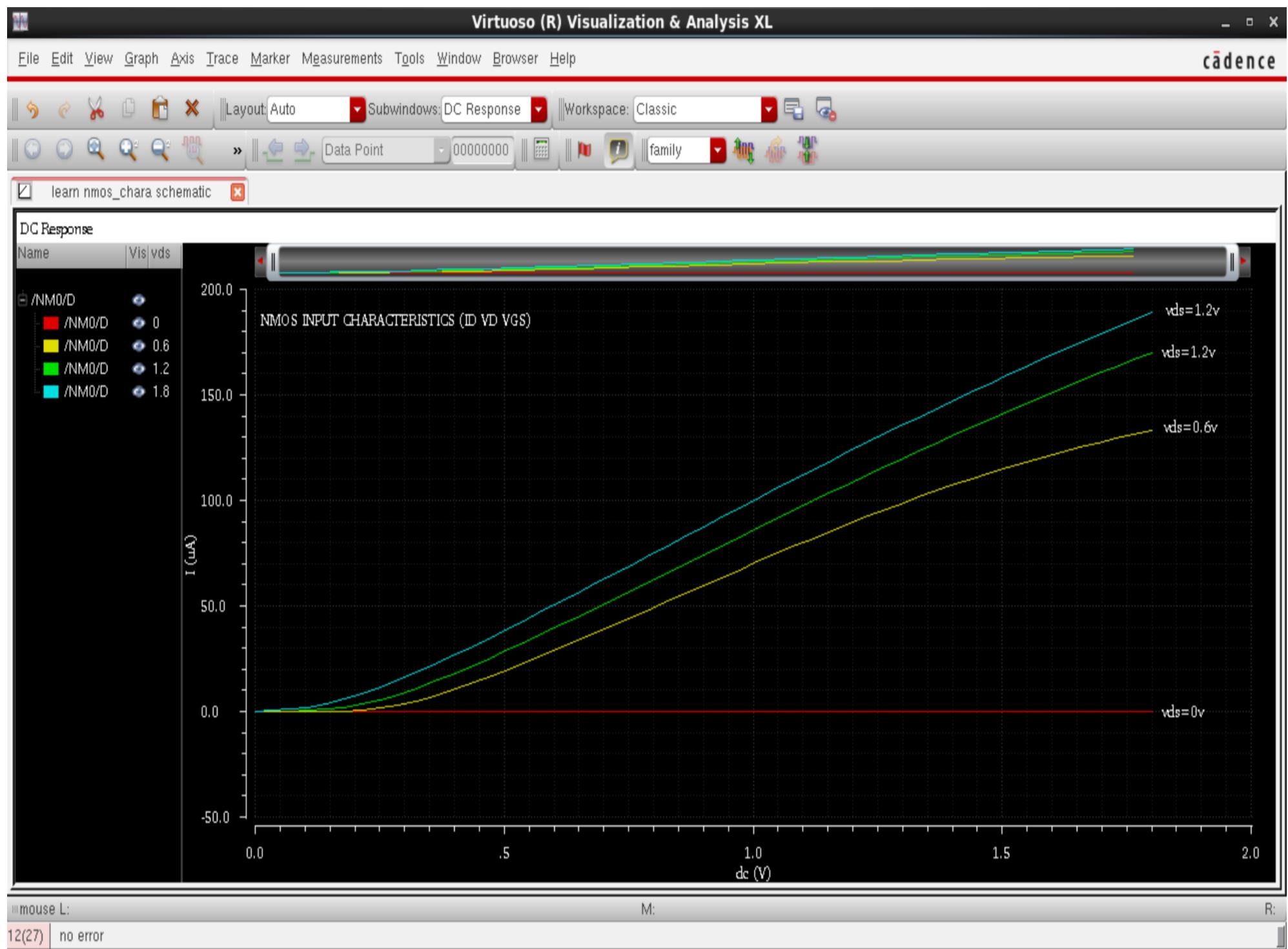
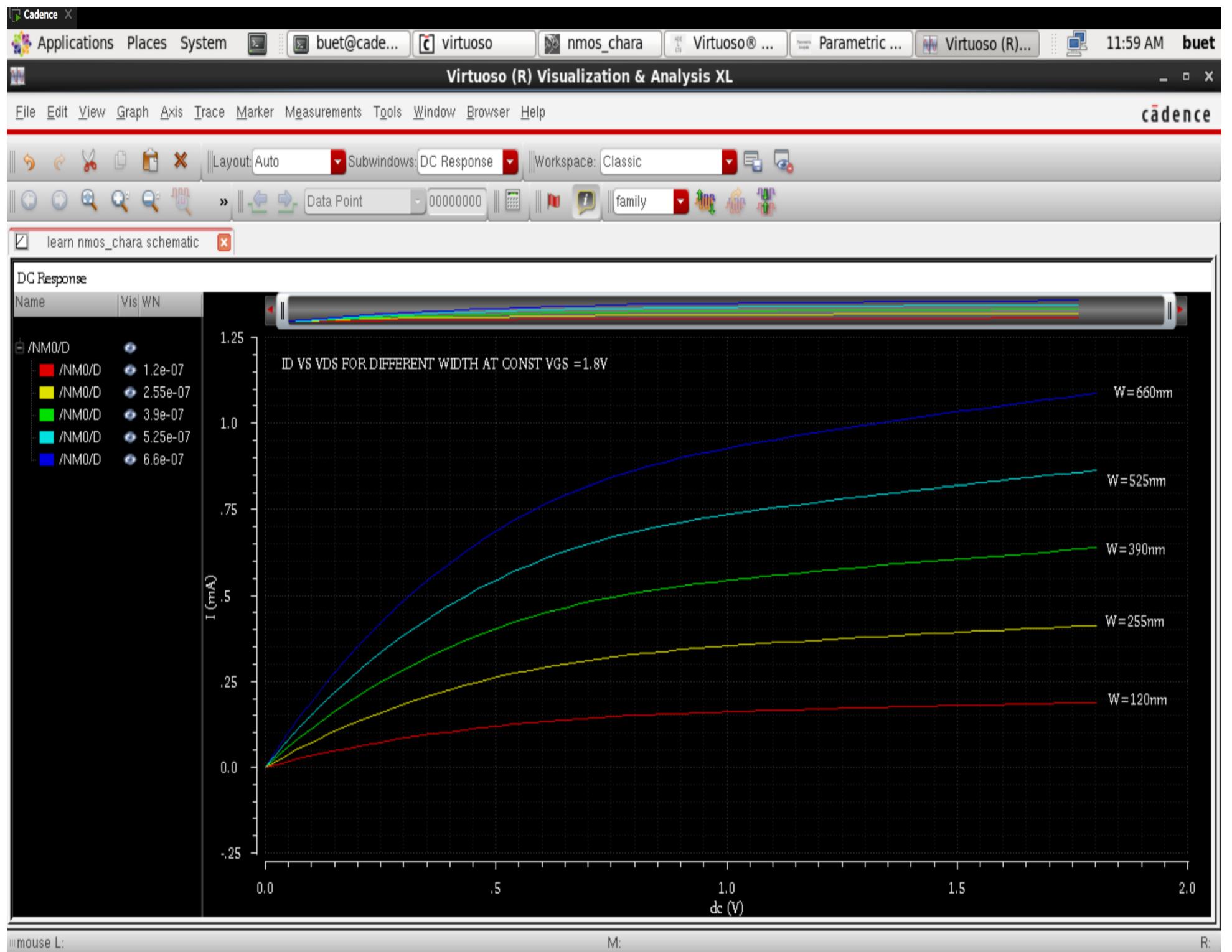


NMOS INPUT AND OUTPT CHARACTERISTICS

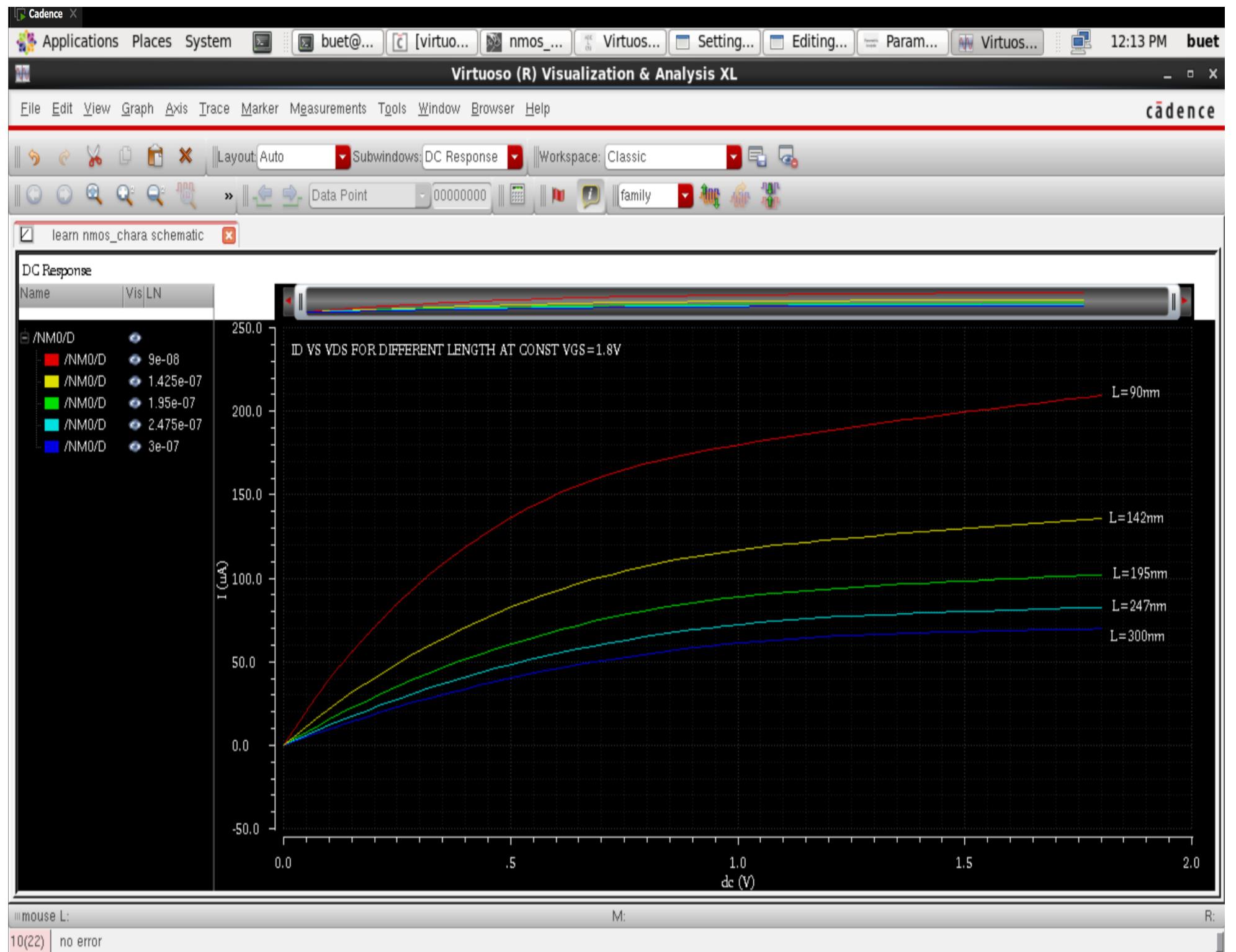


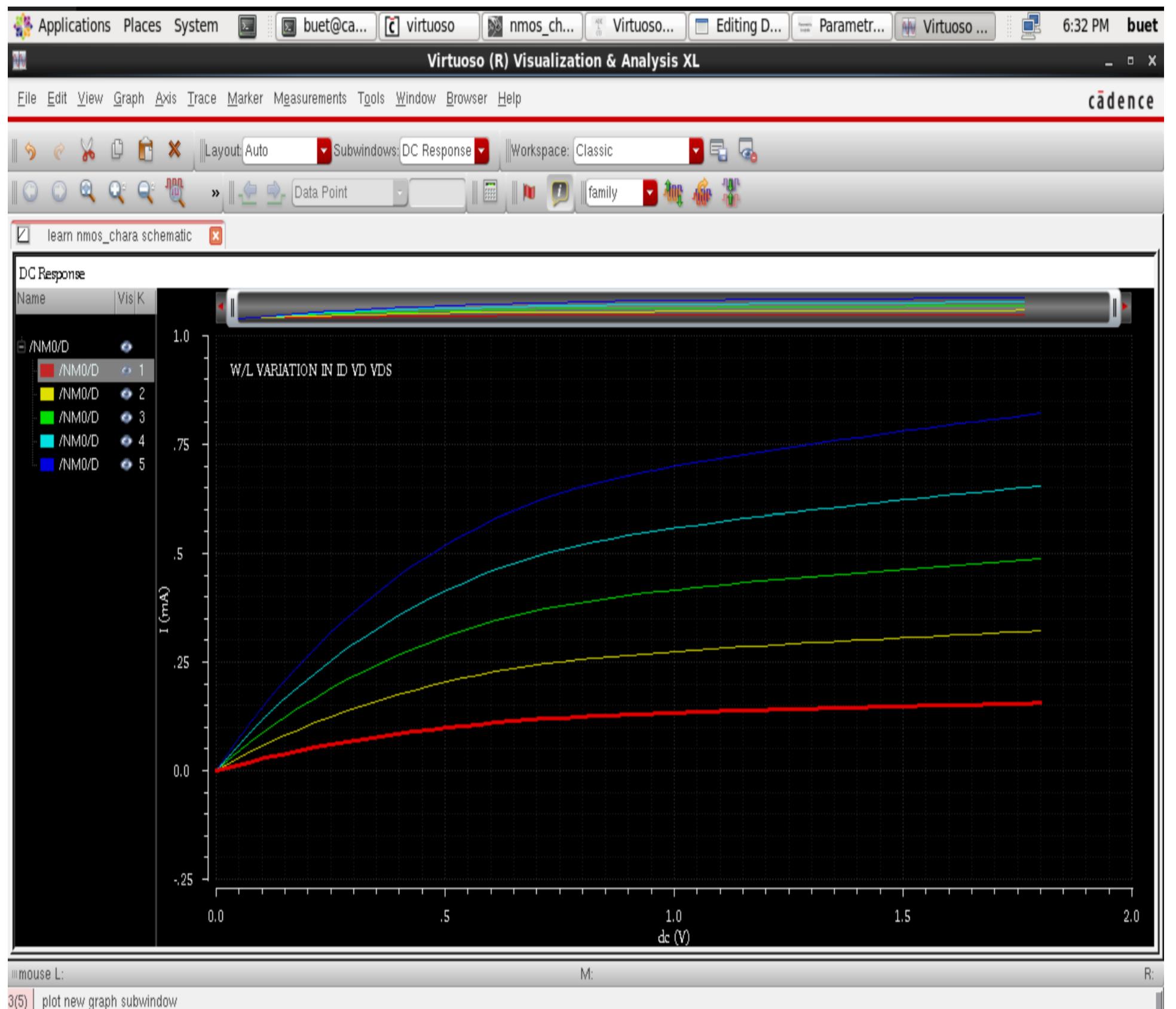


W and L VARIATIONS

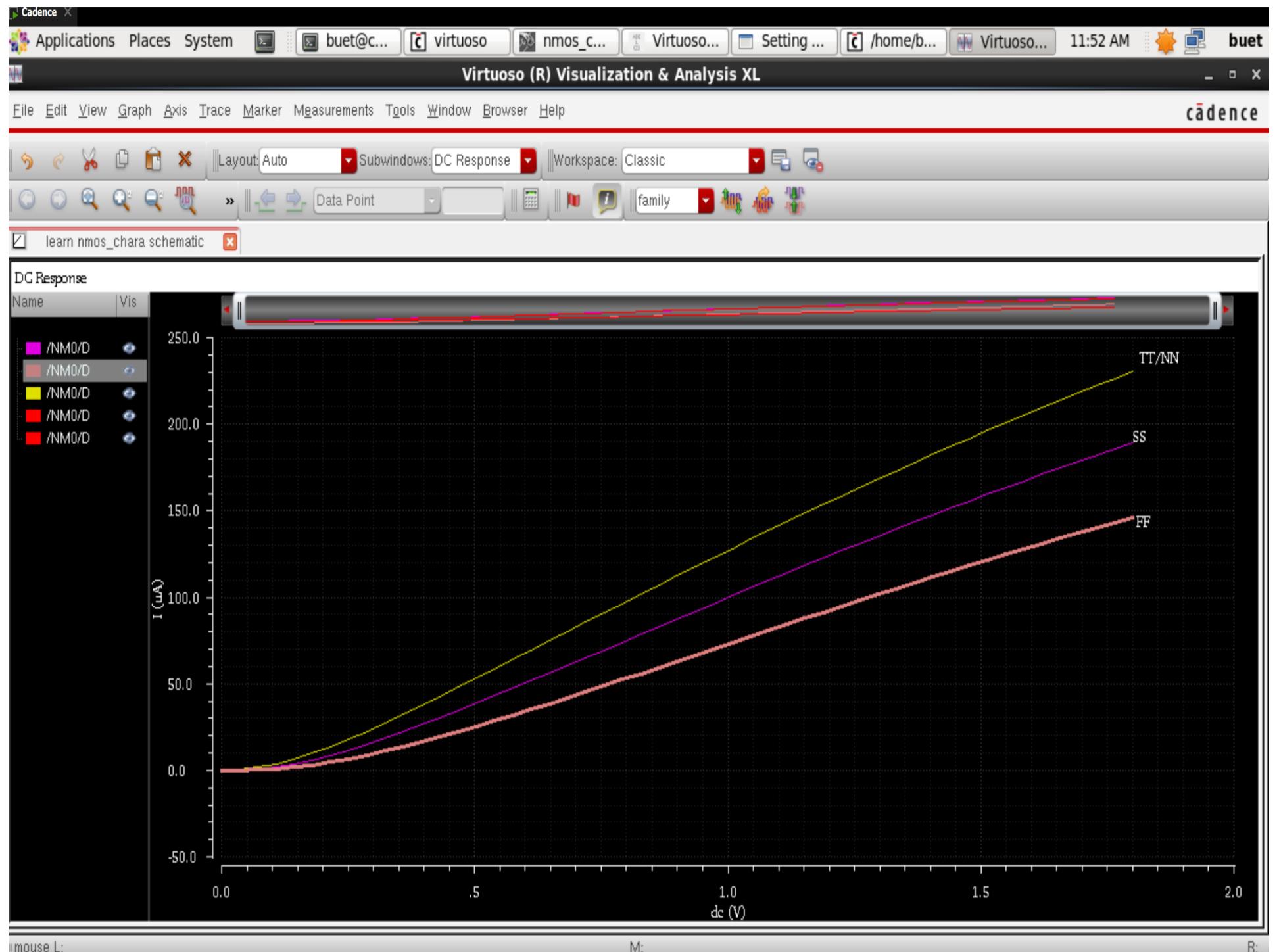


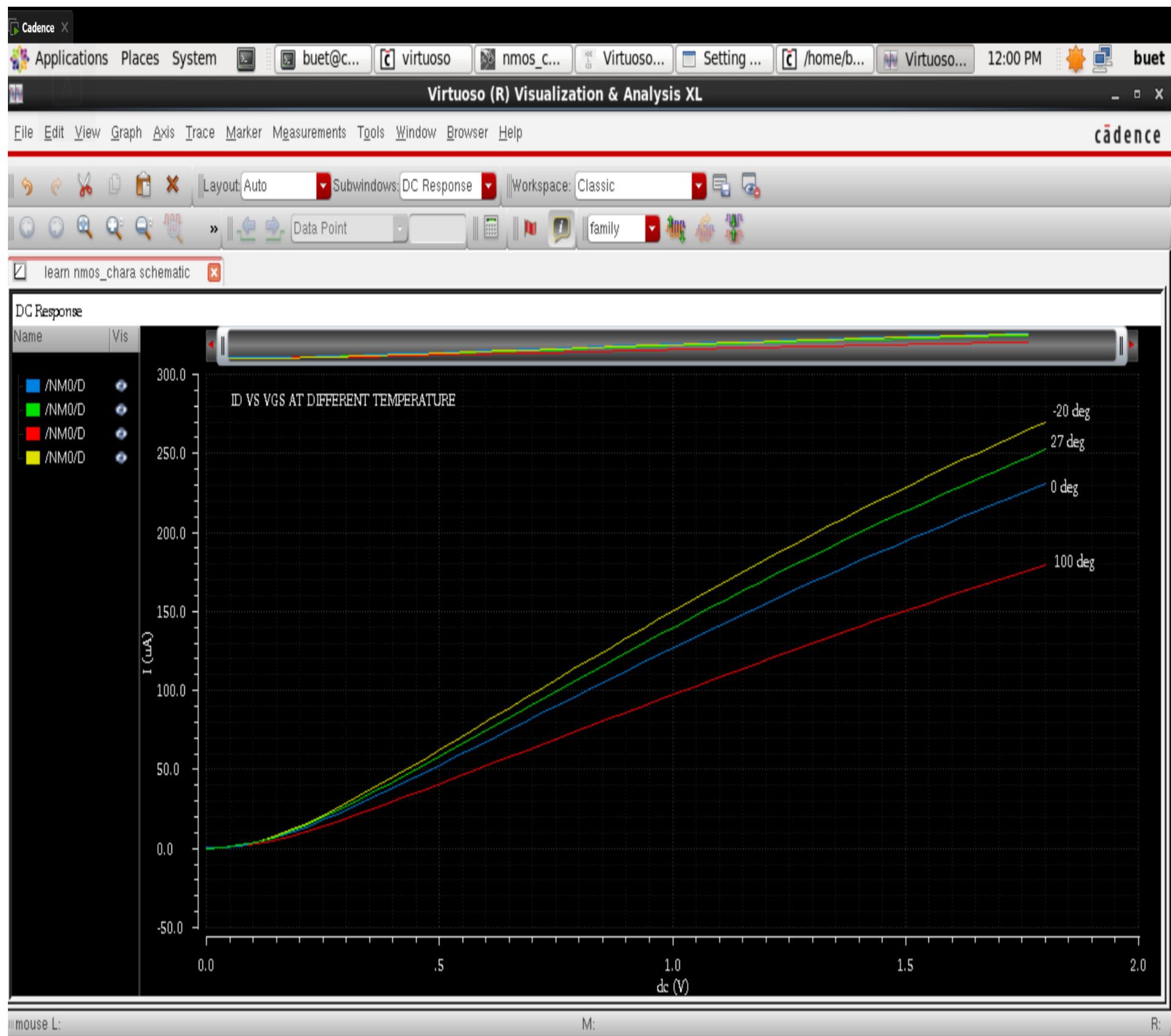
CHANNEL LENGTH VARIATIONS

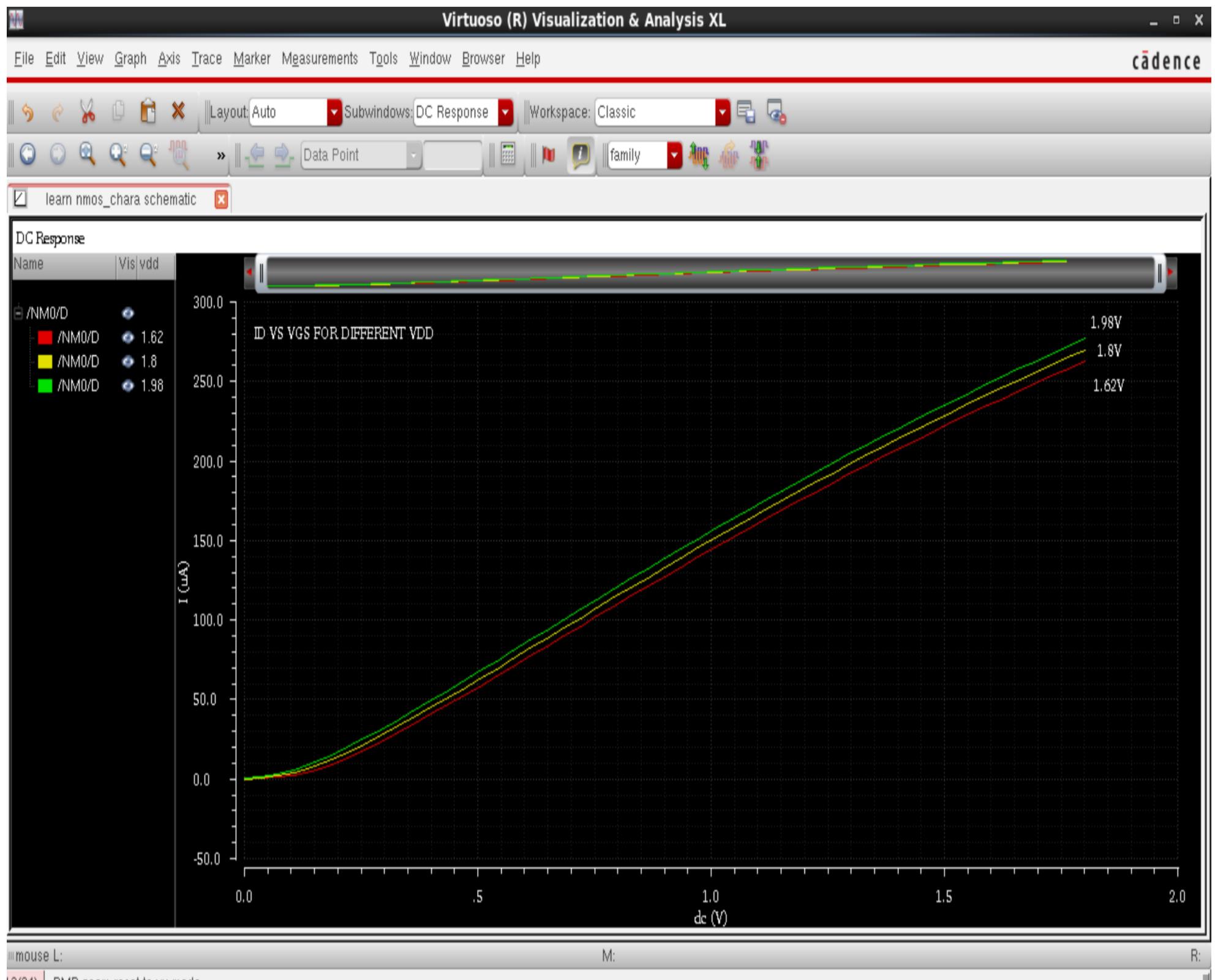


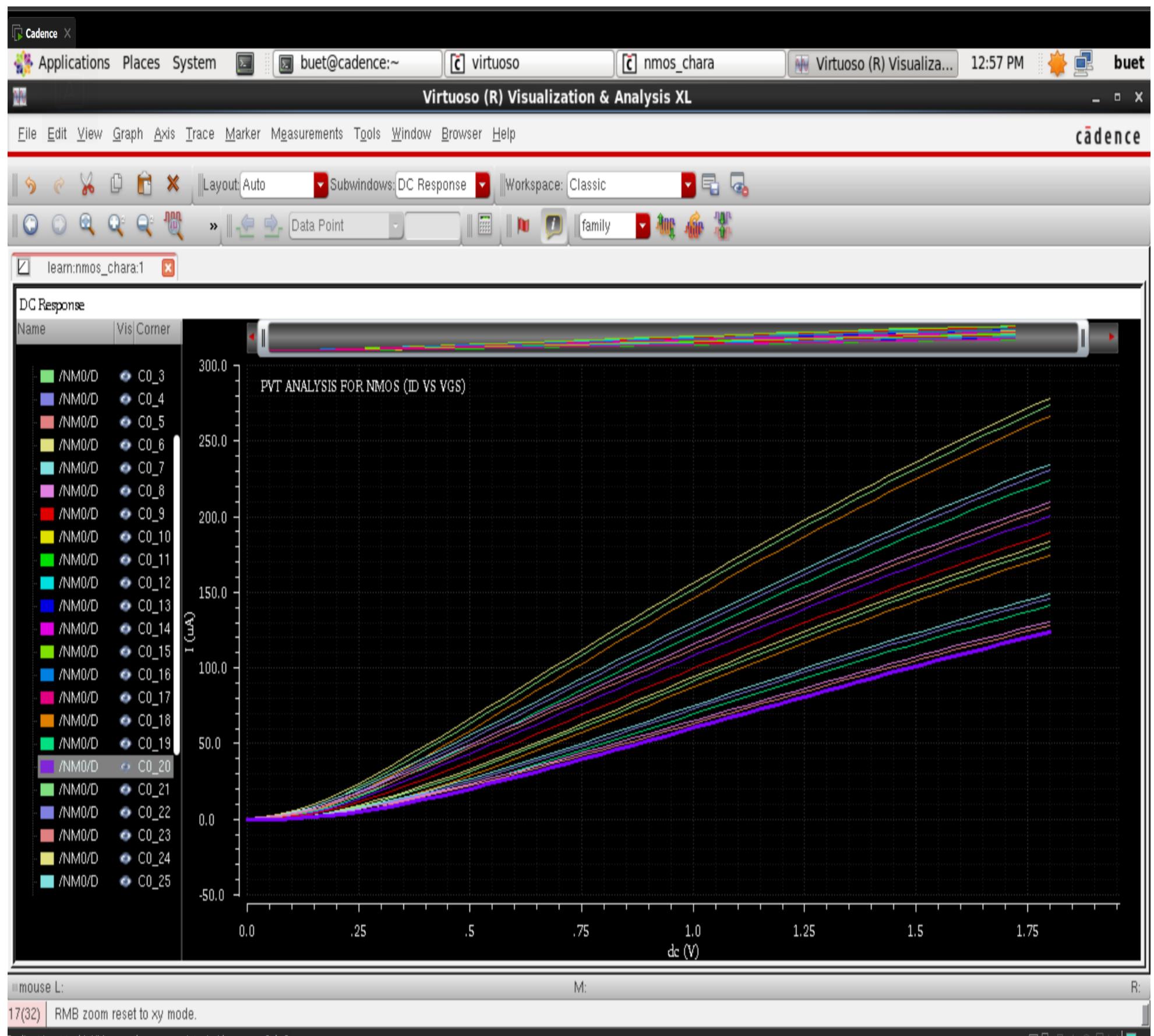


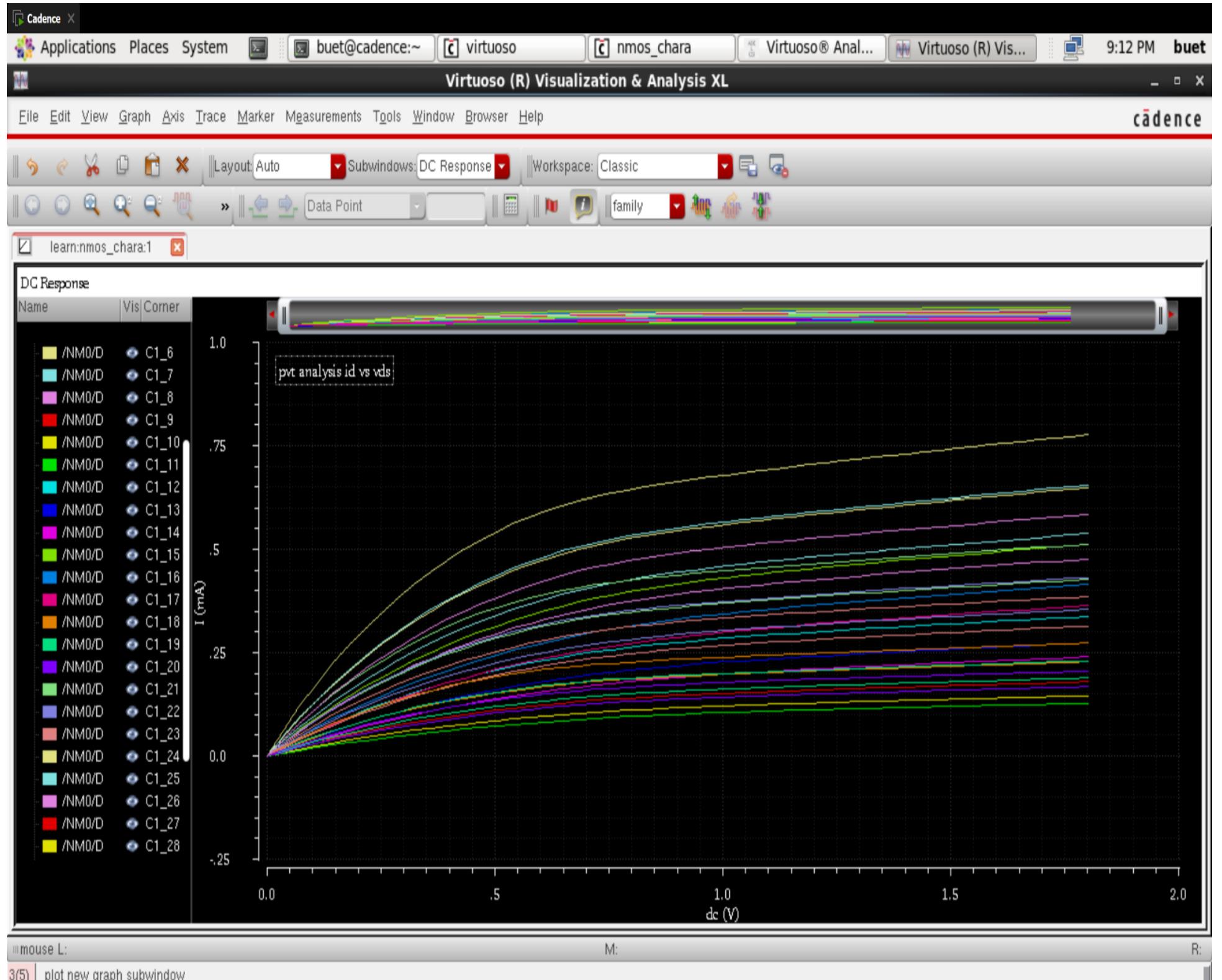
PVT ANALYSIS ON INPUT CHARACTERISTICS











PMOS INPUT OUTPUT CHARACTERISTICS

