

★ Exclusive-OR & Equivalence fⁿ.

- denoted by \oplus & \odot , respectively.

$$x \oplus y = xy' + x'y$$

$$x \odot y = xy + x'y'$$

- two operations are complements of each other.

- both are commutative & associative.

$$\begin{aligned} \text{i.e. } (A \oplus B) \oplus C &= A \oplus (B \oplus C) \\ &= A \oplus B \oplus C \end{aligned}$$

- useful in arithmetic operation & error detection & correction.

- n-variable ex-OR is having $2^n/2$ minterms with binary numbers have an odd number of 1's.

- n-variable ex-NOR is having $2^n/2$ minterms with binary no. have an even number of 0's.

- 2-input X-OR & X-NOR gate

- **XOR**

A	B	$A \oplus B$
0	0	0
0	1	1
1	0	1
1	1	0

$$A \oplus B = A'B + AB'$$

- $2^n/2$ minterms have odd no of 1's

$$- \frac{2^2}{2} = 4/2 = 2 \text{ terms.}$$

- **X-NOR**

A	B	$A \odot B$
0	0	1
0	1	0
1	0	0
1	1	1

$$A \odot B = AB + A'B'$$

- $2^n/2$ minterms have even no of 0's

- **X**

$$\frac{2^2}{2} = 4/2 = 2 \text{ terms}$$

Note :-

* when the no of input n is even the ex-OR & ex-NOR are complement of each other

* when n is odd both are same.

ex-or, 1's n=3

A	B	C	$A \oplus B$ A C	$A \odot B \odot C$
0	0	0	0	0
0	0	1	1	1
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	1
1	1	1	1	

$$C \oplus A \oplus B = \Sigma(1, 2, 4, 7)$$

$$C \odot A \odot B = \Sigma(1, 2, 4, 7)$$

A \ BC	00	01	11	10
00		1		1
01	1		1	

Ex-OR - odd nos of 1's

Ex-NOR - even nos of 1's

$$A \oplus B \oplus C = A \odot B \odot C$$

- when the minterms of a P^n with an odd no of variables have even no of 1's (or, odd nos of 0's), the P^n can be expressed as the complement of either an ex-OR or an ex-NOR.

$$\text{i.e. } (A \oplus B \oplus C)' = A \oplus B \odot C$$

$$(A \odot B \odot C)' = A \odot B \oplus C$$

A \ BC				
	00	01	11	10
0	1		1	
1		1		1

$$F = A \oplus B \odot C$$

$$= A \odot B \oplus C$$

$$= A B' C' + A B C + A B' C + A B C'$$

$$= A' (B' C' + B C) + A (B' C + B C')$$

$$= A' (B \oplus C)' + A (B \oplus C)$$

$$\boxed{F = A \odot (B \oplus C)}$$

- EX-OR & EX-NOR (for - 4 inputs)

- EX-OR

AB \ CD	00	01	11	10
00		1		1
01	1		1	
11		1		1
10	1		1	

$$F = A \oplus B \oplus C \oplus D$$

= odd no of 1's

- EX-NOR

AB \ CD	00	01	11	10
00	1		1	
01		1		1
11	1		1	
10		1		1

$$F = A \odot B \odot C \odot D$$

= even no of 1's.

* Parity generator & checker

→ parity generator

* The circuit that generates the parity bit in the transmitter is called a parity generator

* 3-bit parity generator

→ odd parity

x	y	z	parity bit generated (P)
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0

$$P = \Sigma(0, 3, 5, 6)$$

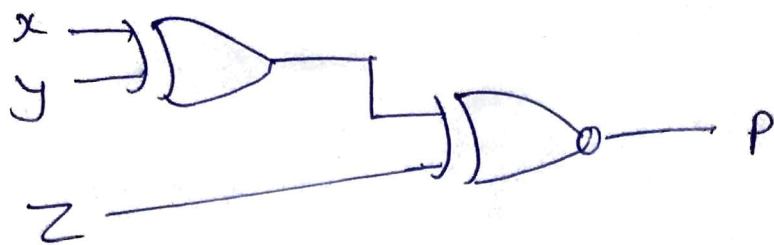
x \ yz	00	01	11	10
0	1		1	
1		1		1

- even 1's & odd 1's

$$P = x \oplus y \oplus z$$

or

$$P = x \oplus y \oplus z$$



3-bit odd parity generator

* 4-bit even parity generator

				even parity bit
w	x	y	z	p
0	0	0	0	0
0	0	0	1	1
0	0	1	0	1
0	0	1	1	0
0	1	0	0	1
0	1	0	1	0
0	1	1	0	0
0	1	1	1	1
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	0
1	1	0	1	1
1	1	1	0	1
1	1	1	1	0

$\Sigma(1, 2, 4, 7, 8, 11, 13, 14)$

→ odd no of 1s

$$p = w \oplus x \oplus y \oplus z$$

\Rightarrow parity checker

- The circuit that checks the parity in the receiver is called parity checker

\Rightarrow odd parity checker

- * 3-bit message with parity bit are transmitted to their destination & applies to a parity checker circuit.

- * An error occurs during transmission if the parity of the four bits received is even.

received is even.

parity = error check

→

0 X	Y	Z	P	C
0	0	0	0	1
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	0
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	0	1	1	1
1	1	0	0	1
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

(0, 3, 5, 6,
9, 10, 12, 15)

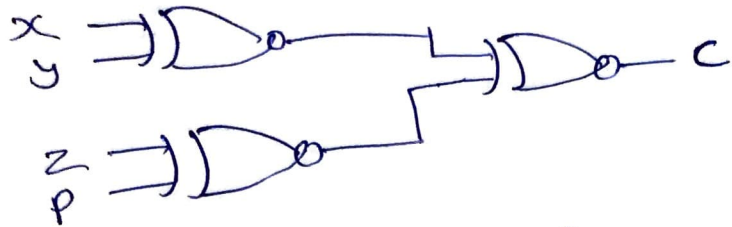
n o's.

X ⊙ Y ⊙ Z ⊙ P

$$C = \sum (0, 3, 5, 6, 9, 10, 12, 15)$$

\rightarrow even o/s.

$$C = X \oplus Y \oplus Z \oplus P$$



4-bit odd parity checker.