3rd Class Test PCC CS302

Due Dec 28 at 2:45pm   **Points** 30   **Questions** 20

Available Dec 28 at 1:30pm - Dec 28 at 2:45pm about 1 hour   **Time Limit** None

Attempt History

	Attempt	Time	Score
LATEST	**Attempt 1**	29 minutes	30 out of 30

Score for this quiz: **30** out of 30

Submitted Dec 28 at 1:59pm

This attempt took 29 minutes.

Question 1	1 / 1 pts

The circuit used to store one bit of data is known as

Correct!

- ◉ Flip-Flop

- ○ Decoder

- ○ OR gate

- ○ Encoder

Question 2	1 / 1 pts

To reduce the memory access time we generally make use of _____

○ Higher capacity RAM's

○ Heaps

Correct!   ● Cache's

○ SDRAM's

Question 3   1 / 1 pts

The main function of the central processing unit is to

○ carry out program instructions

○ perform arithmetic and logical operations

○ control all external and internal devices

○ process datcarry out program instructionsa and information

Correct!   ● All of the above

Question 4   1 / 1 pts

When when power is switched off which memory loses its data?

○ None of the above

○ Both A and B

○ Non-Volatile Memory

Correct!   ⦿ Volatile Memory

Question 5   **1 / 1 pts**

The fastest data access is provided using _____

○ Cache

○ SRAM's

○ DRAM's

Correct!   ⦿ Registers

Question 6   **1 / 1 pts**

An 24 bit address generates an address space of _____ locations.

○ 4096

○ 1024

○ 2^48

Correct!

    ◉ 16,777,216

Question 7   1 / 1 pts

If a system is 64 bit machine, then the length of each word will be

_____

○ 16 bytes

○ 12 bytes

○ 4 bytes

Correct!

◉ 8 bytes

Question 8   1 / 1 pts

The smallest entity of memory is called _____

○ Block

○ Unit

○ Instance

Correct!

◉ Cell

Question 9   1 / 1 pts

The addressing mode which makes use of in-direction pointers is _____

○ Offset addressing mode

○ Relative addressing mode

○ Index addressing mode

Correct!   ● Indirect addressing mode

Question 10   1 / 1 pts

The addressing mode/s, which uses the PC instead of a general purpose register is _____

○ Indexed with offset

○ Direct

Correct!   ● Relative

○ Both Indexed with offset and direct

Question 11   2 / 2 pts

In signed-magnitude binary division, if the dividend is (11100) 2 and divisor is (10011) 2 then the result is

Correct!

○ 00100

◉ 10100

○ 11001)

○ 01100)

Question 12   2 / 2 pts

Logic X-OR operation of (4ACO) H & (B53F) H results

Correct!

◉ FFFF

○ 0000

○ ABCD

○ AACB

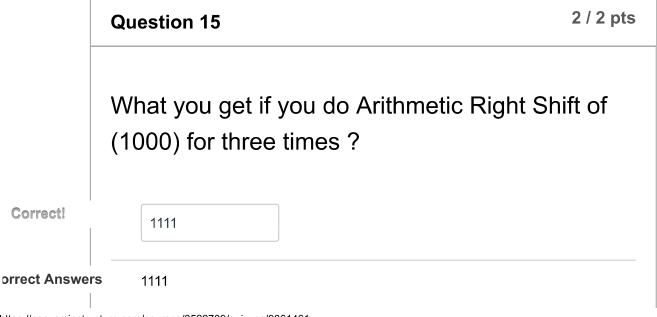---

Question 13   2 / 2 pts

To Design a memory of size 4Mx32 how many 512x8 size memory chips are

needed_____

Correct!

52

rrect Answers   52

Question 14   2 / 2 pts

To build a memory of size 16M x 8, how many address lines and data
lines are required

Correct!

⦿ 24, 8

○ 4, 8

○ 24, 3

○ 14, 8

Question 15   2 / 2 pts

What you get if you do Arithmetic Right Shift of
(1000) for three times ?

Correct!

1111

rrect Answers   1111

Question 16   2 / 2 pts

What will be the decimal value of "00111110010000000000000000000000" (represented in IEEE 754 format)

○ 0.5*2^5

○ 0.5*2^3

○ 1.5* 2^3

Correct!   ◉ 1.5 * 2^-3

Question 17   2 / 2 pts

When we use auto increment or auto decrements, which of the following is/are true?
1) In both, the address is used to retrieve the operand and then the address gets altered
2) In auto increment, the operand is retrieved first and then the address altered
3) Both of them can be used on general purpose registers as well as memory locations

Correct!   ◉ 2, 3

○ 2

○ 1, 2, 3

○ 1, 3

Question 18   2 / 2 pts

The addressing mode, where you directly specify the operand value is

———

Correct!

⦿ Immediate

○ Definite

○ Direct

○ Relative

Question 19   2 / 2 pts

For the addition of large integers, most of the systems make use of

———

○ Fast adders

○ Full adders

○ None of the mentioned

Correct!

⦿ Carry look-ahead adders

Question 20   2 / 2 pts

To design a memory of size 2KB how many 128 x 8 memory chips are required.....................

Correct!

16

orrect Answers   16

Quiz Score: **30** out of 30