

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80053001	ALGUR GAURAV ISHWAR				VASANTI					, 71200757B		S80053005	PICT		S80053001
01. SIGNAL AND SYSTEMS	PP	100	40	43	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	54	P C				
02. SIGNAL AND SYSTEMS	OR	50	20	23	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P C				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	41	P	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P C				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	22	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P C				
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	14	P C				
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	40	P C				
08. POWER DEVICES AND MACHINES	PP	100	40	43	P C	18. DATA STRUCTURES	PR	50	20	22	P C				
09. NETWORK AND POWER LAB.	TW	50	20	25	P C	19. COMMUNICATION THEORY	PP	100	40	51	P C				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	29	P C	20. COMMUNICATION THEORY	OR	50	20	34	P C				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P C				

GRAND TOTAL = 683/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80053008	CHANDAK ANKIT PANDURANGJI				MAYA					, 71100760J		S80053158	PICT		S80053008
01. SIGNAL AND SYSTEMS	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P C				
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P C				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	40	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	36	P C				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P C				
06. DIGITAL LOGIC DESIGN	PP	100	40	60	P C	16. ELECTROMAGNETIC	TW	25	10	18	P C				
07. DIGITAL LOGIC DESIGN	PR	50	20	35	P C	17. DATA STRUCTURES	PP	100	40	43	P C				
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	25	P C				
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	45	P C				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	35	P C	20. COMMUNICATION THEORY	OR	50	20	21	P C				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	37	P C				

GRAND TOTAL = 767/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80053010	DHODI SHRADDHA MADHU				ARUNA					, 71045415F		S80053161	PICT		S80053010
01. SIGNAL AND SYSTEMS	PP	100	40	49	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C				
02. SIGNAL AND SYSTEMS	OR	50	20	25	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P C				
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C				
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	24	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P C				
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	40	P C				
06. DIGITAL LOGIC DESIGN	PP	100	40	46	P C	16. ELECTROMAGNETIC	TW	25	10	16	P C				
07. DIGITAL LOGIC DESIGN	PR	50	20	22	P C	17. DATA STRUCTURES	PP	100	40	40	P C				
08. POWER DEVICES AND MACHINES	PP	100	40	46	P	18. DATA STRUCTURES	PR	50	20	39	P C				
09. NETWORK AND POWER LAB.	TW	50	20	20	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C				
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	25	P C	20. COMMUNICATION THEORY	OR	50	20	20	P C				
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P C				

GRAND TOTAL = 689/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

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S80053011	DOKADE ABHIJIT VISHNU				BEBY		, 71200847M		S80053045		PICT		S80053011
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P
02.	SIGNAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	13	P C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	AA	F	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	25	F
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C
05.	NETWORK ANALYSIS	PP	100	40	17	F	15.	ELECTROMAGNETIC	PP	100	40	13	F
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16.	ELECTROMAGNETIC	TW	25	10	12	P C
07.	DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17.	DATA STRUCTURES	PP	100	40	44	P C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES	PR	50	20	20	P C
09.	NETWORK AND POWER LAB.	TW	50	20	26	P C	19.	COMMUNICATION THEORY	PP	100	40	06	F
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	31	P C	20.	COMMUNICATION THEORY	OR	50	20	22	P C
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	22	P C

GRAND TOTAL = 491/1500, RESULT: FAILS

RESULT RESERVED FOR BKL

ORDN. 1 MARKS :

S80053012	DOLAS LUBDHA RAJU				SUNITA		, 71236310G		S80053162		PICT		S80053012
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P
02.	SIGNAL AND SYSTEMS	OR	50	20	22	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	14	P C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	41	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	21	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C
05.	NETWORK ANALYSIS	PP	100	40	40	P C	15.	ELECTROMAGNETIC	PP	100	40	40	P C
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16.	ELECTROMAGNETIC	TW	25	10	14	P C
07.	DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17.	DATA STRUCTURES	PP	100	40	40	P C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P C	18.	DATA STRUCTURES	PR	50	20	36	P C
09.	NETWORK AND POWER LAB.	TW	50	20	32	P C	19.	COMMUNICATION THEORY	PP	100	40	40	P C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	36	P C	20.	COMMUNICATION THEORY	OR	50	20	29	P C
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	30	P C

GRAND TOTAL = 683/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80053013	DONDE YATHARTH ANAND				MANISHA		, 71200848K		S80053046		PICT		S80053013
01.	SIGNAL AND SYSTEMS	PP	100	40	40	P C	11.	ENGINEERING MATHEMATICS III	PP	100	40	AA	F
02.	SIGNAL AND SYSTEMS	OR	50	20	20	P C	12.	ENGINEERING MATHEMATICS III	TW	25	10	11	P C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C	13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C
05.	NETWORK ANALYSIS	PP	100	40	10	F	15.	ELECTROMAGNETIC	PP	100	40	40	P
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16.	ELECTROMAGNETIC	TW	25	10	17	P C
07.	DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17.	DATA STRUCTURES	PP	100	40	51	P C
08.	POWER DEVICES AND MACHINES	PP	100	40	40	P	18.	DATA STRUCTURES	PR	50	20	25	P C
09.	NETWORK AND POWER LAB.	TW	50	20	32	P C	19.	COMMUNICATION THEORY	PP	100	40	40	P C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	28	P C	20.	COMMUNICATION THEORY	OR	50	20	22	P C
							21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	27	P C

GRAND TOTAL = 583/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

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S80053015	GENGAJE PRAJAKTA SHANTARAM				SUREKHA			, 71236312C		S80053165		PICT		S80053015
01.	SIGNAL AND SYSTEMS	PP	100	40	49	P C		11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P
02.	SIGNAL AND SYSTEMS	OR	50	20	25	P C		12.	ENGINEERING MATHEMATICS III	TW	25	10	21	P C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	22	P C
05.	NETWORK ANALYSIS	PP	100	40	40	P C		15.	ELECTROMAGNETIC	PP	100	40	40	P C
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P C		16.	ELECTROMAGNETIC	TW	25	10	17	P C
07.	DIGITAL LOGIC DESIGN	PR	50	20	22	P C		17.	DATA STRUCTURES	PP	100	40	45	P C
08.	POWER DEVICES AND MACHINES	PP	100	40	48	P C		18.	DATA STRUCTURES	PR	50	20	38	P C
09.	NETWORK AND POWER LAB.	TW	50	20	40	P C		19.	COMMUNICATION THEORY	PP	100	40	45	P C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	40	P C		20.	COMMUNICATION THEORY	OR	50	20	23	P C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P C

GRAND TOTAL = 740+10/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

S80053017	JADHAV POONAM SANJAY				SANGITA			, 71344359G		S80053059		PICT		S80053017
01.	SIGNAL AND SYSTEMS	PP	100	40	46	P C		11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P
02.	SIGNAL AND SYSTEMS	OR	50	20	22	P C		12.	ENGINEERING MATHEMATICS III	TW	25	10	18	P C
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P C		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	44	P C
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	21	P C		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P C
05.	NETWORK ANALYSIS	PP	100	40	46	P C		15.	ELECTROMAGNETIC	PP	100	40	42	P C
06.	DIGITAL LOGIC DESIGN	PP	100	40	41	P C		16.	ELECTROMAGNETIC	TW	25	10	18	P C
07.	DIGITAL LOGIC DESIGN	PR	50	20	25	P C		17.	DATA STRUCTURES	PP	100	40	40	P C
08.	POWER DEVICES AND MACHINES	PP	100	40	58	P C		18.	DATA STRUCTURES	PR	50	20	32	P C
09.	NETWORK AND POWER LAB.	TW	50	20	39	P C		19.	COMMUNICATION THEORY	PP	100	40	47	P C
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C		20.	COMMUNICATION THEORY	OR	50	20	44	P C
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P C

GRAND TOTAL = 763/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80053019	KHANDELWAL DIVYANG KAMALKISHOR				SANGEETA			, 71100863K		S8053062		PICT		S80053019
01.	SIGNAL AND SYSTEMS	PP	100	40	43	P C		11.	ENGINEERING MATHEMATICS III	PP	100	40	19	F
02.	SIGNAL AND SYSTEMS	OR	50	20	20	P C		12.	ENGINEERING MATHEMATICS III	TW	25	10	18	P
03.	SOLID STATES DEVICES AND CIRCUITS	SPP	100	40	40	P		13.	INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	59	P
04.	SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	29	P		14.	INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	24	P
05.	NETWORK ANALYSIS	PP	100	40	52	P		15.	ELECTROMAGNETIC	PP	100	40	25	F
06.	DIGITAL LOGIC DESIGN	PP	100	40	40	P		16.	ELECTROMAGNETIC	TW	25	10	11	P
07.	DIGITAL LOGIC DESIGN	PR	50	20	29	P C		17.	DATA STRUCTURES	PP	100	40	43	P
08.	POWER DEVICES AND MACHINES	PP	100	40	54	P		18.	DATA STRUCTURES	PR	50	20	32	P
09.	NETWORK AND POWER LAB.	TW	50	20	21	P C		19.	COMMUNICATION THEORY	PP	100	40	40	P
10.	ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	23	P C		20.	COMMUNICATION THEORY	OR	50	20	20	P
								21.	CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P

GRAND TOTAL = 676/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80053020	KOLI VIJAYA VYANKATRAO				SEETA					, 71100866D			S80053172		PICT			S80053020
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C							
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P C							
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C							
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C							
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	40	P C							
06. DIGITAL LOGIC DESIGN	PP	100	40	42	P C	16. ELECTROMAGNETIC	TW	25	10	18	P C							
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	43	P C							
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	32	P C							
09. NETWORK AND POWER LAB.	TW	50	20	27	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C							
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	24	P C							
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P C							

GRAND TOTAL = 677/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80053021	KONDHAWALE PRATIBHA MHATARABA				MANGAL					, 71200929K			S80053075		PICT			S80053021
01. SIGNAL AND SYSTEMS	PP	100	40	47	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C							
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	20	P C							
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	48	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C							
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	27	P C							
05. NETWORK ANALYSIS	PP	100	40	59	P	15. ELECTROMAGNETIC	PP	100	40	51	P C							
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	21	P C							
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	56	P C							
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	35	P C							
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	51	P C							
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P C	20. COMMUNICATION THEORY	OR	50	20	24	P C							
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	40	P C							

GRAND TOTAL = 792/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80053023	MORE ANKUR SURESHRAO				LAXMI					, 71100900H			S80053173		PICT			S80053023
01. SIGNAL AND SYSTEMS	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P C							
02. SIGNAL AND SYSTEMS	OR	50	20	26	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	12	P C							
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	AA	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	25	F							
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P C							
05. NETWORK ANALYSIS	PP	100	40	AA	F	15. ELECTROMAGNETIC	PP	100	40	18	F							
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	16. ELECTROMAGNETIC	TW	25	10	11	P C							
07. DIGITAL LOGIC DESIGN	PR	50	20	25	P C	17. DATA STRUCTURES	PP	100	40	40	P C							
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	32	P C							
09. NETWORK AND POWER LAB.	TW	50	20	24	P C	19. COMMUNICATION THEORY	PP	100	40	21	F							
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	27	P C	20. COMMUNICATION THEORY	OR	50	20	21	P							
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	23	P C							

GRAND TOTAL = 509/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80053024	NANDNAWARE AKASH JAGNNATH				DRAUPADA				, 71100910E				S80053176		PICT				S80053024
01. SIGNAL AND SYSTEMS	PP	100	40	12	F	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	C							
02. SIGNAL AND SYSTEMS	OR	50	20	26	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	11	P	C						
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C						
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P							
05. NETWORK ANALYSIS	PP	100	40	16	F	15. ELECTROMAGNETIC	PP	100	40	11	F								
06. DIGITAL LOGIC DESIGN	PP	100	40	49	P	C	16. ELECTROMAGNETIC	TW	25	10	10	P	C						
07. DIGITAL LOGIC DESIGN	PR	50	20	AA	F	17. DATA STRUCTURES	PP	100	40	40	P	C							
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	35	P	C							
09. NETWORK AND POWER LAB.	TW	50	20	30	P	C	19. COMMUNICATION THEORY	PP	100	40	40	P							
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	28	P	C	20. COMMUNICATION THEORY	OR	50	20	30	P	C						
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	25	P	C							

GRAND TOTAL = 568/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80053025	NEHA NIKUM				KAUSHALYA				, 71200984B				S80053092		PICT				S80053025
01. SIGNAL AND SYSTEMS	PP	100	40	40	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	19	F							
02. SIGNAL AND SYSTEMS	OR	50	20	23	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	11	P	C						
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	42	P							
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P	C						
05. NETWORK ANALYSIS	PP	100	40	13	F	15. ELECTROMAGNETIC	PP	100	40	40	P	C							
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16. ELECTROMAGNETIC	TW	25	10	10	P	C						
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P	C	17. DATA STRUCTURES	PP	100	40	40	P	C						
08. POWER DEVICES AND MACHINES	PP	100	40	40	P	18. DATA STRUCTURES	PR	50	20	AA	F								
09. NETWORK AND POWER LAB.	TW	50	20	28	P	C	19. COMMUNICATION THEORY	PP	100	40	40	P							
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P	C	20. COMMUNICATION THEORY	OR	50	20	22	P	C						
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	22	P	C							

GRAND TOTAL = 567/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80053026	OSWAL ARCHA AMRUT				ALKA OSWAL				, 71344365M				S80053096		PICT				S80053026
01. SIGNAL AND SYSTEMS	PP	100	40	46	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	44	P							
02. SIGNAL AND SYSTEMS	OR	50	20	27	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	17	P	C						
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C						
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	24	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	24	P	C						
05. NETWORK ANALYSIS	PP	100	40	43	P	C	15. ELECTROMAGNETIC	PP	100	40	47	P	C						
06. DIGITAL LOGIC DESIGN	PP	100	40	48	P	C	16. ELECTROMAGNETIC	TW	25	10	17	P	C						
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P	C	17. DATA STRUCTURES	PP	100	40	40	P	C						
08. POWER DEVICES AND MACHINES	PP	100	40	44	P	18. DATA STRUCTURES	PR	50	20	33	P	C							
09. NETWORK AND POWER LAB.	TW	50	20	40	P	C	19. COMMUNICATION THEORY	PP	100	40	40	P	C						
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	38	P	C	20. COMMUNICATION THEORY	OR	50	20	26	P	C						
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P	C							

GRAND TOTAL = 740+10/1500, RESULT: SECOND CLASS [0.2]

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80053027	PAWAR SACHIN VINAYAKRAO				SAVITA					, 70925557J				S80053180		PICT			S80053027
01. SIGNAL AND SYSTEMS	PP	100	40	AA	F	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F								
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P C								
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	AA	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	AA	F								
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	40	P C								
05. NETWORK ANALYSIS	PP	100	40	AA	F	15. ELECTROMAGNETIC	PP	100	40	AA	F								
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F	16. ELECTROMAGNETIC	TW	25	10	18	P C								
07. DIGITAL LOGIC DESIGN	PR	50	20	AA	F	17. DATA STRUCTURES	PP	100	40	AA	F								
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	18. DATA STRUCTURES	PR	50	20	34	P C								
09. NETWORK AND POWER LAB.	TW	50	20	33	P C	19. COMMUNICATION THEORY	PP	100	40	AA	F								
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P C	20. COMMUNICATION THEORY	OR	50	20	21	P C								
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P C								

GRAND TOTAL = 255/1500, RESULT: FAILS

RESULT RESERVED FOR BKL

ORDN. 1 MARKS :

S80053029	ROKADE SUYOG SUDARSHAN				SUREKHA					, 71201043C				S80053118		PICT			S80053029
01. SIGNAL AND SYSTEMS	PP	100	40	44	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P								
02. SIGNAL AND SYSTEMS	OR	50	20	35	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	18	P C								
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P C								
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	27	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	28	P C								
05. NETWORK ANALYSIS	PP	100	40	57	P	15. ELECTROMAGNETIC	PP	100	40	47	P C								
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	20	P C								
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	57	P C								
08. POWER DEVICES AND MACHINES	PP	100	40	46	P C	18. DATA STRUCTURES	PR	50	20	20	P C								
09. NETWORK AND POWER LAB.	TW	50	20	39	P C	19. COMMUNICATION THEORY	PP	100	40	44	P C								
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	37	P C	20. COMMUNICATION THEORY	OR	50	20	22	P C								
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	39	P C								

GRAND TOTAL = 770/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80053031	SHANTANU SHEKAR JAGTAP				SHUBHANGI					, 71201063H				S80053125		PICT			S80053031
01. SIGNAL AND SYSTEMS	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	47	P								
02. SIGNAL AND SYSTEMS	OR	50	20	22	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	15	P								
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	63	P								
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P								
05. NETWORK ANALYSIS	PP	100	40	40	P C	15. ELECTROMAGNETIC	PP	100	40	42	P								
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	11	P								
07. DIGITAL LOGIC DESIGN	PR	50	20	20	P C	17. DATA STRUCTURES	PP	100	40	27	F								
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	30	P								
09. NETWORK AND POWER LAB.	TW	50	20	23	P C	19. COMMUNICATION THEORY	PP	100	40	55	P								
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	20	P C	20. COMMUNICATION THEORY	OR	50	20	22	P								
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	34	P								

GRAND TOTAL = 692/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80053032	SHELKE AMOL MARUTI				SHARADA					, 71344372D		S80053128		PICT		S80053032
01. SIGNAL AND SYSTEMS	PP	100	40	40	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P					
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	19	P C					
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	51	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	50	P C					
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	25	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	24	P					
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	40	P					
06. DIGITAL LOGIC DESIGN	PP	100	40	58	P C	16. ELECTROMAGNETIC	TW	25	10	20	P C					
07. DIGITAL LOGIC DESIGN	PR	50	20	34	P C	17. DATA STRUCTURES	PP	100	40	42	P C					
08. POWER DEVICES AND MACHINES	PP	100	40	59	P C	18. DATA STRUCTURES	PR	50	20	26	P C					
09. NETWORK AND POWER LAB.	TW	50	20	35	P C	19. COMMUNICATION THEORY	PP	100	40	40	P C					
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	32	P C	20. COMMUNICATION THEORY	OR	50	20	29	P C					
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	38	P C					

GRAND TOTAL = 762/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80053035	SONAWANE NIKHIL BHIMRAO				SUNANDA					, 71045632J		S80053190		PICT		S80053035
01. SIGNAL AND SYSTEMS	PP	100	40	42	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P					
02. SIGNAL AND SYSTEMS	OR	50	20	24	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	10	P C					
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	54	P					
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	20	P					
05. NETWORK ANALYSIS	PP	100	40	13	F	15. ELECTROMAGNETIC	PP	100	40	24	F					
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P C	16. ELECTROMAGNETIC	TW	25	10	10	P C					
07. DIGITAL LOGIC DESIGN	PR	50	20	30	P C	17. DATA STRUCTURES	PP	100	40	40	P					
08. POWER DEVICES AND MACHINES	PP	100	40	40	P C	18. DATA STRUCTURES	PR	50	20	24	P					
09. NETWORK AND POWER LAB.	TW	50	20	27	P C	19. COMMUNICATION THEORY	PP	100	40	42	P					
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	30	P C	20. COMMUNICATION THEORY	OR	50	20	AA	F					
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	20	P C					

GRAND TOTAL = 590/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80053037	THAKARE BHAGYASHREE RAJENDRA				SUNITA					, 71045645L		S8053104		PICT		S80053037
01. SIGNAL AND SYSTEMS	PP	100	40	40	P	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C					
02. SIGNAL AND SYSTEMS	OR	50	20	20	P C	12. ENGINEERING MATHEMATICS III	TW	25	10	21	P C					
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	26	F	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	AA	F					
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	20	P C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	30	P C					
05. NETWORK ANALYSIS	PP	100	40	40	P	15. ELECTROMAGNETIC	PP	100	40	17	F					
06. DIGITAL LOGIC DESIGN	PP	100	40	AA	F	16. ELECTROMAGNETIC	TW	25	10	15	P C					
07. DIGITAL LOGIC DESIGN	PR	50	20	28	P C	17. DATA STRUCTURES	PP	100	40	57	P C					
08. POWER DEVICES AND MACHINES	PP	100	40	AA	F	18. DATA STRUCTURES	PR	50	20	44	P C					
09. NETWORK AND POWER LAB.	TW	50	20	37	P C	19. COMMUNICATION THEORY	PP	100	40	21	F					
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	39	P C	20. COMMUNICATION THEORY	OR	50	20	20	P C					
						21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	35	P C					

GRAND TOTAL = 550/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80053039	VADGAONKAR MANALI ABHAY			ROOPA			, 71201103L		S80053145		PICT		S80053039
01. SIGNAL AND SYSTEMS	PP	100	40	49	P	C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P	
02. SIGNAL AND SYSTEMS	OR	50	20	28	P	C	12. ENGINEERING MATHEMATICS III	TW	25	10	13	P	C
03. SOLID STATES DEVICES AND CIRCUITS	PP	100	40	40	P	C	13. INTEGRATED CIRCUITS APPLICATIONS	PP	100	40	40	P	C
04. SOLID STATES DEVICES AND CIRCUITS	SPR	50	20	35	P	C	14. INTEGRATED CIRCUITS APPLICATIONS	PR	50	20	25	P	C
05. NETWORK ANALYSIS	PP	100	40	15	F		15. ELECTROMAGNETIC	PP	100	40	53	P	C
06. DIGITAL LOGIC DESIGN	PP	100	40	40	P	C	16. ELECTROMAGNETIC	TW	25	10	15	P	C
07. DIGITAL LOGIC DESIGN	PR	50	20	38	P	C	17. DATA STRUCTURES	PP	100	40	40	P	C
08. POWER DEVICES AND MACHINES	PP	100	40	19	F		18. DATA STRUCTURES	PR	50	20	36	P	C
09. NETWORK AND POWER LAB.	TW	50	20	37	P	C	19. COMMUNICATION THEORY	PP	100	40	26	F	
10. ELECTRONIC INSTRUMENTS AND TOOLS	TW	50	20	34	P	C	20. COMMUNICATION THEORY	OR	50	20	25	P	C
							21. CIRCUIT SIMULATION AND TOOLS	TW	50	20	32	P	C

GRAND TOTAL = 680/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054201	ABHINAV KAUL				SUREKHA				, 71200747E			S80054202		PICT		S80054201
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C					
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P					
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C					
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	48	P C					
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P					
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	33	P C					
07. PROGRAMMING LABORATORY	PR	50	20	41	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	38	P C					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P C					
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P C					
10. SOFT SKILLS	TW	50	20	32	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	35	P C					
						21. DATA STRUCTURES LABORATORY	PR	50	20	31	P C					

GRAND TOTAL = 781/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80054202	ABHISHEK KUMAR SAURAV				SUMAN				, 71045357E			S80054412		PICT		S80054202
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C					
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	48	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F					
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	43	P C	13. DATA STRUCTURES	PP	100	40	43	P C					
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	41	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C					
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C					
06. PROGRAMMING LABORATORY	TW	25	10	10	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P C					
07. PROGRAMMING LABORATORY	PR	50	20	36	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	AA	F					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	22	P C					
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F					
10. SOFT SKILLS	TW	50	20	20	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C					
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F					

GRAND TOTAL = 557/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054203	ABNANE OMKAR RAJENDRA				SHOBHA				, 71045358C			S80054352		PICT		S80054203
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	42	P C					
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P C					
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C					
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	57	P C					
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	41	P C	15. COMPUTER ORGANIZATION	PP	100	40	45	P C					
06. PROGRAMMING LABORATORY	TW	25	10	13	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	23	P C					
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	AA	F					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	13	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P C					
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C					
10. SOFT SKILLS	TW	50	20	22	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C					
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P C					

GRAND TOTAL = 637/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054204	AMAN RATHI				GEETA				, 71200759J			S80054206		PICT		S80054204
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P					
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	50	P C					
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	55	P C	13. DATA STRUCTURES	PP	100	40	49	P C					
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS	PP	100	40	60	P C					
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	66	P C	15. COMPUTER ORGANIZATION	PP	100	40	58	P C					
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P C					
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	46	P C					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	22	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P C					
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	36	P C					
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	38	P C					
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P C					

GRAND TOTAL = 890+10/1500, RESULT: FIRST CLASS [0.2]

ORDN. 1 MARKS :

S80054206	BANDEWAR AKASH DEVIDAS				MANISHA				, 71200779C			S80054215		PICT		S80054206
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P C					
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	52	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P					
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C	13. DATA STRUCTURES	PP	100	40	40	P C					
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C					
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	43	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P					
06. PROGRAMMING LABORATORY	TW	25	10	15	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P C					
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P C					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C					
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	28	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P					
10. SOFT SKILLS	TW	50	20	28	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C					
						21. DATA STRUCTURES LABORATORY	PR	50	20	22	P C					

GRAND TOTAL = 678/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80054207	BASAPURE SHRAVYA RAMESH				VIJAYA				, 71100743J			S80054357		PICT		S80054207
01. DISCRETE STRUCTURES	PP	100	40	17	F	11. ENGINEERING MATHEMATICS III	PP	100	40	02	F					
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F					
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	AA	F	13. DATA STRUCTURES	PP	100	40	19	F					
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	14. COMPUTER GRAPHICS	PP	100	40	23	F					
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	19	F					
06. PROGRAMMING LABORATORY	TW	25	10	21	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	32	P C					
07. PROGRAMMING LABORATORY	PR	50	20	31	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P					
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	21	P C					
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F					
10. SOFT SKILLS	TW	50	20	36	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	26	P C					
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F					

GRAND TOTAL = 432/1500, RESULT: FAILS

RESULT RESERVED FOR BKLG

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054210	BHOSALE SHITAL MANOHAR			JAYA		, 71132426D	, S80054360	PICT		, S80054210
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	48 P
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	44	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40 P C
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	41	P C	13. DATA STRUCTURES	PP	100	40	40 P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	47	P C	14. COMPUTER GRAPHICS	PP	100	40	46 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40 P C
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39 P C
07. PROGRAMMING LABORATORY	PR	50	20	25	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20 P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	28 P C
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32 P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	20 P C

GRAND TOTAL = 714/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80054212	DATIR DEEPAK HARIBHAU			MANDA		, 71200828E	, S80054232	PICT		, S80054212
01. DISCRETE STRUCTURES	PP	100	40	52	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	44 P C
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	23 F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	26 F
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P C	15. COMPUTER ORGANIZATION	PP	100	40	40 P C
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30 P C
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30 P C
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	14	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	24 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20 P
10. SOFT SKILLS	TW	50	20	28	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	28 P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	25 P C

GRAND TOTAL = 654/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054213	GADGE PRASHIK SUNIL			MALA		, 71200851K	, S80054240	PICT		, S80054213
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA F
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA F
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40 P C
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	50 P C
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	57	P C	15. COMPUTER ORGANIZATION	PP	100	40	40 P C
06. PROGRAMMING LABORATORY	TW	25	10	14	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35 P C
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23 P
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	37 P C
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20 P C
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	37 P C
						21. DATA STRUCTURES LABORATORY	PR	50	20	21 P C

GRAND TOTAL = 666/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054215	GAWALI PRAJAKTA PANDURANG				SHOBHA				, 71200861G				S80054245		PICT			S80054215
01.	DISCRETE STRUCTURES	PP	100	40	40	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	58	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	46	P	C		
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	C		13.	DATA STRUCTURES	PP	100	40	40	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	C		14.	COMPUTER GRAPHICS	PP	100	40	41	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	40	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	18	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	39	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	20	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	32	P	C		
10.	SOFT SKILLS	TW	50	20	40	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	39	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	20	P	C		

GRAND TOTAL = 756/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80054217	JADHAV AMOL JETA				LATA				, 71045453J				S80054371		PICT			S80054217
01.	DISCRETE STRUCTURES	PP	100	40	40	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	43	P	C		
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	C		
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	43	P	C		13.	DATA STRUCTURES	PP	100	40	57	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	55	P	C		14.	COMPUTER GRAPHICS	PP	100	40	49	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	43	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	20	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	24	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	40	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	28	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F			
10.	SOFT SKILLS	TW	50	20	26	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	29	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	AA	F			

GRAND TOTAL = 685/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054221	KAMBALE NILESH VIJAY				JAYMALA				, 71347766M				S80054260		PICT			S80054221
01.	DISCRETE STRUCTURES	PP	100	40	40	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	15	F			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	60	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	51	P			
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	42	P	C		13.	DATA STRUCTURES	PP	100	40	40	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	42	P	C		14.	COMPUTER GRAPHICS	PP	100	40	45	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	47	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	20	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	28	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	33	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	23	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	18	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	30	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P	C		
10.	SOFT SKILLS	TW	50	20	29	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	30	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	22	P	C		

GRAND TOTAL = 709/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054222	KANTALE SACHIN WAMANRAO				PRAMILA			, 71100848F		S80054372		PICT		S80054222
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	45	P			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	13. DATA STRUCTURES	PP	100	40	40	P C			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	46	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	44	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C			
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P C			
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	25	P			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	24	P C			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P			
10. SOFT SKILLS	TW	50	20	31	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	29	P C			
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P C			

GRAND TOTAL = 708/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80054223	KEDARE SWAPNIL PANDIT				NIRMALA			, 71045479B		S80054375		PICT		S80054223
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	03	F			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS	PP	100	40	43	P C			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	47	P C			
06. PROGRAMMING LABORATORY	TW	25	10	10	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P C			
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F			
10. SOFT SKILLS	TW	50	20	20	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C			
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F			

GRAND TOTAL = 537/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054224	KHADE SOURABH DURYODHAN				SANGITA			, 71200919B		S80054266		PICT		S80054224
01. DISCRETE STRUCTURES	PP	100	40	56	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	12	F			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	45	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P C			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	62	P C	13. DATA STRUCTURES	PP	100	40	49	P C			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	51	P C	14. COMPUTER GRAPHICS	PP	100	40	58	P C			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	48	P C	15. COMPUTER ORGANIZATION	PP	100	40	49	P C			
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P C			
07. PROGRAMMING LABORATORY	PR	50	20	36	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	32	P C			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P C			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	42	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	30	P C			
10. SOFT SKILLS	TW	50	20	34	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	33	P C			
						21. DATA STRUCTURES LABORATORY	PR	50	20	30	P C			

GRAND TOTAL = 805/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054225	KHALID RAZA KHAN				AYESHA				, 70925473D				S80054376		PICT			S80054225
01.	DISCRETE STRUCTURES	PP	100	40	40	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P			
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	C		13.	DATA STRUCTURES	PP	100	40	40	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	C		14.	COMPUTER GRAPHICS	PP	100	40	43	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	42	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	40	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	10	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	25	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	23	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	24	P			
10.	SOFT SKILLS	TW	50	20	20	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	20	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	22	P	C		

GRAND TOTAL = 619/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80054227	MAHAJAN PRITESH JAYRAM				ANUSAYA				, 71200947H				S80054274		PICT			S80054227
01.	DISCRETE STRUCTURES	PP	100	40	40	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	24	F			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	C		
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	C		13.	DATA STRUCTURES	PP	100	40	40	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	C		14.	COMPUTER GRAPHICS	PP	100	40	40	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	52	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	40	P			
06.	PROGRAMMING LABORATORY	TW	25	10	22	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	24	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	32	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	21	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	22	P			
10.	SOFT SKILLS	TW	50	20	38	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	34	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	30	P			

GRAND TOTAL = 697/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054235	PANDHARE PRIYANKA RAVINDRA				RAJANI				, 71200998B				S80054300		PICT			S80054235
01.	DISCRETE STRUCTURES	PP	100	40	49	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	55	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	C		
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	50	P	C		13.	DATA STRUCTURES	PP	100	40	40	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	C		14.	COMPUTER GRAPHICS	PP	100	40	42	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	44	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	19	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	38	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	20	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	35	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	20	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	35	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P	C		
10.	SOFT SKILLS	TW	50	20	40	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	38	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	35	P	C		

GRAND TOTAL = 791/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054237	PATTHE KAUSTUBH TEJRAM				SHOBHA				, 71347772F				S80054306		PICT			S80054237
01.	DISCRETE STRUCTURES	PP	100	40	40	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	16	F			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	57	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	43	P	C		
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	58	P	C		13.	DATA STRUCTURES	PP	100	40	46	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	53	P	C		14.	COMPUTER GRAPHICS	PP	100	40	52	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	45	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	20	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	36	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	35	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	36	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	30	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P	C		
10.	SOFT SKILLS	TW	50	20	40	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	35	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	36	P	C		

GRAND TOTAL = 819/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054238	RATHI ADITYA PRADIPKUMAR				SNEHAL				, 71201032H				S80054309		PICT			S80054238
01.	DISCRETE STRUCTURES	PP	100	40	44	P	C		11.	ENGINEERING MATHEMATICS III	PP	100	40	55	P			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P	C		
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	47	P	C		13.	DATA STRUCTURES	PP	100	40	46	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P	C		14.	COMPUTER GRAPHICS	PP	100	40	47	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	48	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	20	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	42	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	40	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	30	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	19	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	37	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	35	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	28	P	C		
10.	SOFT SKILLS	TW	50	20	37	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	40	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	40	P	C		

GRAND TOTAL = 839/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

S80054242	SAURAV JAUHARI				POONAM				, 70925593E				S80054397		PICT			S80054242
01.	DISCRETE STRUCTURES	PP	100	40	40	P			11.	ENGINEERING MATHEMATICS III	PP	100	40	00	F			
02.	PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P	C		12.	MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P			
03.	DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P	C		13.	DATA STRUCTURES	PP	100	40	40	P	C		
04.	DATA STRUCTURES AND ALGORITHMS	PP	100	40	48	P			14.	COMPUTER GRAPHICS	PP	100	40	40	P	C		
05.	HUMANITIES AND SOCIAL SCIENCE	PP	100	40	47	P	C		15.	COMPUTER ORGANIZATION	PP	100	40	41	P	C		
06.	PROGRAMMING LABORATORY	TW	25	10	13	P	C		16.	O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	20	P	C		
07.	PROGRAMMING LABORATORY	PR	50	20	20	P	C		17.	O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P	C		
08.	DIGITAL ELECTRONICS LABORATORY	TW	25	10	10	P	C		18.	MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P	C		
09.	DIGITAL ELECTRONICS LABORATORY	PR	50	20	22	P	C		19.	MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P	C		
10.	SOFT SKILLS	TW	50	20	26	P	C		20.	DATA STRUCTURES LABORATORY	TW	50	20	20	P	C		
									21.	DATA STRUCTURES LABORATORY	PR	50	20	20	P	C		

GRAND TOTAL = 587/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.
OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054243	SHAHANE TANMAY SHIVKUMAR				VASUNDHARA			, 71201062K		S80054320		PICT		S80054243
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	40	P			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	41	P C			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	51	P C			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	54	P C	15. COMPUTER ORGANIZATION	PP	100	40	54	P C			
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	25	P C			
07. PROGRAMMING LABORATORY	PR	50	20	32	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	36	P C			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	23	P C			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	20	P C			
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	22	P C			
						21. DATA STRUCTURES LABORATORY	PR	50	20	21	P C			

GRAND TOTAL = 722/1500, RESULT: PASS CLASS [\$ 0.1]

ORDN. 1 MARKS : (19)2,

S80054244	SHELKANDE AKASH DAGADU				SEETA			, 71101000F		S80054399		PICT		S80054244
01. DISCRETE STRUCTURES	PP	100	40	41	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	25	F			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	54	P C	13. DATA STRUCTURES	PP	100	40	40	P			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C			
06. PROGRAMMING LABORATORY	TW	25	10	17	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	26	P C			
07. PROGRAMMING LABORATORY	PR	50	20	21	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P C			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	33	P C			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	20	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P C			
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	32	P C			
						21. DATA STRUCTURES LABORATORY	PR	50	20	24	P C			

GRAND TOTAL = 677/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054245	SHEWALE NEHA SHAMRAO				SHEWALE VAISHALI			, 71347775L		S80054324		PICT		S80054245
01. DISCRETE STRUCTURES	PP	100	40	74	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	28	F			
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	49	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	42	P C			
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	53	P C	13. DATA STRUCTURES	PP	100	40	56	P C			
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	43	P C	14. COMPUTER GRAPHICS	PP	100	40	56	P C			
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	58	P C	15. COMPUTER ORGANIZATION	PP	100	40	49	P C			
06. PROGRAMMING LABORATORY	TW	25	10	22	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	40	P C			
07. PROGRAMMING LABORATORY	PR	50	20	28	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	28	P C			
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	40	P C			
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	35	P C			
10. SOFT SKILLS	TW	50	20	41	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P C			
						21. DATA STRUCTURES LABORATORY	PR	50	20	40	P C			

GRAND TOTAL = 875/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054250	TALPE SHITAL LAXMAN				HEMLATA				, 71347777G				S80054334		PICT			S80054250
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	27	F							
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P							
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C							
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C							
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	40	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C							
06. PROGRAMMING LABORATORY	TW	25	10	18	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	35	P C							
07. PROGRAMMING LABORATORY	PR	50	20	33	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	22	P C							
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	34	P C							
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	25	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	21	P C							
10. SOFT SKILLS	TW	50	20	37	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	34	P C							
						21. DATA STRUCTURES LABORATORY	PR	50	20	20	P C							

GRAND TOTAL = 683/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054252	TAYADE SUSHANT RAMESH				NANDINI				, 71201094H				S80054336		PICT			S80054252
01. DISCRETE STRUCTURES	PP	100	40	62	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	07	F							
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	63	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	40	P C							
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	40	P C	13. DATA STRUCTURES	PP	100	40	40	P C							
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	50	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C							
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F							
06. PROGRAMMING LABORATORY	TW	25	10	19	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	30	P C							
07. PROGRAMMING LABORATORY	PR	50	20	20	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P C							
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	17	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	26	P C							
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	32	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P C							
10. SOFT SKILLS	TW	50	20	33	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	30	P C							
						21. DATA STRUCTURES LABORATORY	PR	50	20	28	P C							

GRAND TOTAL = 686/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80054253	VAIRAGE ROHIT VISHNU				RAJASHRI				, 70925643E				S8054403		PICT			S80054253
01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	AA	F							
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	40	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	AA	F							
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	48	P C	13. DATA STRUCTURES	PP	100	40	41	P C							
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	46	P C							
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	55	P C	15. COMPUTER ORGANIZATION	PP	100	40	AA	F							
06. PROGRAMMING LABORATORY	TW	25	10	16	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	34	P C							
07. PROGRAMMING LABORATORY	PR	50	20	24	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	20	P C							
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	16	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	20	P C							
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	31	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	AA	F							
10. SOFT SKILLS	TW	50	20	40	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	20	P C							
						21. DATA STRUCTURES LABORATORY	PR	50	20	AA	F							

GRAND TOTAL = 542/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80054254	VALLARI ANAND				RACHANA		, 71201104J		S80054340		PICT		S80054254
01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENGINEERING MATHEMATICS III	PP	100	40	62	P		
02. PROGRAMMING & PROBLEM SOLVING	PP	100	40	47	P C	12. MICROPROC. & INTERFACING TECHNIQ.	PP	100	40	45	P C		
03. DIGIT. ELECTRONICS & LOGIC DESIGN	PP	100	40	49	P C	13. DATA STRUCTURES	PP	100	40	51	P C		
04. DATA STRUCTURES AND ALGORITHMS	PP	100	40	40	P C	14. COMPUTER GRAPHICS	PP	100	40	40	P C		
05. HUMANITIES AND SOCIAL SCIENCE	PP	100	40	53	P C	15. COMPUTER ORGANIZATION	PP	100	40	40	P C		
06. PROGRAMMING LABORATORY	TW	25	10	20	P C	16. O. O. PROG. & COMP. GRAPH. LAB	TW	50	20	41	P C		
07. PROGRAMMING LABORATORY	PR	50	20	35	P C	17. O. O. PROG. & COMP. GRAPH. LAB	PR	50	20	33	P C		
08. DIGITAL ELECTRONICS LABORATORY	TW	25	10	21	P C	18. MICROPROCESSORS & INTERFACING LAB	TW	50	20	39	P C		
09. DIGITAL ELECTRONICS LABORATORY	PR	50	20	34	P C	19. MICROPROCESSORS & INTERFACING LAB	PR	50	20	23	P C		
10. SOFT SKILLS	TW	50	20	35	P C	20. DATA STRUCTURES LABORATORY	TW	50	20	40	P C		
						21. DATA STRUCTURES LABORATORY	PR	50	20	25	P C		

GRAND TOTAL = 827/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058501	ADARSH KUMAR				KIRAN					, 71200750E					S80058502		PICT					S80058501
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	40	P C											
02. COMPUTER ORGANIZATION	PP	100	40	55	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C											
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C											
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C											
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	49	P C											
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C											
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C											
08. PROGRAMMING LABORATORY	TW	50	20	22	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C											
09. PROGRAMMING LABORATORY	PR	50	20	36	P	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P C											
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C											
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P											

GRAND TOTAL = 731/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80058504	ANUSHKA GHOGALE				NIVEDITA					, 70925337M					S80058709		PICT					S80058504
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P											
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	42	P C											
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	46	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C											
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C											
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	42	P C											
06. DIGITAL LABORATORY	TW	50	20	23	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C											
07. DIGITAL LABORATORY	PR	50	20	32	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P C											
08. PROGRAMMING LABORATORY	TW	50	20	27	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C											
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	20	P C											
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P C											
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	30	P C											

GRAND TOTAL = 680/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80058507	BHALCHIM NILESH LAKSHMAN				JIJABAI					, 71350917B					S80058516		PICT					S80058507
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	AA	F											
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12. COMPUTER GRAPHICS	PP	100	40	AA	F											
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F											
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	AA	F	14. DATA STRUCTURES AND FILES	PP	100	40	AA	F											
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	50	P C											
06. DIGITAL LABORATORY	TW	50	20	30	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C											
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P C											
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	14	P C											
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C											
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P C											
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	32	P											

GRAND TOTAL = 378/1500, RESULT: FAILS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058508	BHALERAO DHAIRYASHEEL RATNAKAR				RENUKA					, 71200790D				S80058517		PICT				S80058508
01. DISCRETE STRUCTURES	PP	100	40	45	P C	11. ENG MATHS III	PP	100	40	49	P									
02. COMPUTER ORGANIZATION	PP	100	40	64	P C	12. COMPUTER GRAPHICS	PP	100	40	45	P C									
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C									
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C									
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	44	P C	15. DATA COMMUNICATION	PP	100	40	56	P C									
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C									
07. DIGITAL LABORATORY	PR	50	20	27	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P C									
08. PROGRAMMING LABORATORY	TW	50	20	37	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	18	P C									
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	39	P C									
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	36	P C									
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P C									

GRAND TOTAL = 795/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80058509	BHOIR KALPESH SHIVRAM				SOMABAI					, 71200796C				S80058520		PICT				S80058509
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P									
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	46	P C									
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C									
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	44	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C									
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	45	P C									
06. DIGITAL LABORATORY	TW	50	20	37	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C									
07. DIGITAL LABORATORY	PR	50	20	27	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P C									
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C									
09. PROGRAMMING LABORATORY	PR	50	20	37	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P C									
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P C									
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	22	P C									

GRAND TOTAL = 739/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

S80058511	BORATE SHRADDHA RAJARAM				KAMAL					, 71350919J				S80058523		PICT				S80058511
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	53	P									
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	47	P C									
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	56	P C									
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	53	P C									
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA COMMUNICATION	PP	100	40	54	P C									
06. DIGITAL LABORATORY	TW	50	20	43	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	22	P C									
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P C									
08. PROGRAMMING LABORATORY	TW	50	20	44	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P C									
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	40	P C									
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	43	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	43	P C									
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C									

GRAND TOTAL = 846/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058513	CHAVAN KAVITA DHANAJI				SHOBHA					, 71350920B			S80058529		PICT			S80058513
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENG MATHS III	PP	100	40	51	P							
02. COMPUTER ORGANIZATION	PP	100	40	73	P C	12. COMPUTER GRAPHICS	PP	100	40	47	P C							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	58	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	53	P C							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	42	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P C							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	50	P C	15. DATA COMMUNICATION	PP	100	40	66	P C							
06. DIGITAL LABORATORY	TW	50	20	39	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	19	P C							
07. DIGITAL LABORATORY	PR	50	20	38	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P C							
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P C							
09. PROGRAMMING LABORATORY	PR	50	20	40	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P C							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	44	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	45	P C							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P C							

GRAND TOTAL = 929/1500, RESULT: FIRST CLASS

ORDN. 1 MARKS :

S80058514	CHOKHAR PRITHVIRAJ BALASAHEB				VIMAL					, 71100770F			S80058665		PICT			S80058514
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	05	F							
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	42	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	07	F							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C							
06. DIGITAL LABORATORY	TW	50	20	23	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P C							
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	27	P C							
08. PROGRAMMING LABORATORY	TW	50	20	29	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C							
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P C							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P C							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	21	P C							

GRAND TOTAL = 604/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80058517	DAHAWAD SUNIL NAMDEO				VANITA					, 71241757F			S80058666		PICT			S80058517
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	27	F							
02. COMPUTER ORGANIZATION	PP	100	40	57	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	54	P C							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	54	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	42	P C	15. DATA COMMUNICATION	PP	100	40	43	P C							
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	13	P C							
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	29	P C							
08. PROGRAMMING LABORATORY	TW	50	20	38	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C							
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	39	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	33	P C							

GRAND TOTAL = 765/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058520	GAIKWAD PRINCE VIJAYKUMAR				PRABHA		, 71200853F		S80058544		PICT		S80058520
01. DISCRETE STRUCTURES	PP	100	40	53	P C	11. ENG MATHS III	PP	100	40	40	P C		
02. COMPUTER ORGANIZATION	PP	100	40	42	P C	12. COMPUTER GRAPHICS	PP	100	40	44	P C		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	52	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	27	F		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	46	P C		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	56	P C		
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P C		
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	37	P C		
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C		
09. PROGRAMMING LABORATORY	PR	50	20	38	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	29	P C		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P C		

GRAND TOTAL = 799/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80058527	HARKIRPAL SINGH				PARAMJEET KAUR		, 71045444K		S80058675		PICT		S80058527
01. DISCRETE STRUCTURES	PP	100	40	25	F	11. ENG MATHS III	PP	100	40	26	F		
02. COMPUTER ORGANIZATION	PP	100	40	52	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	22	F		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	43	P C	14. DATA STRUCTURES AND FILES	PP	100	40	43	P C		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	58	P C	15. DATA COMMUNICATION	PP	100	40	52	P C		
06. DIGITAL LABORATORY	TW	50	20	21	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C		
07. DIGITAL LABORATORY	PR	50	20	21	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P C		
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C		
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	22	P C		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P C		

GRAND TOTAL = 652/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80058529	IRNAK SWAPNIL ASHOK				MEENA		, 71350922J		S80058559		PICT		S80058529
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	11	F		
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	41	P C		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	43	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	49	P C	15. DATA COMMUNICATION	PP	100	40	48	P C		
06. DIGITAL LABORATORY	TW	50	20	25	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C		
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C		
08. PROGRAMMING LABORATORY	TW	50	20	32	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	16	P C		
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P C		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P C		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P C		

GRAND TOTAL = 678/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058531	KAILAS JAHANGIR PAWARA				THAWALI		, 71100842G		, S80058713		PICT		, S80058531
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P C		
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	25	F		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	72	P C	14. DATA STRUCTURES AND FILES	PP	100	40	54	P C		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	43	P C	15. DATA COMMUNICATION	PP	100	40	40	P C		
06. DIGITAL LABORATORY	TW	50	20	41	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C		
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	22	P C		
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P C		
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	38	P C		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	42	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	39	P C		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	29	P C		

GRAND TOTAL = 800/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80058536	KULKARNI ALOK DIPAK				GEETA		, 71045491M		, S80058679		PICT		, S80058536
01. DISCRETE STRUCTURES	PP	100	40	AA	F	11. ENG MATHS III	PP	100	40	AA	F		
02. COMPUTER ORGANIZATION	PP	100	40	AA	F	12. COMPUTER GRAPHICS	PP	100	40	AA	F		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	AA	F	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	AA	F		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F		
06. DIGITAL LABORATORY	TW	50	20	22	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	15	P C		
07. DIGITAL LABORATORY	PR	50	20	33	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F		
08. PROGRAMMING LABORATORY	TW	50	20	35	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C		
09. PROGRAMMING LABORATORY	PR	50	20	35	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	29	P C		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	37	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P C		

GRAND TOTAL = 381/1500, RESULT: FAILS

ORDN. 1 MARKS :

RESULT RESERVED FOR BKLG

S80058538	MANSI MATHUR				PREETI		, 71100890G		, S80058680		PICT		, S80058538
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P		
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	46	P C		
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	54	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	42	P C		
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	51	P C		
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	53	P C	15. DATA COMMUNICATION	PP	100	40	46	P C		
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	14	P C		
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	28	P C		
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	13	P C		
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	42	P C		
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	35	P C		
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P C		

GRAND TOTAL = 765/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058539 MITRA ANSHUMAN SUDEEP MALINI , 71100897D , S80058594 , PICT , S80058539

01. DISCRETE STRUCTURES	PP	100	40	54	P C	11. ENG MATHS III	PP	100	40	19	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	50	P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	56	P C	15. DATA COMMUNICATION	PP	100	40	44	P C
06. DIGITAL LABORATORY	TW	50	20	22	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	26	P
08. PROGRAMMING LABORATORY	TW	50	20	21	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	30	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	20	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	25	P

GRAND TOTAL = 656/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

S80058542 MUNOT PAYAL KAMLESHKUMAR MEENAKSHI , 70925517K , S80058718 , PICT , S80058542

01. DISCRETE STRUCTURES	PP	100	40	51	P C	11. ENG MATHS III	PP	100	40	AA	F
02. COMPUTER ORGANIZATION	PP	100	40	40	P C	12. COMPUTER GRAPHICS	PP	100	40	AA	F
03. DIGITAL ELECTRONICS & LOGIC DESIGPP	PP	100	40	50	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	AA	F
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	11	P C
07. DIGITAL LABORATORY	PR	50	20	20	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	AA	F
08. PROGRAMMING LABORATORY	TW	50	20	33	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	27	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	40	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	41	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	AA	F

GRAND TOTAL = 508/1500, RESULT: FAILS

ORDN. 1 MARKS :

S80058544 NILESH D PHADTARE KUNDA , 70925526J , S80058688 , PICT , S80058544

01. DISCRETE STRUCTURES	PP	100	40	43	P C	11. ENG MATHS III	PP	100	40	40	P C
02. COMPUTER ORGANIZATION	PP	100	40	50	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P C
03. DIGITAL ELECTRONICS & LOGIC DESIGPP	PP	100	40	50	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	AA	F
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	72	P C
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	43	P C
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	20	P C
07. DIGITAL LABORATORY	PR	50	20	31	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C
08. PROGRAMMING LABORATORY	TW	50	20	43	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	23	P C
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P C
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	36	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	44	P C
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	41	P C

GRAND TOTAL = 771/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058548	PAWAR KAVITA SANDIPAN				SHEVANTA			, 71350932F		S80058616		PICT		S80058548
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	05	F			
02. COMPUTER ORGANIZATION	PP	100	40	47	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	44	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	28	F	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	64	P C	15. DATA COMMUNICATION	PP	100	40	56	P C			
06. DIGITAL LABORATORY	TW	50	20	32	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	16	P C			
07. DIGITAL LABORATORY	PR	50	20	22	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C			
08. PROGRAMMING LABORATORY	TW	50	20	30	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P C			
09. PROGRAMMING LABORATORY	PR	50	20	22	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	22	P C			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	32	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	30	P C			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	20	P			

GRAND TOTAL = 667/1500, RESULT: FAILS A.T.K.T. [\$ 0.1]

ORDN. 1 MARKS : (17)2,

S80058549	PAWAR NEHA ANIL				JAYA			, 71100949L		S80058692		PICT		S80058549
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P C			
02. COMPUTER ORGANIZATION	PP	100	40	45	P C	12. COMPUTER GRAPHICS	PP	100	40	55	P C			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	50	P C	14. DATA STRUCTURES AND FILES	PP	100	40	44	P C			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	51	P C	15. DATA COMMUNICATION	PP	100	40	42	P C			
06. DIGITAL LABORATORY	TW	50	20	35	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C			
07. DIGITAL LABORATORY	PR	50	20	34	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	24	P C			
08. PROGRAMMING LABORATORY	TW	50	20	34	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	15	P C			
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	37	P C			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	38	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P C			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	40	P			

GRAND TOTAL = 774/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80058554	SAPKALE SNEHAL SURESH				USHA			, 71201052B		S80058634		PICT		S80058554
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	80	P			
02. COMPUTER ORGANIZATION	PP	100	40	48	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P			
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	25	F			
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	45	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C			
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	40	P C			
06. DIGITAL LABORATORY	TW	50	20	31	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C			
07. DIGITAL LABORATORY	PR	50	20	35	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	25	P C			
08. PROGRAMMING LABORATORY	TW	50	20	25	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	17	P C			
09. PROGRAMMING LABORATORY	PR	50	20	30	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	25	P C			
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	34	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	34	P C			
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P C			

GRAND TOTAL = 734/1500, RESULT: FAILS A.T.K.T.

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058560	SHUKLA MANISH NITYANAND				MADHURI					, 71045622M			S80058711		PICT			S80058560
01. DISCRETE STRUCTURES	PP	100	40	57	P C	11. ENG MATHS III	PP	100	40	55	P							
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	57	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	51	P C							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	49	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	45	P C	15. DATA COMMUNICATION	PP	100	40	45	P C							
06. DIGITAL LABORATORY	TW	50	20	28	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	12	P C							
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	21	P C							
08. PROGRAMMING LABORATORY	TW	50	20	28	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	11	P C							
09. PROGRAMMING LABORATORY	PR	50	20	32	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P C							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	31	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P C							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	37	P C							

GRAND TOTAL = 768/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80058561	SOLANKE BHUSHAN NARENDRA				REKHA					, 71101017L			S80058700		PICT			S80058561
01. DISCRETE STRUCTURES	PP	100	40	44	P C	11. ENG MATHS III	PP	100	40	40	P							
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	43	P C							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	46	P C							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	48	P C	14. DATA STRUCTURES AND FILES	PP	100	40	57	P C							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	46	P C							
06. DIGITAL LABORATORY	TW	50	20	33	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C							
07. DIGITAL LABORATORY	PR	50	20	30	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P C							
08. PROGRAMMING LABORATORY	TW	50	20	31	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	12	P C							
09. PROGRAMMING LABORATORY	PR	50	20	24	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	35	P C							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	35	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	33	P C							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	34	P C							

GRAND TOTAL = 754/1500, RESULT: SECOND CLASS

ORDN. 1 MARKS :

S80058562	TEJASH KUMAR				RITA					, 71045642F			S80058703		PICT			S80058562
01. DISCRETE STRUCTURES	PP	100	40	40	P C	11. ENG MATHS III	PP	100	40	40	P							
02. COMPUTER ORGANIZATION	PP	100	40	44	P C	12. COMPUTER GRAPHICS	PP	100	40	40	P C							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	40	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	40	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	40	P C	15. DATA COMMUNICATION	PP	100	40	44	P C							
06. DIGITAL LABORATORY	TW	50	20	24	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	10	P C							
07. DIGITAL LABORATORY	PR	50	20	23	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	20	P C							
08. PROGRAMMING LABORATORY	TW	50	20	20	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	10	P C							
09. PROGRAMMING LABORATORY	PR	50	20	20	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	30	P C							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	33	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	27	P C							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	28	P C							

GRAND TOTAL = 653/1500, RESULT: PASS CLASS

ORDN. 1 MARKS :

NOTE: FIRST LINE : SEAT NO., NAME OF THE CANDIDATE, MOTHER, PERMANENT REG. NO., PREVIOUS SEAT NO., COLLEGE, SEAT NO.

OTHER LINES: HEAD OF PASSING, MAX. MARKS, MIN. PASS MARKS, MARKS OBTAINED, P/F:PASS/FAIL, C:PREVIOUS CARRY OVER

S80058569	ZANJARE SHRIDATTA GOVIND				PRAMILA				, 71201125M				, S80058653		PICT			, S80058569
01. DISCRETE STRUCTURES	PP	100	40	48	P C	11. ENG MATHS III	PP	100	40	51	P							
02. COMPUTER ORGANIZATION	PP	100	40	43	P C	12. COMPUTER GRAPHICS	PP	100	40	51	P C							
03. DIGITAL ELECTRONICS & LOGIC DESIG	PP	100	40	49	P C	13. PROCESSOR ARCHITECTURE & INTER.	PP	100	40	40	P C							
04. FUNDAMENTAL OF DATA STRUCTURES	PP	100	40	46	P C	14. DATA STRUCTURES AND FILES	PP	100	40	40	P C							
05. HUMANITIES AND SOCIAL SCIENCES	PP	100	40	68	P C	15. DATA COMMUNICATION	PP	100	40	54	P C							
06. DIGITAL LABORATORY	TW	50	20	42	P C	16. PROCESSOR INTERFACING LABORATORY	TW	25	10	18	P C							
07. DIGITAL LABORATORY	PR	50	20	27	P C	17. PROCESSOR INTERFACING LABORATORY	OR	50	20	30	P C							
08. PROGRAMMING LABORATORY	TW	50	20	42	P C	18. DATA STRUCTURES AND FILES LAB	TW	25	10	20	P C							
09. PROGRAMMING LABORATORY	PR	50	20	25	P C	19. DATA STRUCTURES AND FILES LAB	PR	50	20	28	P C							
10. COMMUNICATION AND LANGUAGE LAB.	TW	50	20	46	P C	20. OBJECT ORIENTED PROGRAMMING LAB	TW	50	20	40	P C							
						21. OBJECT ORIENTED PROGRAMMING LAB	PR	50	20	35	P C							

GRAND TOTAL = 843/1500, RESULT: HIGHER SECOND CLASS

ORDN. 1 MARKS :