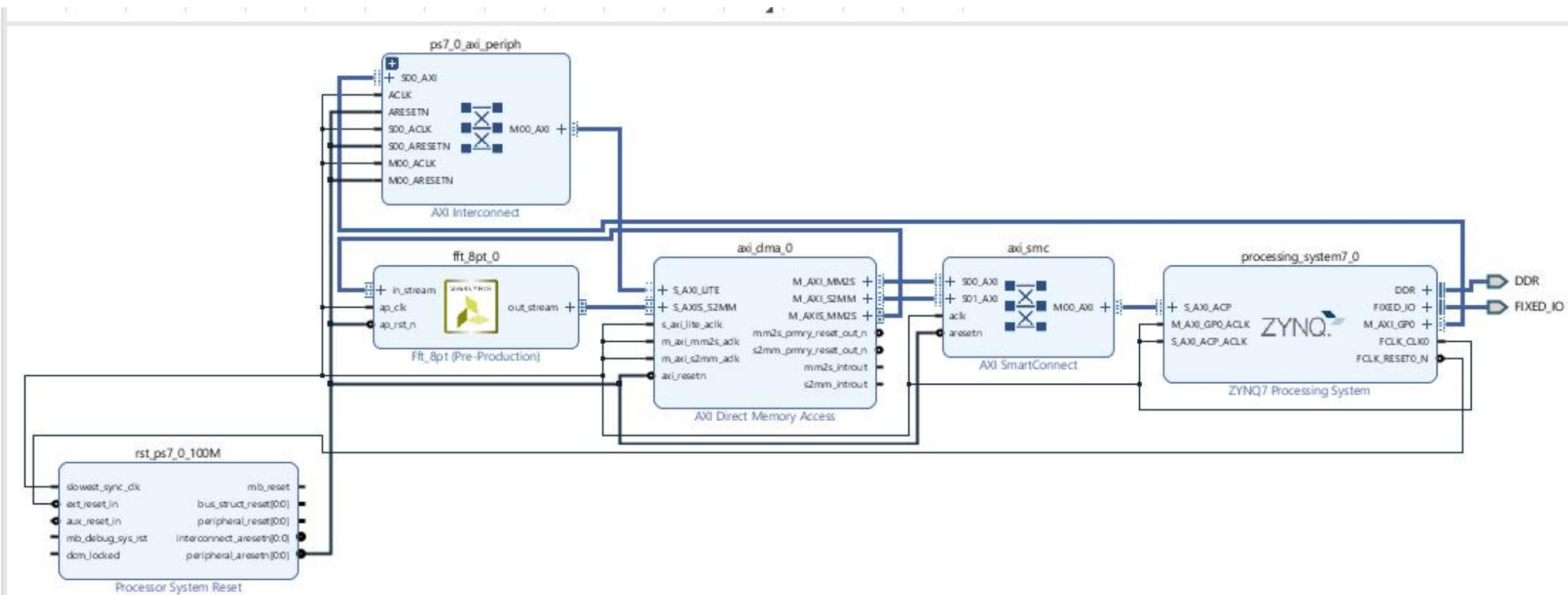


# Block Design and Output of 8 point FFT



## Terminal requirements :

(i) Processor's STDOUT is redirected to the ARM DCC/MDM UART

(ii) Processor's STDIN is redirected to the ARM DCC/MDM UART.

Then, text input from this console will be sent to DCC/MDM's UART port.

NOTE: This is a line-buffered console and you have to press "Enter"  
to send a string of characters to DCC/MDM.

PS Output: 305.000000+190.000000I, PL Output: 305.000000+190.000000I DMA Transfer Successful!

PS Output: -103.284271+27.071068I, PL Output: -103.284271+27.071068I DMA Transfer Successful!

PS Output: -25.000000+-10.000000I, PL Output: -25.000000+-10.000000I DMA Transfer Successful!

PS Output: -53.284271+-42.928932I, PL Output: -53.284271+-42.928932I DMA Transfer Successful!

PS Output: 45.000000+10.000000I, PL Output: 45.000000+10.000000I DMA Transfer Successful!

PS Output: -46.715729+12.928932I, PL Output: -46.715729+12.928932I DMA Transfer Successful!

PS Output: -45.000000+-90.000000I, PL Output: -45.000000+-90.000000I DMA Transfer Successful!

PS Output: 3.284271+-57.071068I, PL Output: 3.284271+-57.071068I DMA Transfer Successful!

----- Execution Time Comparison -----

Execution time for PS in Microseconds: 4.713846

Execution time for PL in Microseconds: 3.547692|