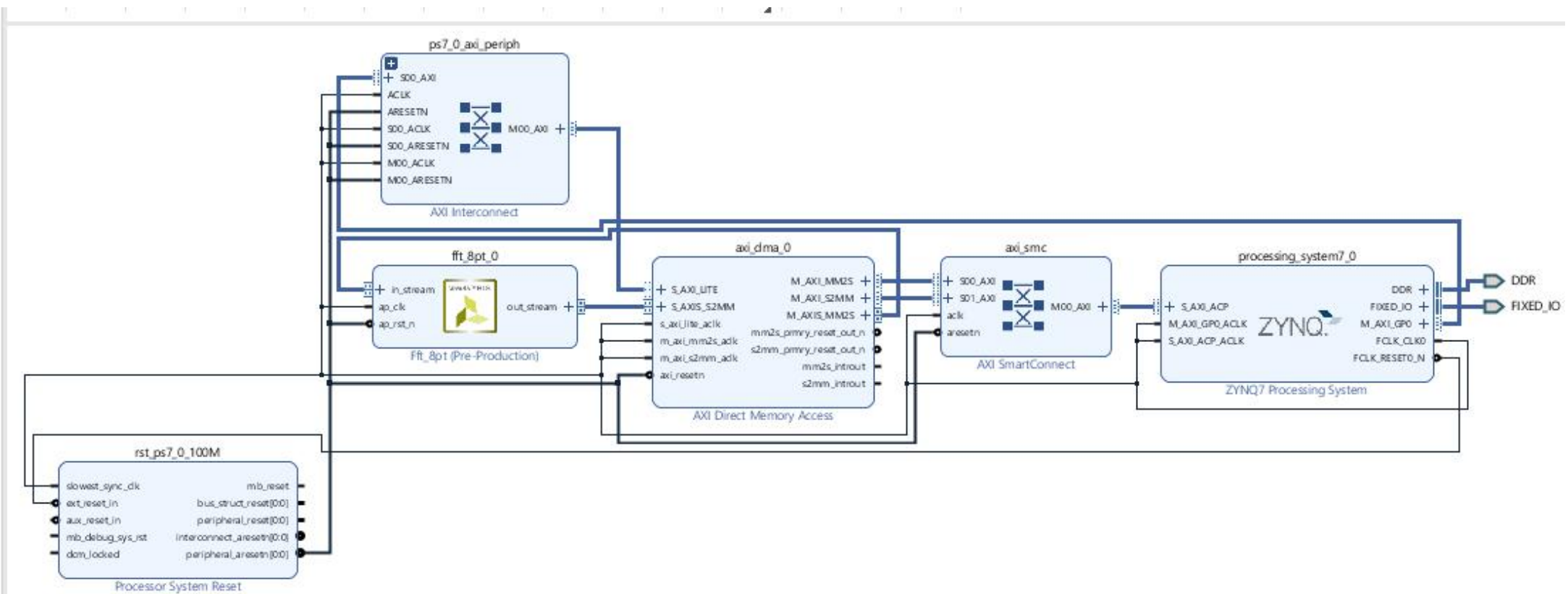


Block Design of 8 point FFT



Output Of the 8 point FFT

```
C:\Xilinx\SDK\2019.1\bin\unw x + v
Terminal requirements :
(i) Processor's STDOUT is redirected to the ARM DCC/MDM UART
(ii) Processor's STDIN is redirected to the ARM DCC/MDM UART.
Then, text input from this console will be sent to DCC/MDM's UART port.
NOTE: This is a line-buffered console and you have to press "Enter"
to send a string of characters to DCC/MDM.

PS Output: 385.000000+379.000000I, PL Output: 385.000000+379.000000I DMA Transfer Successful!
PS Output: 62.920311+-44.665474I, PL Output: 62.920311+-44.665474I DMA Transfer Successful!
PS Output: -234.000000+-4.000000I, PL Output: -234.000000+-4.000000I DMA Transfer Successful!
PS Output: -122.192383+-36.280701I, PL Output: -122.192383+-36.280701I DMA Transfer Successful!
PS Output: 105.000000+81.000000I, PL Output: 105.000000+81.000000I DMA Transfer Successful!
PS Output: 19.079691+-91.334526I, PL Output: 19.079691+-91.334526I DMA Transfer Successful!
PS Output: -24.000000+20.000000I, PL Output: -24.000000+20.000000I DMA Transfer Successful!
PS Output: -103.807617+-119.719299I, PL Output: -103.807617+-119.719299I DMA Transfer Successful!

----- Execution Time Comparison -----
Execution time for PS in Microseconds: 4.630769
Execution time for PL in Microseconds: 3.532308|
```