Design of an interleaving DC/DC converter :

Given data:

Input voltage = 45-60V

Output Voltage = 3V

Output Current = 100A

Efficiency > 85%

Assumptions:

Switching Frequency = 20000H

Duty ratio = 0.5

Calculations:

For this design we are using the Forward converter formulas:

1) Duty cycle:

Vout/Vi = D.(N2/N1)

Let's assume that we will design for 50% Duty ratio:

N2/N1 = 0.1

These we choose the following:

N1 = 500

N2 = 50

2) Since we choose D = 0.5 then using equation we found that,

$$D\left(1+\frac{N_3}{N_1}\right)<1$$

$$thus\frac{N_3}{N_1}<1$$

Thus we will choose,

N3 = 150turns

3) Now, we will choose an inductor for 40% current ripple:

$$R = \frac{V_{out}^2}{P} = 0.03$$

$$IL = \frac{V_o}{R} = 100A$$

$$L = \frac{V_{out}}{F_s \cdot \Delta Il} \cdot (1 - D)$$

$$L = 1.5e^{-6}$$

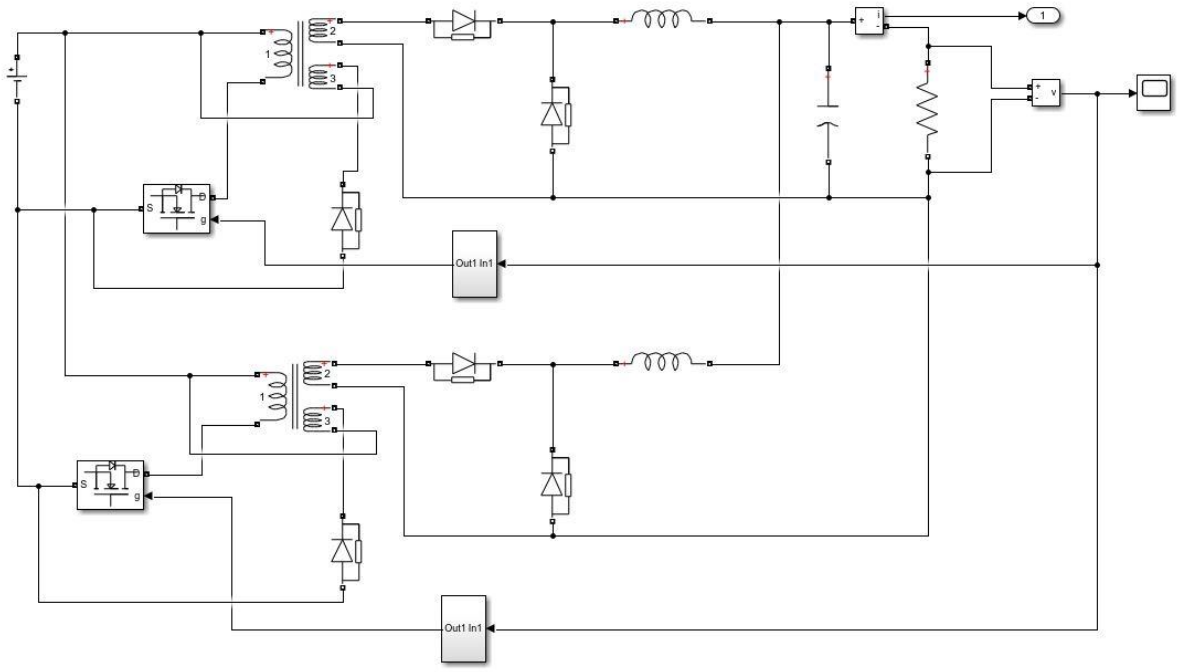
4) Now we are calculating the capacitor:

$$C = \frac{1 - D}{8L \cdot FS^2\left(\frac{\Delta V_O}{V_o}\right)}$$

$$C = 20.83e^{-6}$$

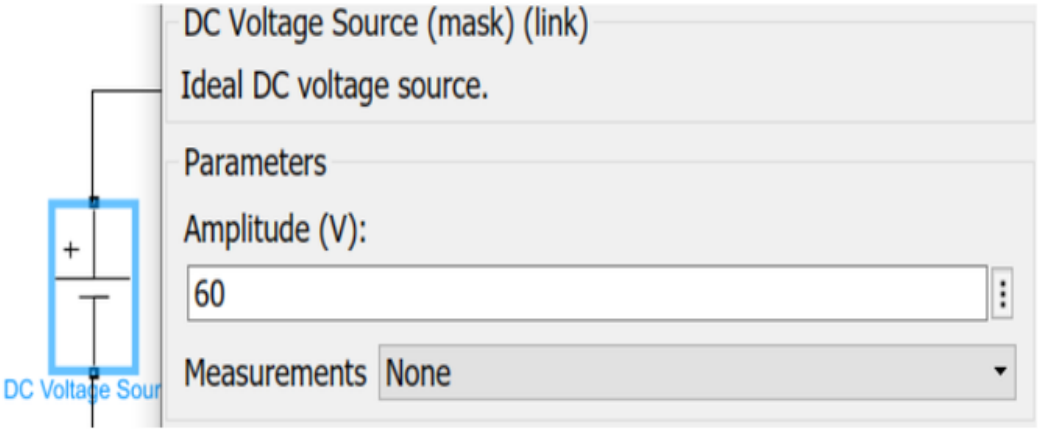
By parallelling the converters the input current can be shared among inductors so that high reliability and efficiency in power electronic systems can be obtained. In this paper, interleaved buck dc-dc converter is simulated using matlab/Simulink software. The converters are tested by varying the in/out voltage with a constant duty cycle in continuous conduction mode. The performance parameters of the converters are compared. The converters are controlled by interleaved switching signals, which have the same switching frequency but the shift in phase. Using interleaved converter we can get improved efficiency, reduced ripple voltage, reduced inductor current ripple, fast switching speed.

As for high efficiency we used multiphase system to increase the efficiency

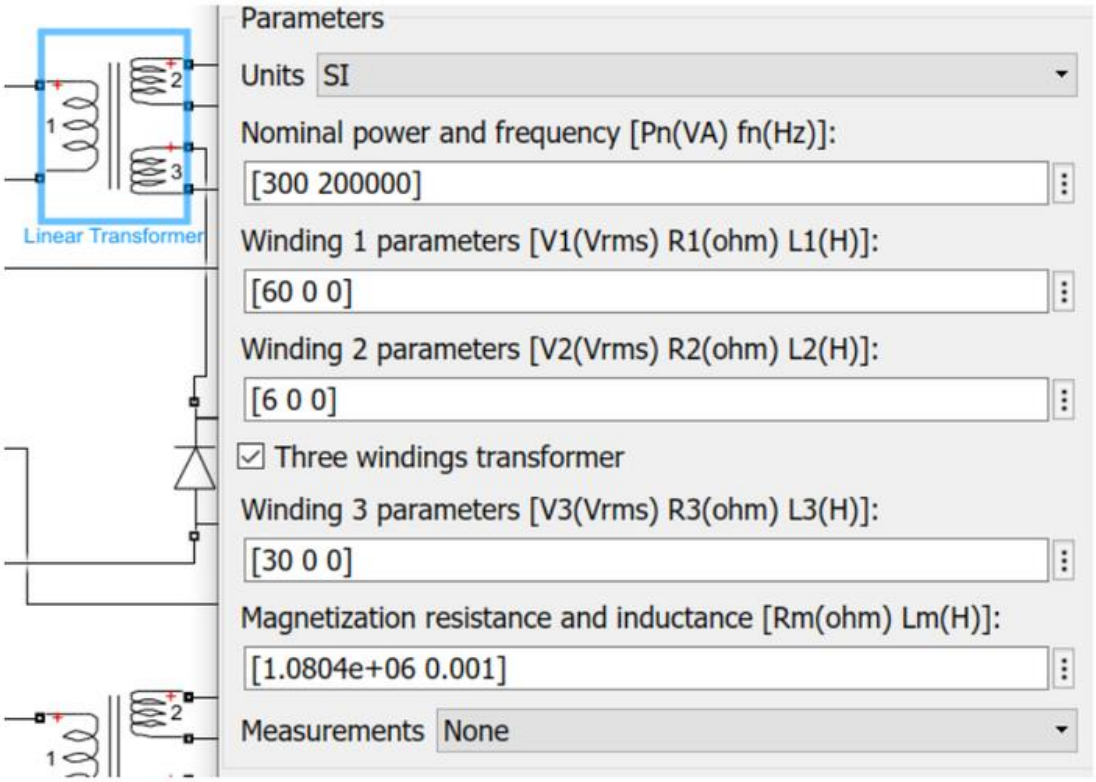


In this model we are using the following parameters which we calculated.

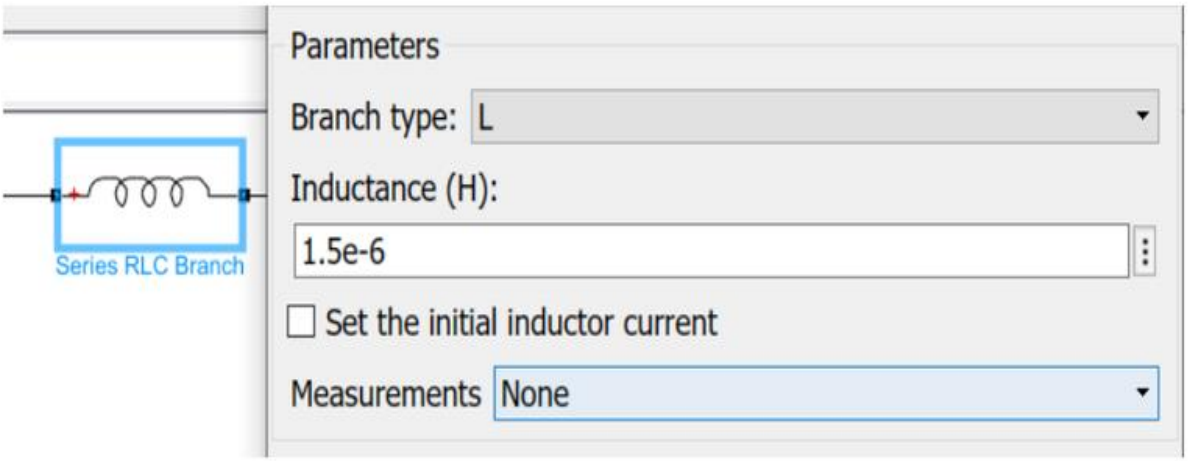
1) Input Voltage:



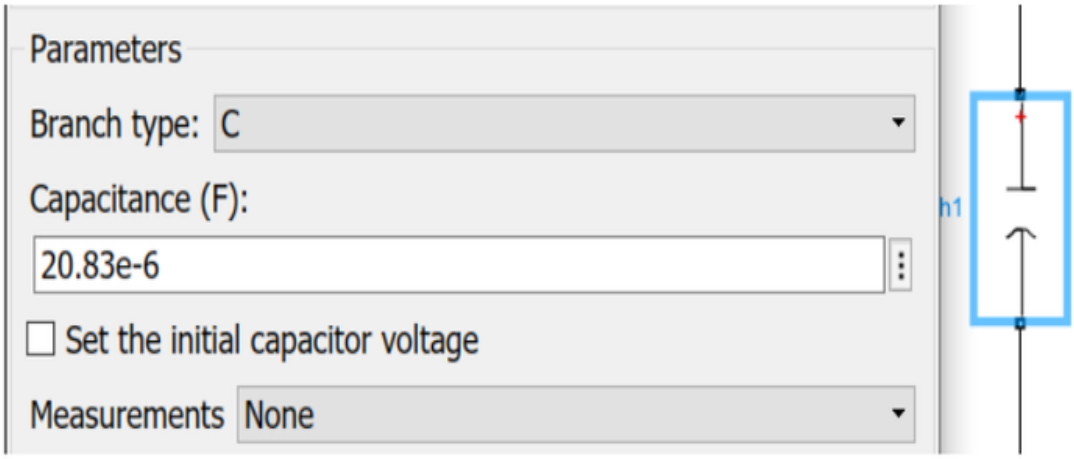
2)Transformer Capacity:



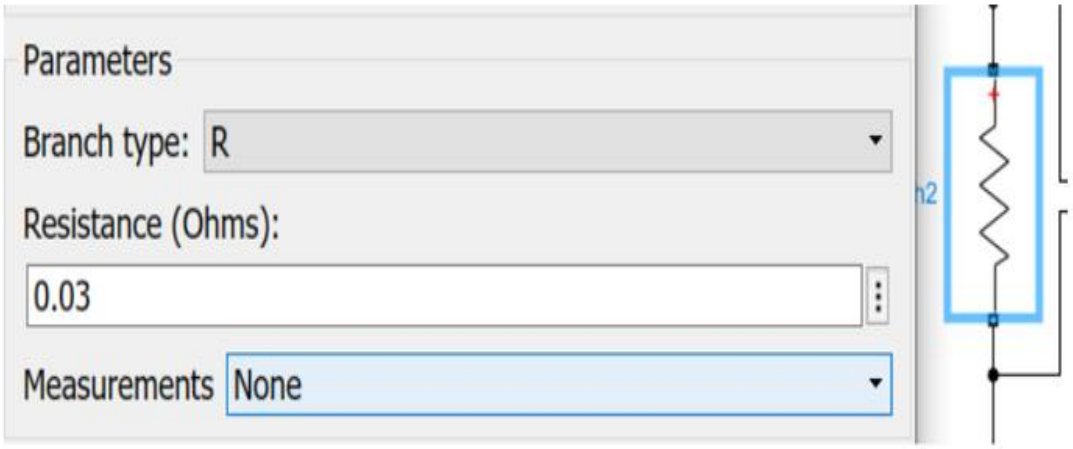
3) Inductor Value:



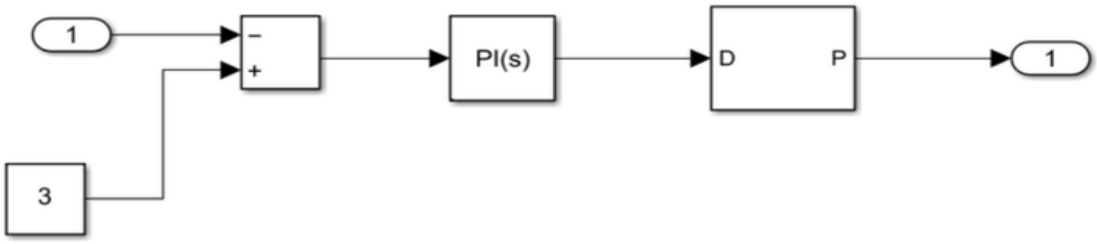
4) Capacitor:



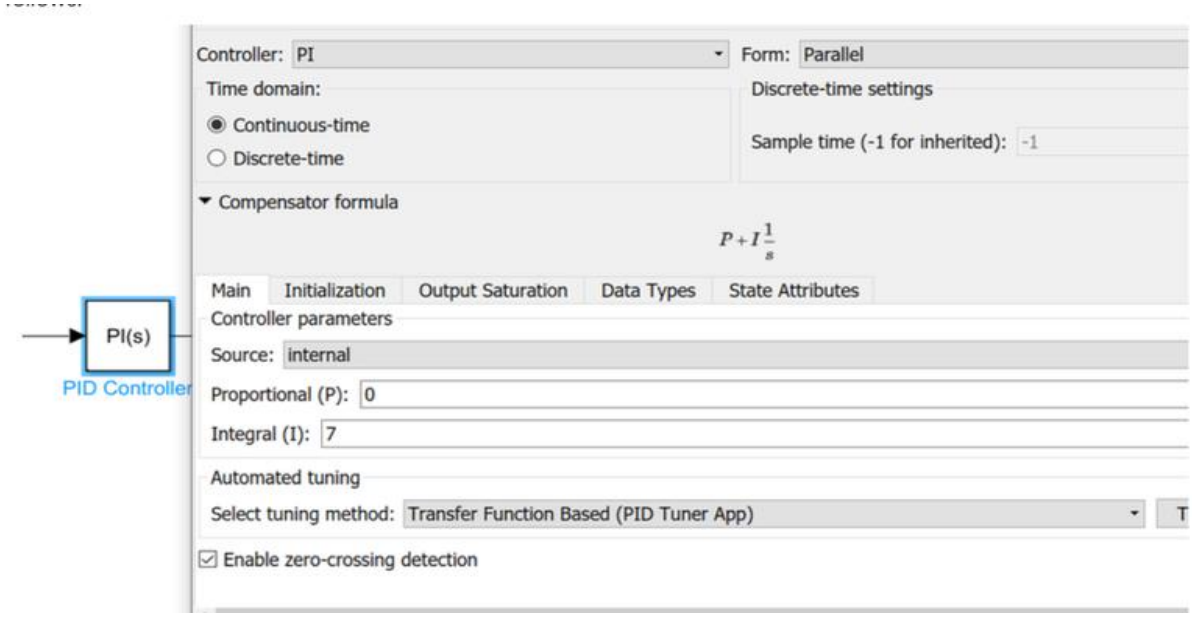
5) Output Load:



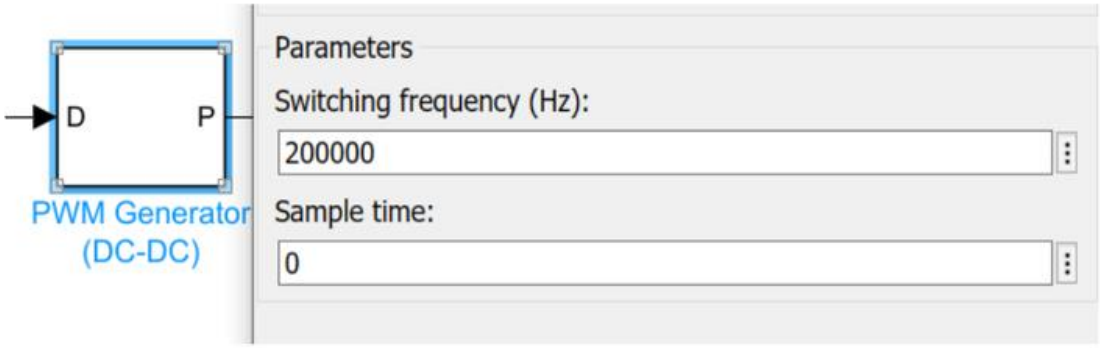
6) Gate Pulse:



As we required the 3V so we were given the reference as the constant 3 and PID get the tuned so PID value are as follows:

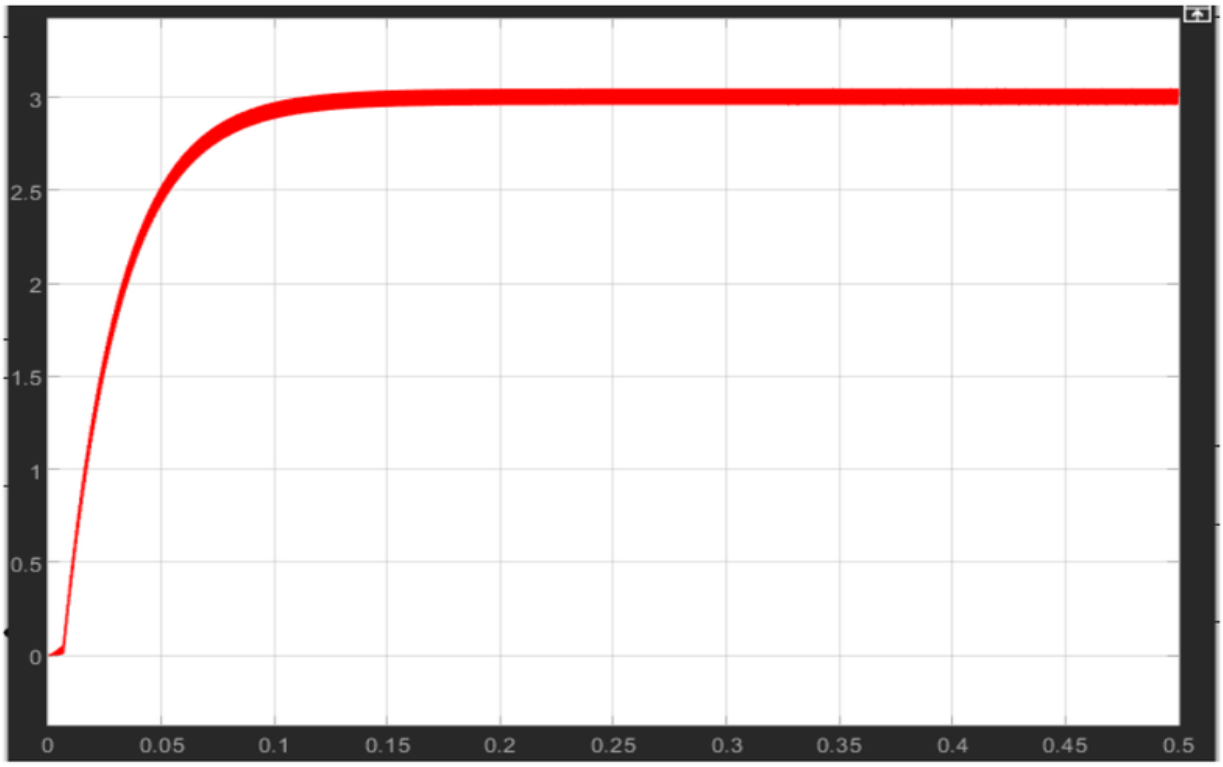


The frequency is chosen is 20000hz:



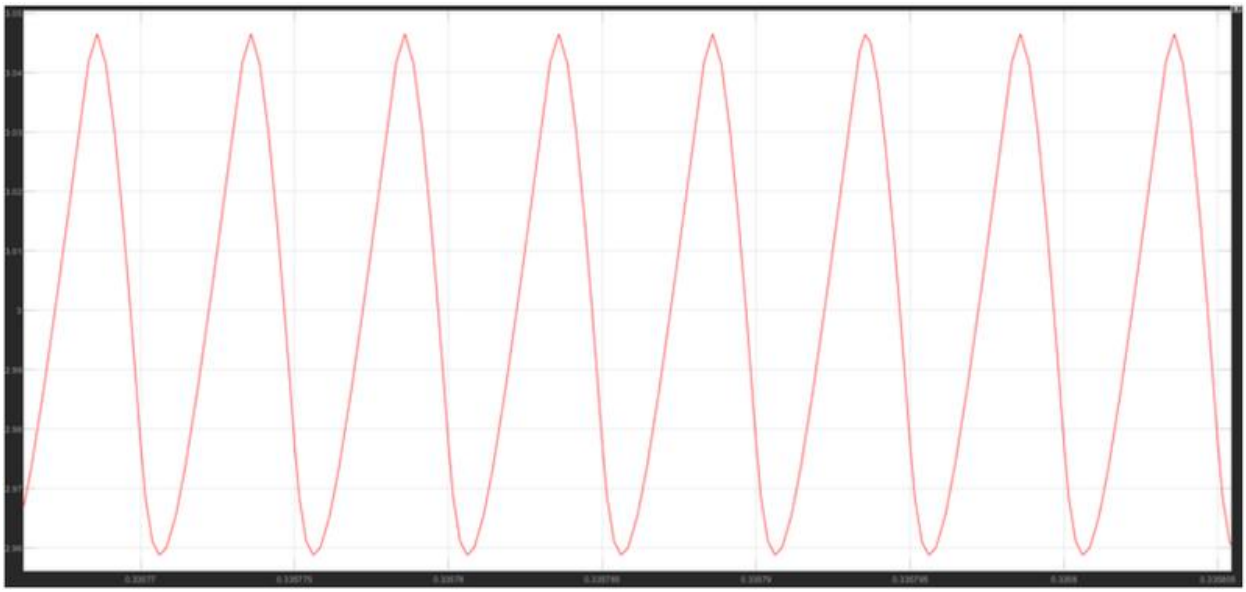
Now we run the model for 0.5 sec and get all outputs:

Output Voltage:



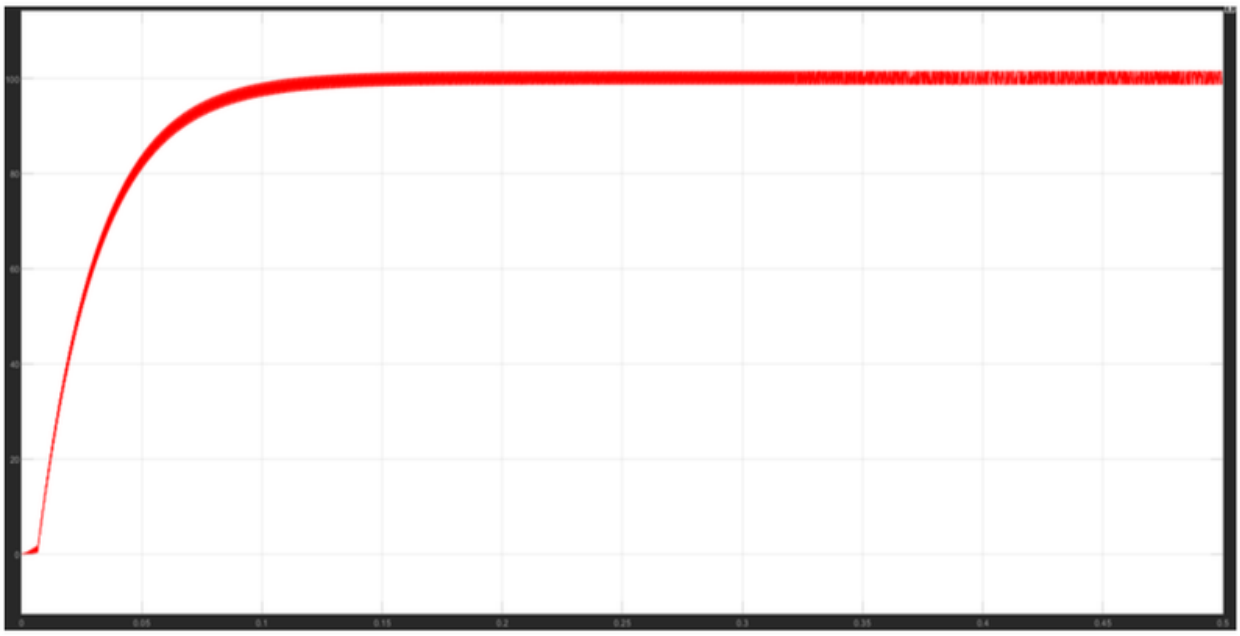
As we output voltage is getting 3 Volt same as we required with some ripples and ripple also same as we calculated which is shown in the following graph.

The ripple of output voltage:



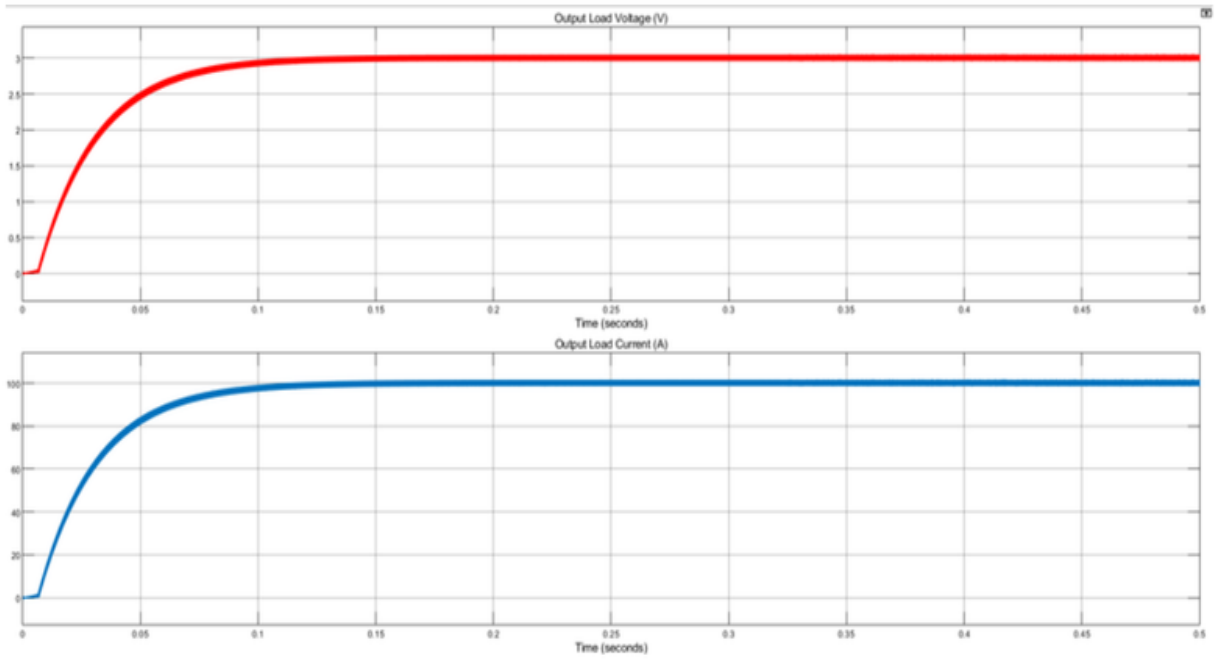
Output Current:

Current is 100A this is because of the isolated system

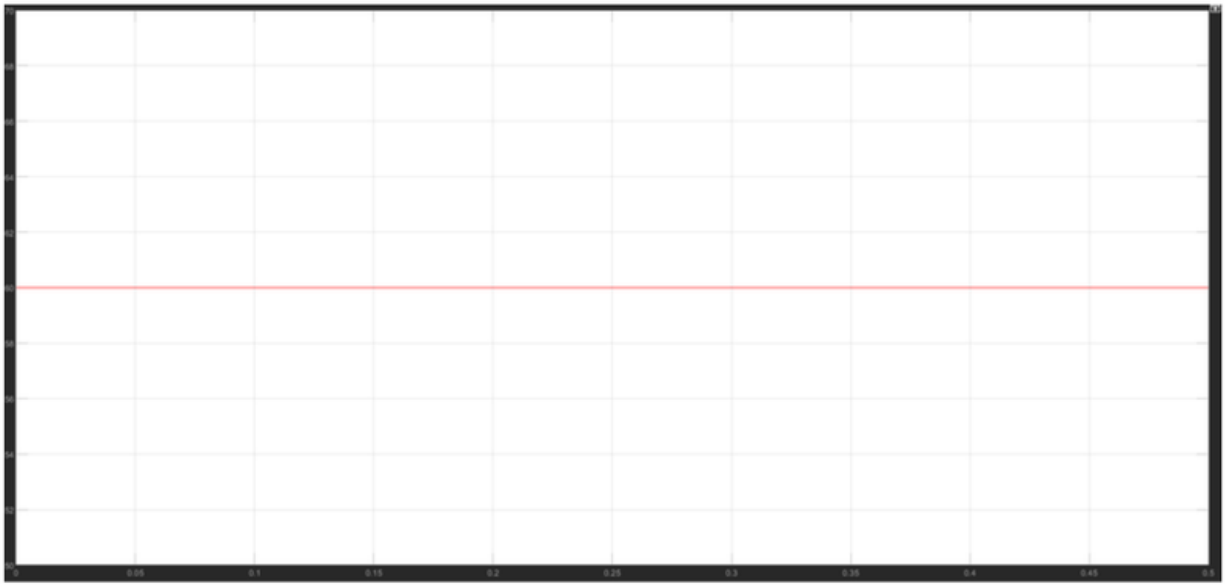


and multiphase system getting the perfect as same as requirements:

Output voltage and current:

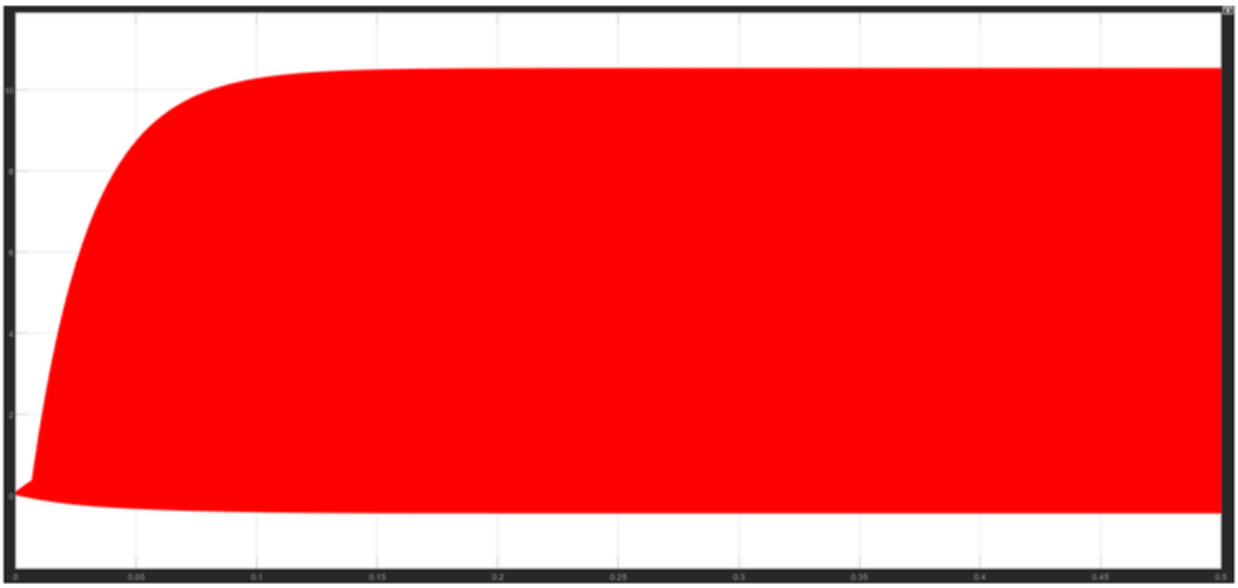


Input Voltage:

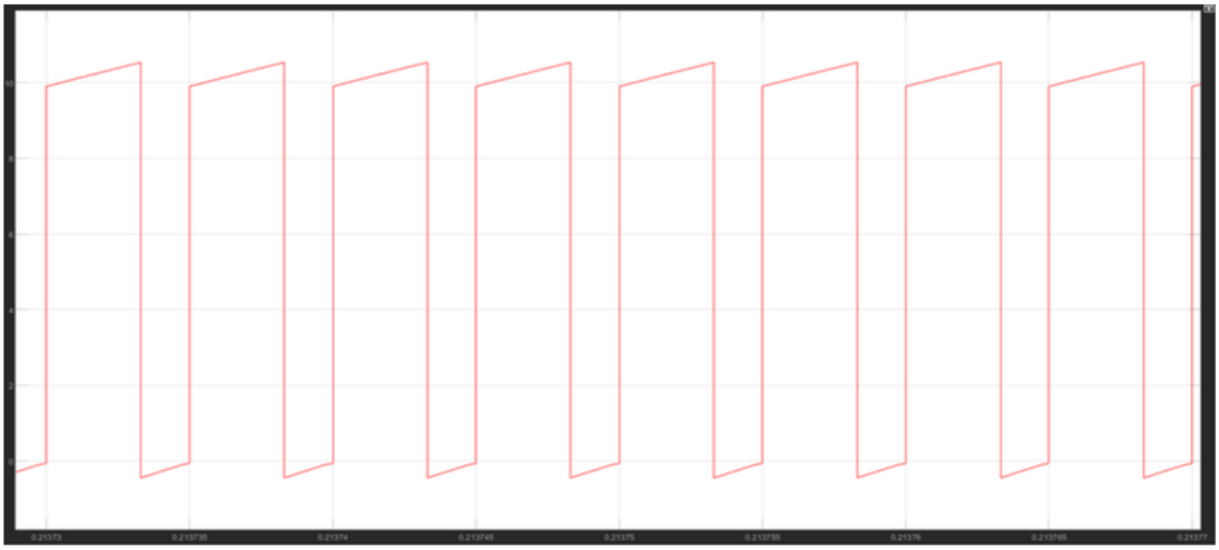


As our input voltage is 60 V given the converter.

Input current:

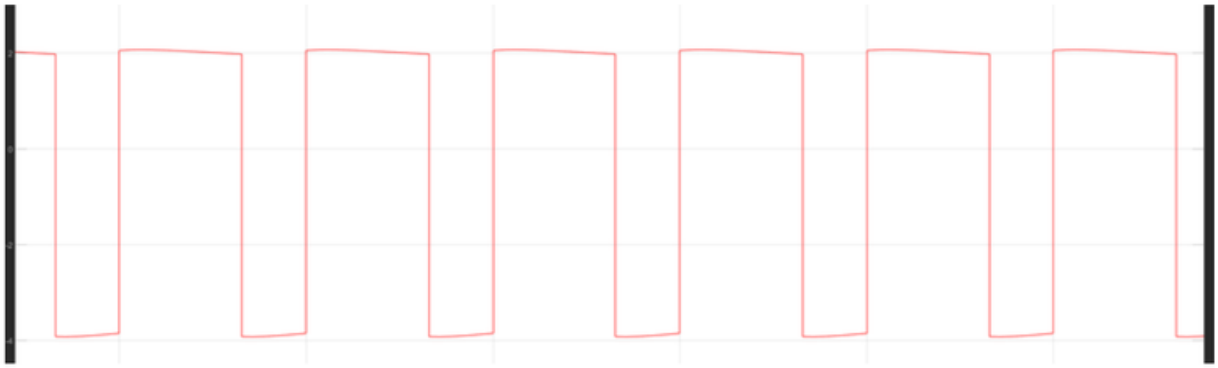


Because of switching action of MOSFET input current is looking like this if Zoom it, it will look like step voltage which is shown in the following graph



The current amplitude is max is 10volt and the minimum voltage is near to zero so as calculate the average value then ware getting 4.7A approximately.

Inductor Voltage:



Voltage is as plus and magnitude are 2V and -4V.

1. The main aim is to get the efficiency as higher as possible and as you see in our requirement are getting approximately same.

so we can say that our efficiency is above/more than 90%.

1. As efficiency is calculated from output power/input power so as we calculated for this converter as following:

Input Voltage = 60V

Input Current = 5A

Output Voltage = 3V

Output Current = 100A
from all the above data we get the input power is 300w and output power is also 300W

So our efficiency of 100% without considering the ripple in voltage and current, if we consider the ripple in voltage and current, if we consider the ripple also we can get the efficiency more than 90%.

Conclusion:

Those we designed the DC/DC converter the given data and get the required output with more than 90% efficiency.