



SAVAITRAREAL NITTECH TECHNOLOGICAL UNIVERSITY

HYDERABAD - 500 072.
ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



IM.No : A071629

Bachelor of Technology in ELECTRICAL & ELECTRONICS ENGINEERING

Registration No. : 623218786301
Name : NAGA RAJA KUMAR VEERANKI

Name of the College: N G I T, VIJAYAWADA

Month & Year of Final Exam: April, 2006

Roll Ticket No. : 03505A0204 Year of Admission 2003-2004

Class Awarded: FIRST CLASS WITH DISTINCTION

COURSE TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL	CREDITS	S.No.	COURSE TITLE	INTERNAL MARKS	EXTERNAL MARKS	TOTAL	CREDITS
THEORY Max.	20	80	100			THEORY Max.	20	80	100	
LAB	25	50	75			LAB	25	50	75	

I YEAR

DIRECT ADMISSION INTO II-YEAR UNDER LATERAL ENTRY SCHEME

I SEMESTER

II YEAR

II SEMESTER

1 MATHEMATICS - II	17	78	95	4	1 MANAGERIAL ECONOMICS & FINANCIAL A	19	44	63	4
2 HYDRAULICS & HYDRAULIC MACHINERY	17	65	82	4	2 MATHEMATICS - III	19	60	79	4
3 PULSE & DIGITAL CIRCUITS	18	34	52	4	3 LINEAR & DIGITAL IC APPLICATIONS	19	53	72	4
4 SWITCHING THEORY & LOGIC DESIGN	18	68	86	4	4 POWER SYSTEMS - I	20	51	71	4
5 ELECTROMAGNETIC FIELDS	16	54	70	4	5 ELECTROMECHANICS - II	19	65	84	4
6 ELECTRO MECHANICS - I	19	76	95	4	6 CONTROL SYSTEMS	17	58	75	4
7 HYDRAULICS & HYDRAULIC MACHINERY (23	40	63	2	7 IC & PULSE & DIGITAL CIRCUITS (LAB)	21	46	67	2
8 NETWORKS (LAB)	24	46	70	2	8 ELECTROMECHANICS (LAB) - I	25	49	74	2

I SEMESTER

III YEAR

II SEMESTER

1 COMPUTER ORGANIZATION	15	43	58	4	1 ANALYSIS OF LINEAR SYSTEMS	18	60	78	4
2 ELECTRICAL MEASUREMENTS	19	70	89	4	2 MICROPROCESSORS AND INTERFACING	14	28	42	4
3 POWER SYSTEMS-II	19	68	87	4	3 MODELLING OF POWER SYSTEM COMPONENTS	17	49	66	4
4 POWER ELECTRONICS	13	62	75	4	4 INSTRUMENTATION	15	48	63	4
5 ELECTROMECHANICS-III	18	73	91	4	5 POWER SYSTEMS - III	19	48	67	4
6 MANAGEMENT SCIENCE	16	48	64	4	6 UTILIZATION OF ELECTRICAL ENERGY	17	55	72	4
7 ELECTROMECHANICS (LAB) - II	24	49	73	2	7 ELECTRICAL MEASUREMENTS LAB	24	46	70	2
8 CONTROL SYSTEMS (LAB)	24	46	70	2	8 POWER ELECTRONICS LAB	24	46	70	2

I SEMESTER

IV YEAR

II SEMESTER

1 DIGITAL SIGNAL PROCESSING	16	44	60	4	1 EMBEDDED SYSTEMS	16	49	65	4
2 POWER SEMICONDUCTOR DRIVES	14	54	68	4	2 DATABASE MANAGEMENT SYSTEMS	19	35	54	4
3 COMPUTER METHODS IN POWER SYS.	18	48	66	4	3 PROJECT & SEMINAR #	38	155	193	8
4 POWER SYSTEM OPERATION & CONTROL	17	54	71	4					
5 NON-CONVENTIONAL ENERGY SOURCES	17	50	67	4					
6 NEURAL NETWORKS & APPLICATIONS	16	45	61	4					
7 MICROPROCESSORS LAB	23	41	64	2					
8 SIMULATION OF ELEC. SYS. LAB	24	47	71	2					

Number of Credits registered for : 156

(# Project Internal = 40, External = 160)

Aggregate Marks Secured for best 156 credits :

3173 OUT OF 4150 (76.46 %)

Date of Declaration of Result May 16, 2006

See overleaf for instruments)

(*Courses registered for but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS