

7

SERIES-PARALLEL CIRCUITS

CHAPTER OUTLINE

- 7-1 Identifying Series-Parallel Relationships
- 7-2 Analysis of Series-Parallel Resistive Circuits
- 7-3 Voltage Dividers with Resistive Loads
- 7-4 Loading Effect of a Voltmeter
- 7-5 Ladder Networks
- 7-6 The Wheatstone Bridge
- 7-7 Troubleshooting
- Application Activity

CHAPTER OBJECTIVES

- ▶ Identify series-parallel relationships
- ▶ Analyze series-parallel circuits
- ▶ Analyze loaded voltage dividers
- ▶ Determine the loading effect of a voltmeter on a circuit
- ▶ Analyze ladder networks
- ▶ Analyze and apply a Wheatstone bridge
- ▶ Troubleshoot series-parallel circuits

KEY TERMS

- ▶ Bleeder current
- ▶ Wheatstone bridge
- ▶ Balanced bridge
- ▶ Unbalanced bridge

APPLICATION ACTIVITY PREVIEW

In this application activity, you will see how a Wheatstone bridge in conjunction with a thermistor can be used in a temperature-control application. The circuit in this application is designed to turn a heating element on and off in order to keep the temperature of a liquid in a tank at a desired level.

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INTRODUCTION

In Chapters 5 and 6, series circuits and parallel circuits were studied individually. In this chapter, both series and parallel resistors are combined into series-parallel circuits. In many practical situations, you will have both series and parallel combinations within the same circuit, and the analysis methods you learned for series circuits and for parallel circuits will apply.

Important types of series-parallel circuits are introduced in this chapter. These circuits include the voltage divider with a resistive load, the ladder network, and the Wheatstone bridge. Ideal circuit elements are assumed including voltage and current sources as well as linear resistors to form equivalent circuits. The methods you learn in this chapter with ideal components can later be applied with modifications to more complex practical circuits.

The analysis of series-parallel circuits requires the use of Ohm's law, Kirchhoff's voltage and current laws, and the methods for finding total resistance and power that you learned in the last two chapters. The topic of loaded voltage dividers is important because this type of circuit is found in many practical situations, such as the voltage-divider bias circuit for a transistor amplifier. Ladder networks are important in several areas, including a major type of digital-to-analog conversion, which you will study in a digital fundamentals course. The Wheatstone bridge is used in many types of systems for the measurement of unknown parameters, including most electronic scales.

7-1 IDENTIFYING SERIES-PARALLEL RELATIONSHIPS

A series-parallel circuit consists of combinations of both series and parallel current paths. It is important to be able to identify how the components in a circuit are arranged in terms of their series and parallel relationships.

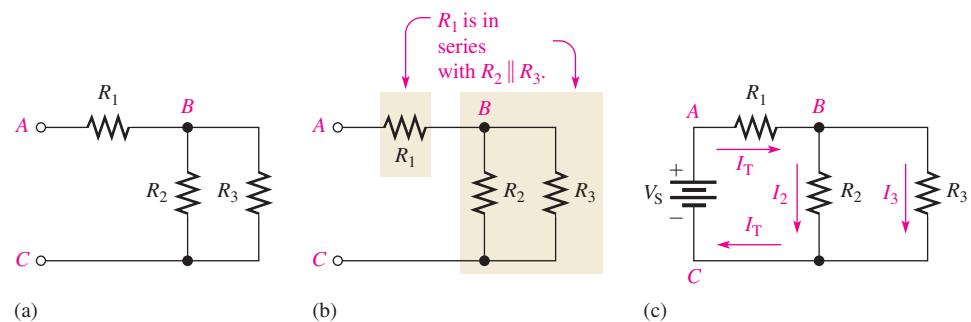
After completing this section, you should be able to

- ◆ **Identify series-parallel relationships**
 - ◆ Recognize how each resistor in a given circuit is related to the other resistors
 - ◆ Determine series and parallel relationships on a PC board

Figure 7-1(a) shows an example of a simple series-parallel combination of resistors. Notice that the resistance from point *A* to point *B* is R_1 . The resistance from point *B* to point *C* is R_2 and R_3 in parallel ($R_2 \parallel R_3$). The total resistance from point *A* to point *C* is R_1 in series with the parallel combination of R_2 and R_3 , as indicated in Figure 7-1(b). The term *point* can refer to either a node or a terminal. For example, in Figure 7-1(a), *A* is a terminal because it is an end point, *B* is a node because it is the junction of two or more components, and *C* is both a terminal and a node. So, the term *point* will sometimes be used to represent either or both.

► FIGURE 7-1

A simple series-parallel resistive circuit.

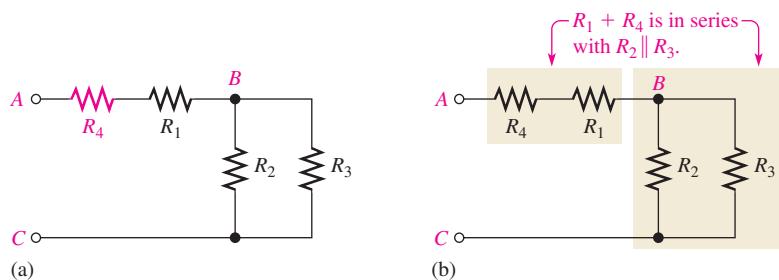


When the circuit of Figure 7-1(a) is connected to a voltage source as shown in Figure 7-1(c), the total current is through R_1 and divides at point *B* into the two parallel paths. These two branch currents then recombine, and the total current is into the negative source terminal as shown.

Now, to illustrate series-parallel relationships, let's increase the complexity of the circuit in Figure 7-1(a) step-by-step. In Figure 7-2(a), another resistor (R_4)

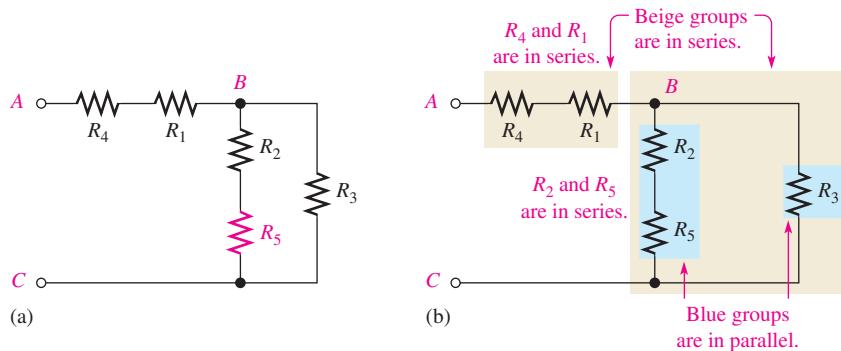
► FIGURE 7-2

R_4 is added to the circuit in series with R_1 .



is connected in series with R_1 . The resistance between points A and B is now $R_1 + R_4$, and this combination is in series with the parallel combination of R_2 and R_3 , as illustrated in Figure 7–2(b).

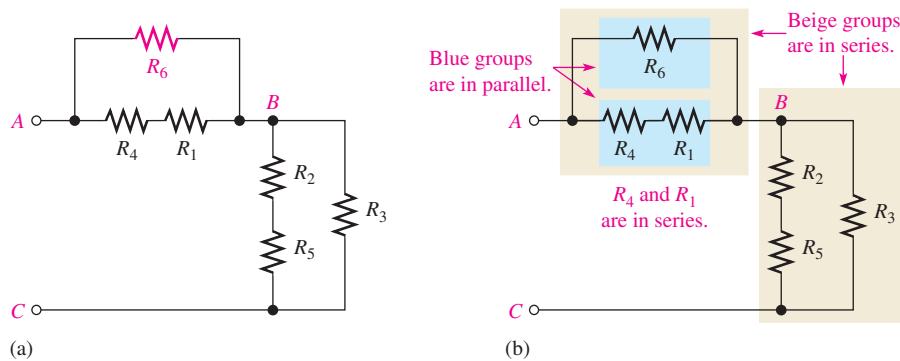
In Figure 7–3(a), R_5 is connected in series with R_2 . The series combination of R_2 and R_5 is in parallel with R_3 . This entire series-parallel combination is in series with the series combination of R_1 and R_4 , as illustrated in Figure 7–3(b).



▲ FIGURE 7–3

R_5 is added to the circuit in series with R_2 .

In Figure 7–4(a), R_6 is connected in parallel with the series combination of R_1 and R_4 . The series-parallel combination of R_1 , R_4 , and R_6 is in series with the series-parallel combination of R_2 , R_3 , and R_5 , as indicated in Figure 7–4(b).



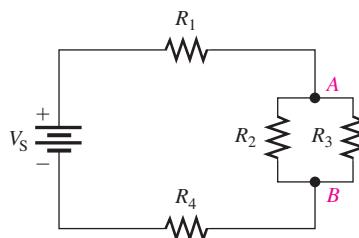
▲ FIGURE 7–4

R_6 is added to the circuit in parallel with the series combination of R_1 and R_4 .

EXAMPLE 7–1

Identify the series-parallel relationships in Figure 7–5.

► FIGURE 7–5

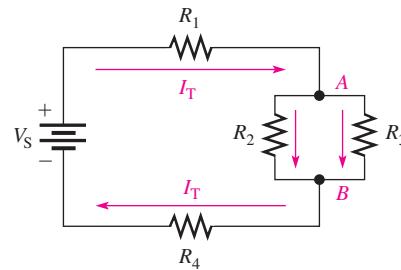


Solution Starting at the positive terminal of the source, follow the current paths. All of the current produced by the source must go through R_1 , which is in series with the rest of the circuit.

The total current takes two paths when it gets to node A . Part of it is through R_2 , and part of it is through R_3 . Resistors R_2 and R_3 are in parallel with each other, and this parallel combination is in series with R_1 .

At node B , the currents through R_2 and R_3 come together again. Thus, the total current is through R_4 . Resistor R_4 is in series with R_1 and the parallel combination of R_2 and R_3 . The currents are shown in Figure 7–6, where I_T is the total current.

► FIGURE 7–6



In summary, R_1 and R_4 are in series with the parallel combination of R_2 and R_3 . From precedence rules, R_2 and R_3 are solved first and the result is added to R_1 and R_4 as given by the following expression:

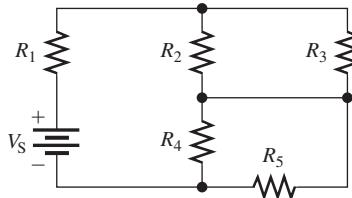
$$R_1 + R_2 \parallel R_3 + R_4$$

Related Problem* If another resistor, R_5 , is connected from node A to the negative side of the source in Figure 7–6, what is its relationship to the other resistors?

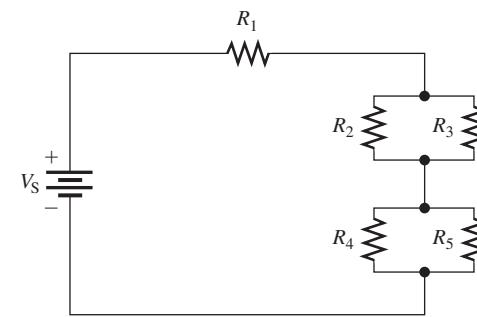
*Answers are at the end of the chapter.

EXAMPLE 7–2

Identify the series-parallel relationships in Figure 7–7.



▲ FIGURE 7–7



▲ FIGURE 7–8

Solution

Sometimes it is easier to see a particular circuit arrangement if it is drawn in a different way. In this case, the circuit schematic is redrawn in Figure 7–8, which better illustrates the series-parallel relationships. Now you can see that R_2 and R_3 are in parallel

with each other and also that R_4 and R_5 are in parallel with each other. Both parallel combinations are in series with each other and with R_1 as stated by the following expression:

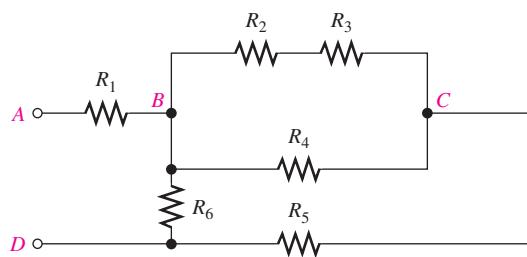
$$R_1 + R_2 \parallel R_3 + R_4 \parallel R_5$$

Related Problem If a resistor is connected from the bottom end of R_3 to the top end of R_5 in Figure 7–8, what effect does it have on the circuit?

EXAMPLE 7–3

Describe the series-parallel combination between terminals *A* and *D* in Figure 7–9.

► FIGURE 7–9



Solution Between nodes *B* and *C*, there are two parallel paths. The lower path consists of R_4 , and the upper path consists of a series combination of R_2 and R_3 . This parallel combination is in series with R_5 . The R_2 , R_3 , R_4 , R_5 combination is in parallel with R_6 . Resistor R_1 is in series with this entire combination as stated by the following expression:

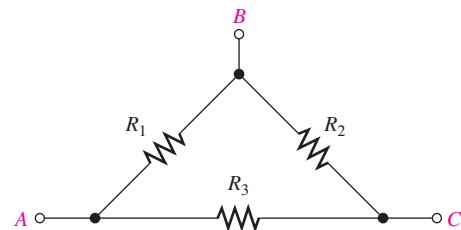
$$R_1 + R_6 \parallel (R_5 + R_4 \parallel (R_2 + R_3))$$

Related Problem If a resistor is connected from *C* to *D* in Figure 7–9, describe its parallel relationship.

EXAMPLE 7–4

Describe the total resistance between each pair of terminals in Figure 7–10.

► FIGURE 7–10



Solution 1. From *A* to *B*: R_1 is in parallel with the series combination of R_2 and R_3 .

$$R_1 \parallel (R_2 + R_3)$$

2. From *A* to *C*: R_3 is in parallel with the series combination of R_1 and R_2 .

$$R_3 \parallel (R_1 + R_2)$$

3. From *B* to *C*: R_2 is in parallel with the series combination of R_1 and R_3 .

$$R_2 \parallel (R_1 + R_3)$$

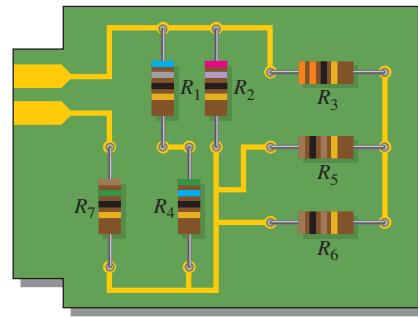
Related Problem In Figure 7–10, describe the total resistance between each terminal and an added ground if a new resistor, R_4 , is connected from C to ground. None of the existing resistors connect directly to the ground.

Usually, the physical arrangement of components on a PC or protoboard bears no resemblance to the actual circuit relationships. By tracing out the circuit and rearranging the components on paper into a recognizable form, you can determine the series-parallel relationships.

EXAMPLE 7–5

Determine the relationships of the resistors on the PC board in Figure 7–11.

► FIGURE 7–11



Solution

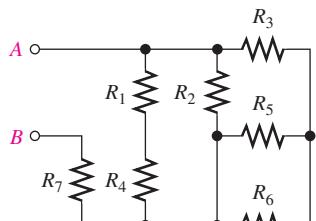
In Figure 7–12(a), the schematic is drawn in the same arrangement as that of the resistors on the board. In part (b), the resistors are rearranged so that the series-parallel relationships are more obvious.

Resistors R_1 and R_4 are in series; $R_1 + R_4$ is in parallel with R_2 ; R_5 and R_6 are in parallel and this combination is in series with R_3 .

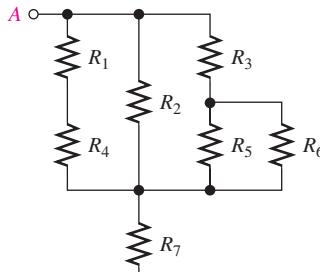
The R_3 , R_5 , and R_6 series-parallel combination is in parallel with both R_2 and the $R_1 + R_4$ combination. This entire series-parallel combination is in series with R_7 .

Figure 7–12(c) illustrates these relationships. Summarizing in equation form,

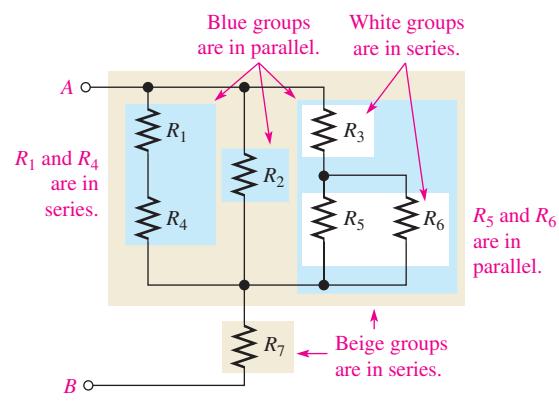
$$R_{AB} = (R_5 \parallel R_6 + R_3) \parallel R_2 \parallel (R_1 + R_4) + R_7$$



(a)



(b)



► FIGURE 7–12

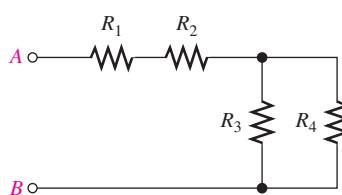
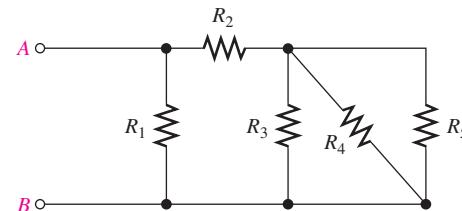
Related Problem

If R_5 were removed from the circuit, what would be the relationship of R_3 and R_6 ?

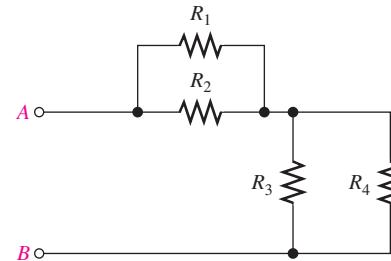
SECTION 7-1**CHECKUP**

Answers are at the end of the chapter.

1. Define *series-parallel resistive circuit*.
2. A certain series-parallel circuit is described as follows: R_1 and R_2 are in parallel. This parallel combination is in series with another parallel combination of R_3 and R_4 . Draw the circuit.
3. In the circuit of Figure 7-13, describe the series-parallel relationships of the resistors.
4. Which resistors are in parallel in Figure 7-14?

**▲ FIGURE 7-13****▲ FIGURE 7-14**

5. Describe the parallel arrangements in Figure 7-15.
6. Are the parallel combinations in Figure 7-15 in series?

► FIGURE 7-15

7-2 ANALYSIS OF SERIES-PARALLEL RESISTIVE CIRCUITS

The analysis of series-parallel circuits can be approached in many ways, depending on what information you need and what circuit values you know. The examples in this section do not represent an exhaustive coverage, but they give you an idea of how to approach series-parallel circuit analysis.

After completing this section, you should be able to

- ◆ **Analyze series-parallel circuits**
 - ◆ Determine total resistance
 - ◆ Determine all the currents
 - ◆ Determine all the voltage drops

If you know Ohm's law, Kirchhoff's laws, the voltage-divider formula, and the current-divider formula, and if you know how to apply these laws, you can solve most resistive circuit analysis problems. The ability to recognize series and parallel combinations is, of course, essential. A few circuits, such as the unbalanced Wheatstone bridge,

do not have basic series and parallel combinations. Other methods are needed for these cases, as we will discuss later.

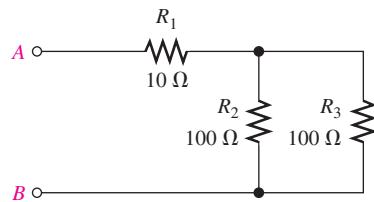
Total Resistance

In Chapter 5, you learned how to determine total series resistance. In Chapter 6, you learned how to determine total parallel resistance. To find the total resistance (R_T) of a series-parallel combination, simply define the series and parallel relationships; then perform the calculations that you have previously learned. The following two examples illustrate this general approach.

EXAMPLE 7–6

Determine R_T of the circuit in Figure 7–16 between terminals *A* and *B*.

► FIGURE 7–16



Solution First, calculate the equivalent parallel resistance of R_2 and R_3 . Since R_2 and R_3 are equal in value, you can use Equation 6–4.

$$R_{2\parallel 3} = \frac{R}{n} = \frac{100 \Omega}{2} = 50 \Omega$$

Notice that the term $R_{2\parallel 3}$ is used here to designate the total resistance of a portion of a circuit in order to distinguish it from the total resistance, R_T , of the complete circuit.

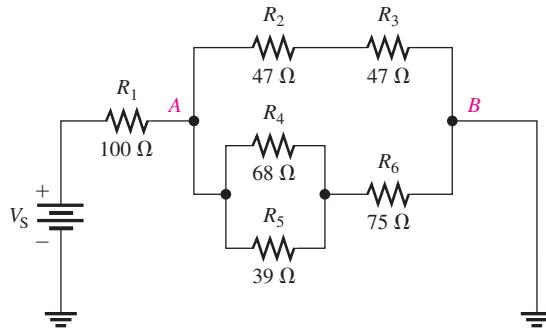
Now, since R_1 is in series with $R_{2\parallel 3}$, add their values as follows:

$$R_T = R_1 + R_{2\parallel 3} = 10 \Omega + 50 \Omega = 60 \Omega$$

Related Problem Determine R_T in Figure 7–16 if R_3 is changed to 82 Ω.

EXAMPLE 7–7

Find the total resistance between the positive and negative terminals of the battery in Figure 7–17.



▲ FIGURE 7–17

Solution In the upper branch, R_2 is in series with R_3 . This series combination is designated R_{2+3} and is equal to $R_2 + R_3$.

$$R_{2+3} = R_2 + R_3 = 47 \Omega + 47 \Omega = 94 \Omega$$

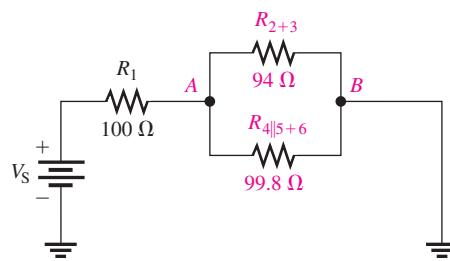
In the lower branch, R_4 and R_5 are in parallel with each other. This parallel combination is designated $R_{4\parallel 5}$.

$$R_{4\parallel 5} = \frac{R_4 R_5}{R_4 + R_5} = \frac{(68 \Omega)(39 \Omega)}{68 \Omega + 39 \Omega} = 24.8 \Omega$$

Also in the lower branch, the parallel combination of R_4 and R_5 is in series with R_6 . This series-parallel combination is designated $R_{4\parallel 5+6}$.

$$R_{4\parallel 5+6} = R_6 + R_{4\parallel 5} = 75 \Omega + 24.8 \Omega = 99.8 \Omega$$

Figure 7–18 shows the original circuit in a simplified equivalent form.



▲ FIGURE 7–18

Now you can find the equivalent resistance between A and B . It is R_{2+3} in parallel with $R_{4\parallel 5+6}$. Calculate the equivalent resistance as follows:

$$R_{AB} = \frac{1}{\frac{1}{R_{2+3}} + \frac{1}{R_{4\parallel 5+6}}} = \frac{1}{\frac{1}{94 \Omega} + \frac{1}{99.8 \Omega}} = 48.4 \Omega$$

Finally, the total resistance is R_1 in series with R_{AB} .

$$R_T = R_1 + R_{AB} = 100 \Omega + 48.4 \Omega = 148.4 \Omega$$

Related Problem Determine R_T if a 68Ω resistor is added in parallel from A to B in Figure 7–17.

Total Current

Once you know the total resistance and the source voltage, you can apply Ohm's law to find the total current in a circuit. Total current is the source voltage divided by the total resistance.

$$I_T = \frac{V_S}{R_T}$$

For example, assuming that the source voltage is 10 V, the total current in the circuit of Example 7–7 (Figure 7–17) is

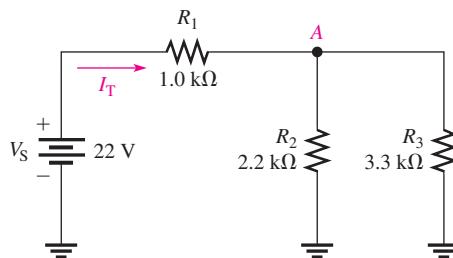
$$I_T = \frac{V_S}{R_T} = \frac{10 \text{ V}}{148.4 \Omega} = 67.4 \text{ mA}$$

Branch Currents

Using the current-divider formula, Kirchhoff's current law, Ohm's law, or combinations of these, you can find the current in any branch of a series-parallel circuit. In some cases, it may take repeated application of the formula to find a given current. The following two examples will help you understand the procedure. (Notice that the subscripts for the current variables (I) match the R subscripts; for example, current through R_1 is referred to as I_1 .)

EXAMPLE 7–8

Find the current through R_2 and the current through R_3 in Figure 7–19.



▲ FIGURE 7–19

Solution First, identify the series and parallel relationship. Next, determine how much current is into node A . This is the total circuit current. To find I_T , you must know R_T .

$$R_T = R_1 + \frac{R_2 R_3}{R_2 + R_3} = 1.0 \text{ k}\Omega + \frac{(2.2 \text{ k}\Omega)(3.3 \text{ k}\Omega)}{2.2 \text{ k}\Omega + 3.3 \text{ k}\Omega} = 1.0 \text{ k}\Omega + 1.32 \text{ k}\Omega = 2.32 \text{ k}\Omega$$

$$I_T = \frac{V_s}{R_T} = \frac{22 \text{ V}}{2.32 \text{ k}\Omega} = 9.48 \text{ mA}$$

Use the current-divider rule for two branches as given in Chapter 6 to find the current through R_2 .

$$I_2 = \left(\frac{R_3}{R_2 + R_3} \right) I_T = \left(\frac{3.3 \text{ k}\Omega}{5.5 \text{ k}\Omega} \right) 9.48 \text{ mA} = 5.69 \text{ mA}$$

Now you can use Kirchhoff's current law to find the current through R_3 .

$$\begin{aligned} I_T &= I_2 + I_3 \\ I_3 &= I_T - I_2 = 9.48 \text{ mA} - 5.69 \text{ mA} = 3.79 \text{ mA} \end{aligned}$$

Related Problem

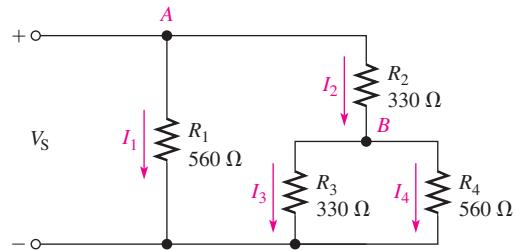
A $4.7 \text{ k}\Omega$ resistor is connected in parallel with R_3 in Figure 7–19. Determine the current through the new resistor.



Use Multisim file E07-08 to verify the calculated results in this example and to confirm your calculation for the related problem.

EXAMPLE 7–9

Determine the current through R_4 in Figure 7–20 if $V_S = 5$ V.

► FIGURE 7–20

Solution First, find the current (I_2) into node B . Once you know this current, use the current-divider formula to find I_4 , the current through R_4 .

Notice that there are two main branches in the circuit. The left-most branch consists of only R_1 . The right-most branch has R_2 in series with the parallel combination of R_3 and R_4 . The voltage across both of these main branches is the same and equal to 5 V. Calculate the equivalent resistance ($R_{2+3\parallel 4}$) of the right-most main branch and then apply Ohm's law; I_2 is the total current through this main branch. Thus,

$$R_{2+3\parallel 4} = R_2 + \frac{R_3 R_4}{R_3 + R_4} = 330 \Omega + \frac{(330 \Omega)(560 \Omega)}{890 \Omega} = 538 \Omega$$

$$I_2 = \frac{V_S}{R_{2+3\parallel 4}} = \frac{5 \text{ V}}{538 \Omega} = 9.30 \text{ mA}$$

Use the two-resistor current-divider formula to calculate I_4 .

$$I_4 = \left(\frac{R_3}{R_3 + R_4} \right) I_2 = \left(\frac{330 \Omega}{890 \Omega} \right) 9.30 \text{ mA} = 3.45 \text{ mA}$$

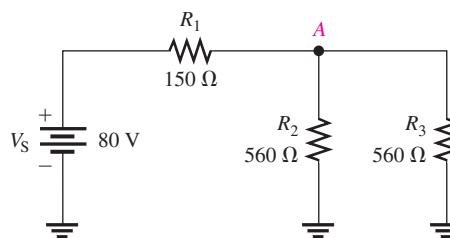
Related Problem Determine the current through R_1 and R_3 in Figure 7–20 if $V_S = 2$ V.

Voltage Drops

To find the voltages across certain parts of a series-parallel circuit, you can use the voltage-divider formula given in Chapter 5, Kirchhoff's voltage law, Ohm's law, or combinations of each. The following three examples illustrate use of the formulas. (The subscripts for V match the subscripts for the corresponding R : V_1 is the voltage across R_1 ; V_2 is the voltage across R_2 ; etc.)

EXAMPLE 7–10

Determine the voltage drop from node A to ground in Figure 7–21. Then find the voltage (V_1) across R_1 .

► FIGURE 7–21

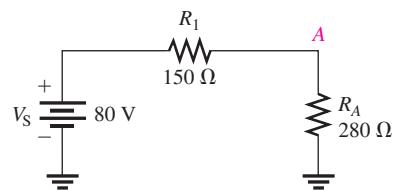
Solution Note that R_2 and R_3 are in parallel in this circuit. Since they are equal in value, their equivalent resistance from node A to ground is

$$R_A = \frac{560 \Omega}{2} = 280 \Omega$$

In the equivalent circuit shown in Figure 7–22, R_1 is in series with R_A . The total circuit resistance as seen from the source is

$$R_T = R_1 + R_A = 150 \Omega + 280 \Omega = 430 \Omega$$

► FIGURE 7–22



Use the voltage-divider formula to find the voltage across the parallel combination of Figure 7–21 (between node A and ground).

$$V_A = \left(\frac{R_A}{R_T} \right) V_S = \left(\frac{280 \Omega}{430 \Omega} \right) 80 \text{ V} = 52.1 \text{ V}$$

Now use Kirchhoff's voltage law to find V_1 .

$$V_S = V_1 + V_A$$

$$V_1 = V_S - V_A = 80 \text{ V} - 52.1 \text{ V} = 27.9 \text{ V}$$

Related Problem

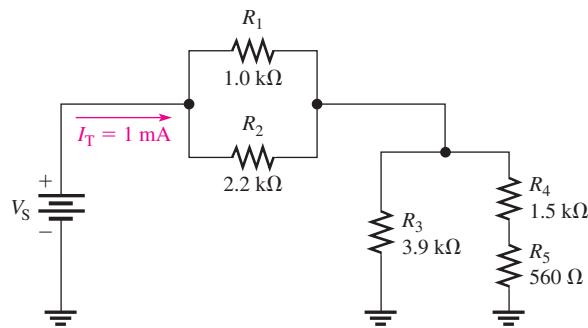
Determine V_A and V_1 if R_1 is changed to 220Ω in Figure 7–21.



Use Multisim file E07-10 to verify the calculated results in this example and to confirm your calculation for the related problem.

EXAMPLE 7–11

Determine the voltage drop across each resistor in the circuit of Figure 7–23.



▲ FIGURE 7–23

Solution The source voltage is not given, but you know the total current from the figure. Since R_1 and R_2 are in parallel, they each have the same voltage. The current through R_1 is

$$I_1 = \left(\frac{R_2}{R_1 + R_2} \right) I_T = \left(\frac{2.2 \text{ k}\Omega}{3.2 \text{ k}\Omega} \right) 1 \text{ mA} = 688 \mu\text{A}$$

The voltages across R_1 and R_2 are

$$V_1 = I_1 R_1 = (688 \mu\text{A})(1.0 \text{ k}\Omega) = 688 \text{ mV}$$

$$V_2 = V_1 = 688 \text{ mV}$$

The series combination of R_4 and R_5 form the branch resistance, R_{4+5} . Apply the current-divider formula to determine the current through R_3 .

$$I_3 = \left(\frac{R_{4+5}}{R_3 + R_{4+5}} \right) I_T = \left(\frac{2.06 \text{ k}\Omega}{5.96 \text{ k}\Omega} \right) 1 \text{ mA} = 346 \mu\text{A}$$

The voltage across R_3 is

$$V_3 = I_3 R_3 = (346 \mu\text{A})(3.9 \text{ k}\Omega) = 1.35 \text{ V}$$

The currents through R_4 and R_5 are the same because these resistors are in series.

$$I_4 = I_5 = I_T - I_3 = 1 \text{ mA} - 346 \mu\text{A} = 654 \mu\text{A}$$

Calculate the voltages across R_4 and R_5 as follows:

$$V_4 = I_4 R_4 = (654 \mu\text{A})(1.5 \text{ k}\Omega) = 981 \text{ mV}$$

$$V_5 = I_5 R_5 = (654 \mu\text{A})(560 \Omega) = 366 \text{ mV}$$

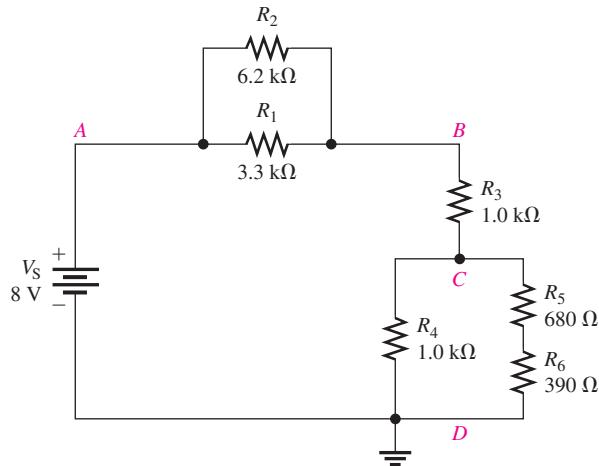
Related Problem What is the source voltage, V_S , in the circuit of Figure 7–23?



Use Multisim file E07-11 to verify the calculated results in this example and to confirm your calculation for the related problem.

EXAMPLE 7–12

Determine the voltage drop across each resistor in Figure 7–24.



▲ FIGURE 7–24

Solution Because the total voltage is given in the figure, you can solve this problem using the voltage-divider formula. First, you need to reduce each parallel combination to an equivalent resistance. Since R_1 and R_2 are in parallel between A and B , combine their values.

$$R_{AB} = \frac{R_1 R_2}{R_1 + R_2} = \frac{(3.3 \text{ k}\Omega)(6.2 \text{ k}\Omega)}{9.5 \text{ k}\Omega} = 2.15 \text{ k}\Omega$$

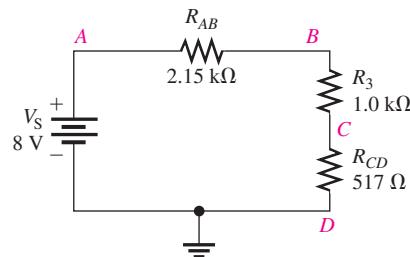
Since R_4 is in parallel with the R_5 and R_6 series combination (R_{5+6}) between C and D , combine these values.

$$R_{CD} = \frac{R_4 R_{5+6}}{R_4 + R_{5+6}} = \frac{(1.0 \text{ k}\Omega)(1.07 \text{ k}\Omega)}{2.07 \text{ k}\Omega} = 517 \Omega$$

The equivalent circuit is drawn in Figure 7–25. The total circuit resistance is

$$R_T = R_{AB} + R_3 + R_{CD} = 2.15 \text{ k}\Omega + 1.0 \text{ k}\Omega + 517 \Omega = 3.67 \text{ k}\Omega$$

► FIGURE 7–25



Next, use the voltage-divider formula to determine the voltages in the equivalent circuit.

$$V_{AB} = \left(\frac{R_{AB}}{R_T} \right) V_S = \left(\frac{2.15 \text{ k}\Omega}{3.67 \text{ k}\Omega} \right) 8 \text{ V} = 4.69 \text{ V}$$

$$V_{CD} = \left(\frac{R_{CD}}{R_T} \right) V_S = \left(\frac{517 \Omega}{3.67 \text{ k}\Omega} \right) 8 \text{ V} = 1.13 \text{ V}$$

$$V_3 = \left(\frac{R_3}{R_T} \right) V_S = \left(\frac{1.0 \text{ k}\Omega}{3.67 \text{ k}\Omega} \right) 8 \text{ V} = 2.18 \text{ V}$$

Refer to Figure 7–24. V_{AB} equals the voltage across both R_1 and R_2 , so

$$V_1 = V_2 = V_{AB} = 4.69 \text{ V}$$

V_{CD} is the voltage across R_4 and across the series combination of R_5 and R_6 . Therefore,

$$V_4 = V_{CD} = 1.13 \text{ V}$$

Now apply the voltage-divider formula to the series combination of R_5 and R_6 to get V_5 and V_6 .

$$V_5 = \left(\frac{R_5}{R_5 + R_6} \right) V_{CD} = \left(\frac{680 \Omega}{1,070 \Omega} \right) 1.13 \text{ V} = 716 \text{ mV}$$

$$V_6 = \left(\frac{R_6}{R_5 + R_6} \right) V_{CD} = \left(\frac{390 \Omega}{1,070 \Omega} \right) 1.13 \text{ V} = 411 \text{ mV}$$

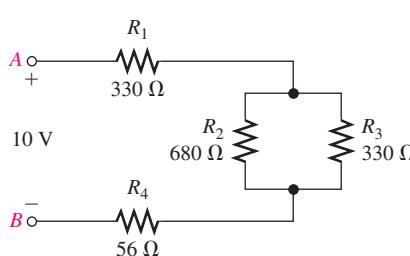
Related Problem R_2 is removed from the circuit in Figure 7–24. Calculate V_{AB} , V_{BC} , and V_{CD} .



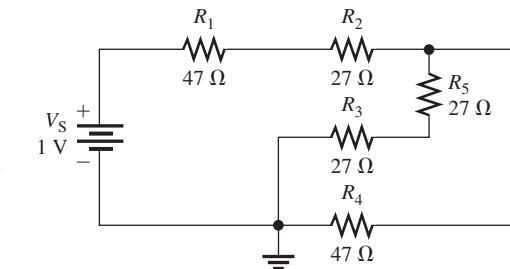
Use Multisim file E07-12 to verify the calculated results in this example and to confirm your calculation for the related problem.

SECTION 7–2 CHECKUP

1. List four circuit laws and formulas that may be necessary in the analysis of series-parallel circuits.
2. Find the total resistance between A and B in the circuit of Figure 7–26.
3. Find the current through R_3 in Figure 7–26.
4. Find the voltage drop across R_2 in Figure 7–26.
5. Determine R_T and I_T in Figure 7–27 as “seen” by the source.



▲ FIGURE 7-26



▲ FIGURE 7-27

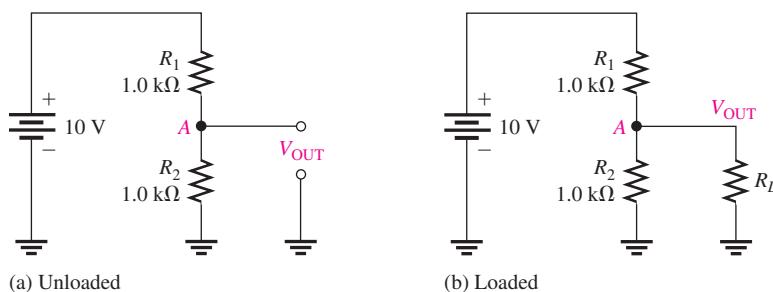
7–3 VOLTAGE DIVIDERS WITH RESISTIVE LOADS

Voltage dividers were introduced in Chapter 5. In this section, you will learn how resistive loads affect the operation of voltage-divider circuits.

After completing this section, you should be able to

- ◆ **Analyze loaded voltage dividers**
 - ◆ Determine the effect of a resistive load on a voltage-divider circuit
 - ◆ Define *bleeder current*

The voltage divider in Figure 7–28(a) produces an output voltage (V_{OUT}) of 5 V because the two resistors are of equal value. This voltage is the *unloaded output voltage*. When a load resistor, R_L , is connected from the output to ground as shown in Figure 7–28(b), the output voltage is reduced by an amount that depends on the value of R_L . The load resistor is in parallel with R_2 , reducing the resistance from node A to ground and, as a result, also reducing the voltage across the parallel combination.

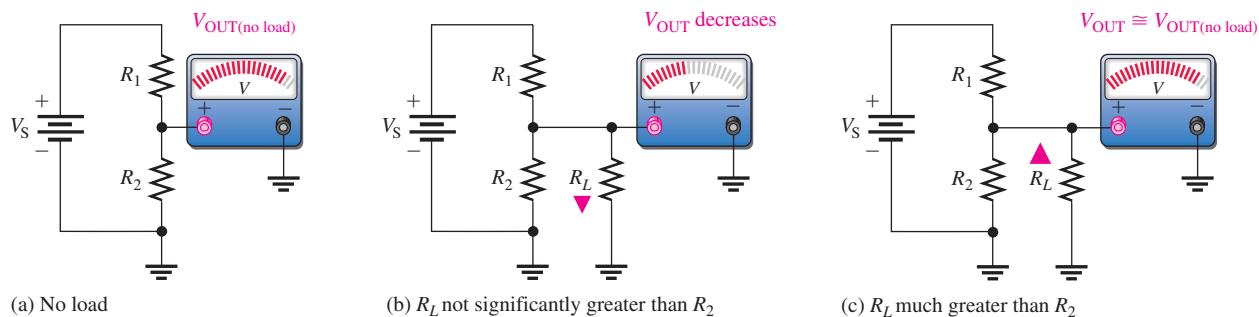


▲ FIGURE 7-28

A voltage divider with both unloaded and loaded outputs.

This is one effect of loading a voltage divider. Another effect of a load is that more current is drawn from the source because the total resistance of the circuit is reduced.

The larger R_L is, compared to R_2 , the less the output voltage is reduced from its unloaded value, as illustrated in Figure 7–29. When R_L is large compared to R_2 (at least 10 times), the loading effect is small, and the output voltage will change only a small amount from its unloaded value, as shown in Figure 7–29(c). In this case, the divider is said to be a **stiff voltage divider**.



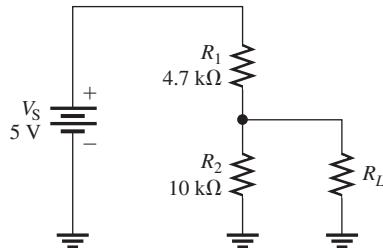
▲ FIGURE 7-29

The effect of a load resistor.

EXAMPLE 7-13

- Determine the unloaded output voltage of the voltage divider in Figure 7–30.
- Find the loaded output voltages of the voltage divider in Figure 7–30 for the following two values of load resistance: $R_L = 10 \text{ k}\Omega$ and $R_L = 100 \text{ k}\Omega$.

► FIGURE 7-30



Solution (a) The unloaded output voltage is

$$V_{\text{OUT(unloaded)}} = \left(\frac{R_2}{R_1 + R_2} \right) V_s = \left(\frac{10 \text{ k}\Omega}{14.7 \text{ k}\Omega} \right) 5 \text{ V} = 3.40 \text{ V}$$

(b) With the $10 \text{ k}\Omega$ load resistor connected, R_L is in parallel with R_2 , which gives

$$R_2 \parallel R_L = \frac{R_2 R_L}{R_2 + R_L} = \frac{100 \text{ M}\Omega}{20 \text{ k}\Omega} = 5 \text{ k}\Omega$$

The equivalent circuit is shown in Figure 7–31(a). The loaded output voltage is

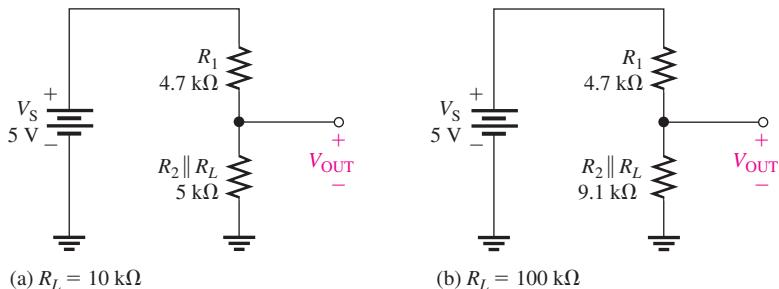
$$V_{\text{OUT.loaded}} = \left(\frac{R_2 \parallel R_L}{R_1 + R_2 \parallel R_L} \right) V_s = \left(\frac{5 \text{ k}\Omega}{9.7 \text{ k}\Omega} \right) 5 \text{ V} = 2.58 \text{ V}$$

With the $100 \text{ k}\Omega$ load, the resistance from output to ground is

$$R_2 \parallel R_L = \frac{R_2 R_L}{R_2 + R_L} = \frac{(10 \text{ k}\Omega)(100 \text{ k}\Omega)}{110 \text{ k}\Omega} = 9.1 \text{ k}\Omega$$

The equivalent circuit is shown in Figure 7–31(b). The loaded output voltage is

$$V_{\text{OUT(loaded)}} = \left(\frac{R_2 \parallel R_L}{R_1 + R_2 \parallel R_L} \right) V_S = \left(\frac{9.1 \text{ k}\Omega}{13.8 \text{ k}\Omega} \right) 5 \text{ V} = 3.30 \text{ V}$$



▲ FIGURE 7–31

For the smaller value of R_L , the reduction in V_{OUT} is

$$3.40 \text{ V} - 2.58 \text{ V} = 0.82 \text{ V} \quad (\text{a } 24\% \text{ drop in output voltage})$$

For the larger value of R_L , the reduction in V_{OUT} is

$$3.40 \text{ V} - 3.30 \text{ V} = 0.10 \text{ V} \quad (\text{a } 3\% \text{ drop in output voltage})$$

This illustrates the loading effect of R_L on the voltage divider.

Related Problem Determine V_{OUT} in Figure 7–30 for a $1.0 \text{ M}\Omega$ load resistance.



Use Multisim file E07-13 to verify the calculated results in this example and to confirm your calculation for the related problem.

A Practical Application

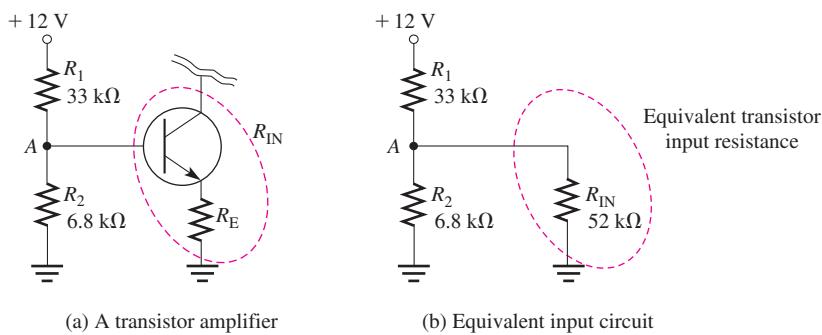
A practical application of a voltage divider with a resistive load occurs in certain transistor amplifiers. The transistor's input circuit can be modeled as a resistance that acts exactly like a load resistor on a voltage divider. The voltage divider sets the dc voltage (called bias) for the transistor to operate properly. The next example illustrates a practical example. Keep in mind that our focus here is on the effect of the equivalent resistive load on the bias rather than the transistor.

EXAMPLE 7–14

A voltage divider is in a transistor amplifier bias network is shown in Figure 7–32(a). The input to the transistor can be modeled as a single resistor, R_{IN} , as shown in Figure 7–32(b). (The method for finding R_{IN} is studied in devices courses.) Our focus here is the effect of loading for the equivalent circuit, which is modeled in (b) as a voltage-divider with a resistive load. From the modeled circuit, determine the voltage at A and the current into the transistor, I_{IN} .

Solution Notice that R_{IN} is in parallel with R_2 . The resistance of this parallel combination is $R_{(\text{IN}+2)}$:

$$R_{(\text{IN}+2)} = \frac{1}{\frac{1}{R_{\text{IN}}} + \frac{1}{R_2}} = \frac{1}{\frac{1}{52 \text{ k}\Omega} + \frac{1}{6.8 \text{ k}\Omega}} = 6.01 \text{ k}\Omega$$



▲ FIGURE 7-32

This resistance is in series with R_1 , so the voltage-divider formula can be applied to find V_A :

$$V_A = \left(\frac{R_{(IN+2)}}{R_{(IN+2)} + R_1} \right) V_S = \left(\frac{6.01 \text{ k}\Omega}{6.01 \text{ k}\Omega + 33 \text{ k}\Omega} \right) 12 \text{ V} = 1.85 \text{ V}$$

$$I_{IN} = \frac{V_A}{R_{IN}} = \frac{1.85 \text{ V}}{52 \text{ k}\Omega} = 35.6 \mu\text{A}$$

Related Problem

What is the current in R_1 ?



Use Multisim file E07-14 to verify the calculated results in this example and to confirm your calculation for the related problem.

Load Current and Bleeder Current

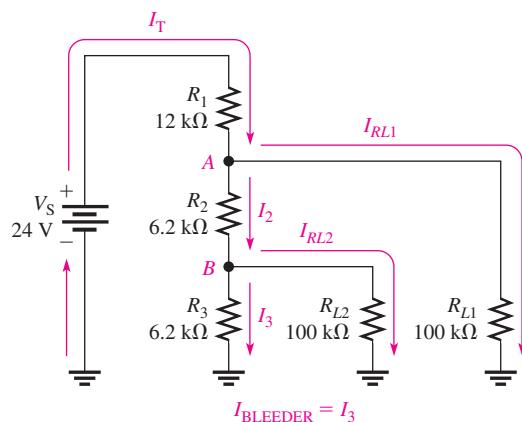
In a multiple-tap loaded voltage-divider circuit, the total current drawn from the source consists of currents through the load resistors, called *load currents*, and the divider resistors. Figure 7-33 shows a voltage divider with two voltage outputs or two taps. Notice that the total current, I_T , through R_1 enters node A where the current divides into I_{RL1} through R_{L1} and into I_2 through R_2 . At node B, the current I_2 divides into I_{RL2} through R_{RL2} and into I_3 through R_3 . Current I_3 is called the **bleeder current**, which is the current left after the total load current is subtracted from the total current in the circuit.

Equation 7-1

$$I_{BLEEDER} = I_T - I_{RL1} - I_{RL2}$$

► FIGURE 7-33

Currents in a two-tap loaded voltage divider.



EXAMPLE 7-15

Determine the load currents I_{RL1} and I_{RL2} and the bleeder current I_3 in the two-tap loaded voltage divider in Figure 7-33.

Solution The equivalent resistance from node A to ground is the $100\text{ k}\Omega$ load resistor R_{L1} in parallel with the combination of R_2 in series with the parallel combination of R_3 and R_{L2} . Determine the resistance values first. R_3 in parallel with R_{L2} is designated R_B . The resulting equivalent circuit is shown in Figure 7-34(a).

$$R_B = \frac{R_3 R_{L2}}{R_3 + R_{L2}} = \frac{(6.2\text{ k}\Omega)(100\text{ k}\Omega)}{106.2\text{ k}\Omega} = 5.84\text{ k}\Omega$$

R_2 in series with R_B is designated R_{2+B} . The resulting equivalent circuit is shown in Figure 7-34(b).

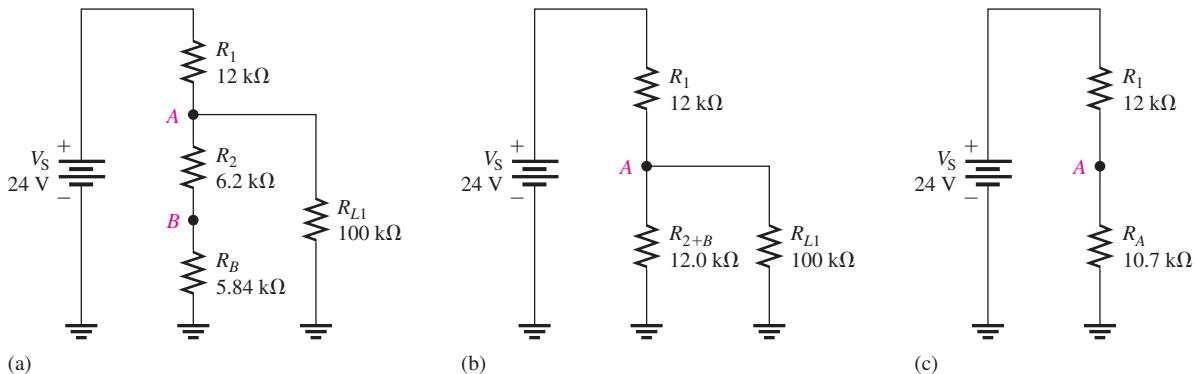
$$R_{2+B} = R_2 + R_B = 6.2\text{ k}\Omega + 5.84\text{ k}\Omega = 12.0\text{ k}\Omega$$

R_{L1} in parallel with R_{2+B} is designated R_A . The resulting equivalent circuit is shown in Figure 7-34(c).

$$R_A = \frac{R_{L1} R_{2+B}}{R_{L1} + R_{2+B}} = \frac{(100\text{ k}\Omega)(12.0\text{ k}\Omega)}{112\text{ k}\Omega} = 10.7\text{ k}\Omega$$

R_A is the total resistance from node A to ground. The total resistance for the circuit is

$$R_T = R_A + R_1 = 10.7\text{ k}\Omega + 12\text{ k}\Omega = 22.7\text{ k}\Omega$$



▲ FIGURE 7-34

Determine the voltage across R_{L1} as follows, using the equivalent circuit in Figure 7-34(c):

$$V_{RL1} = V_A = \left(\frac{R_A}{R_T} \right) V_S = \left(\frac{10.7\text{ k}\Omega}{22.7\text{ k}\Omega} \right) 24\text{ V} = 11.3\text{ V}$$

The load current through R_{L1} is

$$I_{RL1} = \frac{V_{RL1}}{R_{L1}} = \left(\frac{11.3\text{ V}}{100\text{ k}\Omega} \right) = 113\text{ }\mu\text{A}$$

Determine the voltage at node *B* by using the equivalent circuit in Figure 7–34(a) and the voltage at node *A*.

$$V_B = \left(\frac{R_B}{R_{2+B}} \right) V_A = \left(\frac{5.84 \text{ k}\Omega}{12.0 \text{ k}\Omega} \right) 11.3 \text{ V} = 5.50 \text{ V}$$

The load current through R_{L2} is

$$I_{RL2} = \frac{V_{RL2}}{R_{L2}} = \frac{V_B}{R_{L2}} = \frac{5.50 \text{ V}}{100 \text{ k}\Omega} = 55 \mu\text{A}$$

The bleeder current is

$$I_3 = \frac{V_B}{R_3} = \frac{5.50 \text{ V}}{6.2 \text{ k}\Omega} = 887 \mu\text{A}$$

Related Problem

What will happen to the load current in R_{L2} if R_{L1} is disconnected.



Use Multisim file E07-15 to verify the calculated results in this example.

SECTION 7-3 CHECKUP

1. A load resistor is connected to an output tap on a voltage divider. What effect does the load resistor have on the output voltage at this tap?
2. A larger-value load resistor will cause the output voltage to change less than a smaller-value one will. (T or F)
3. For the voltage divider in Figure 7–32(b), determine the unloaded output voltage with respect to ground.

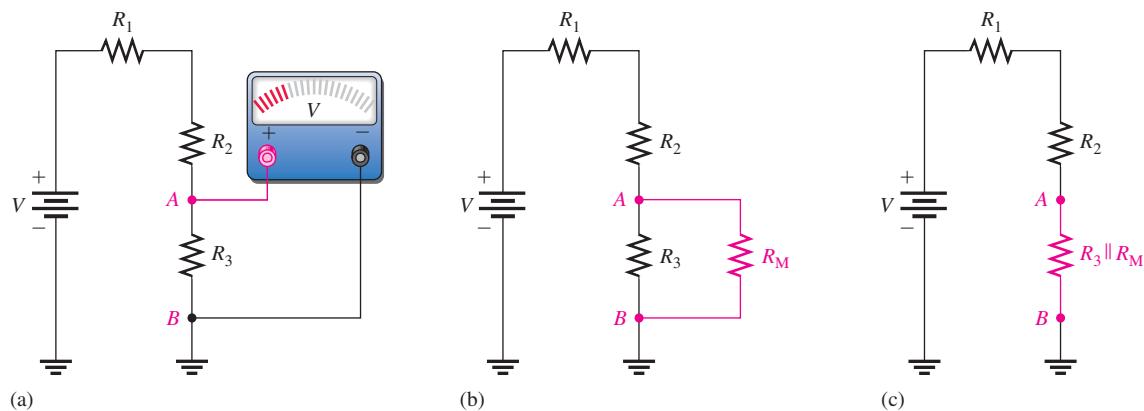
7-4 LOADING EFFECT OF A VOLTMETER

As you have learned, voltmeters must be connected in parallel with a resistor in order to measure the voltage across the resistor. Because of its internal resistance, a voltmeter puts a load on the circuit and will affect, to a certain extent, the voltage that is being measured. Until now, we have ignored the loading effect because the internal resistance of a voltmeter is very high, and normally it has negligible effect on the circuit that is being measured. However, if the internal resistance of the voltmeter is not sufficiently greater than the circuit resistance across which it is connected, the loading effect will cause the measured voltage to be less than its actual value. You should always be aware of this effect.

After completing this section, you should be able to

- ◆ **Determine the loading effect of a voltmeter on a circuit**
- ◆ Explain why a voltmeter can load a circuit
- ◆ Discuss the internal resistance of a voltmeter

When a voltmeter is connected to a circuit as shown, for example, in Figure 7–35(a), its internal resistance appears in parallel with R_3 , as shown in part (b). The resistance from A to B is altered by the loading effect of the voltmeter's internal resistance, R_M , and is equal to $R_3 \parallel R_M$, as indicated in part (c).



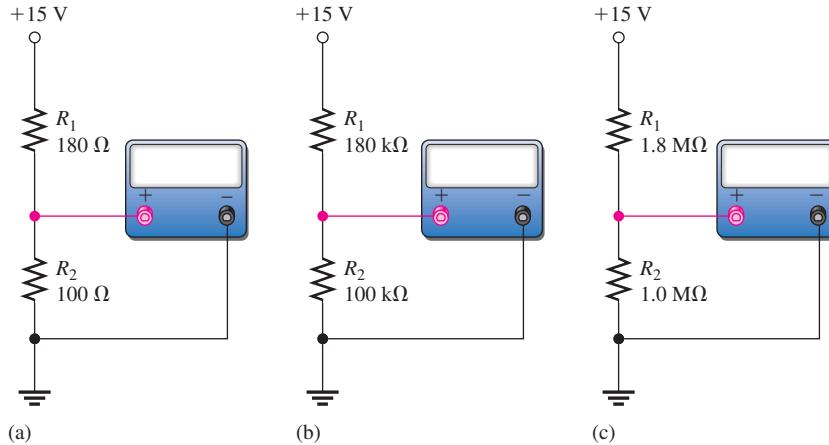
▲ FIGURE 7-35
The loading effect of a voltmeter.

If R_M is much greater than R_3 , the resistance from A to B changes very little, and the meter indicates the actual voltage. If R_M is not sufficiently greater than R_3 , the resistance from A to B is reduced significantly, and the voltage across R_3 is altered by the loading effect of the meter. A good rule of thumb for troubleshooting work is that *if the loading effect is less than 10%, it can usually be neglected, depending on the accuracy required.*

Most voltmeters are part of a multifunction instrument such as the DMM or the analog multimeter discussed in Chapter 2. The voltmeter in a DMM will typically have an internal resistance of $10\text{ M}\Omega$ or more, so the loading effect is important only in very high-resistance circuits. DMMs have a constant resistance on all ranges because the input is connected to an internal fixed voltage divider. For analog multimeters, the internal resistance depends on the range selected for making a measurement. To determine the loading effect, you need to know the sensitivity of the meter, a value given by the manufacturer on the meter or in the manual. Sensitivity is expressed in ohms/volt and is typically about $20,000\text{ }\Omega/\text{V}$. To determine the internal series resistance, multiply the sensitivity by the maximum voltage on the range selected. For example, a $20,000\text{ }\Omega/\text{V}$ meter will have an internal resistance of $20,000\text{ }\Omega$ on the 1 V range and $200,000\text{ }\Omega$ on the 10 V range. As you can see, there is a smaller loading effect for higher voltage ranges than for lower ones on the analog multimeter.

EXAMPLE 7-16

Calculate the expected meter reading for each circuit indicated in Figure 7-36? Assume the meter has an input resistance (R_M) of $10\text{ M}\Omega$.

**▲ FIGURE 7-36**

Solution To show the small differences more clearly, the results are expressed in more than three significant figures in this example. The extra digits are meaningless but retained to show the tiny loading effect.

- (a) Refer to Figure 7-36(a). The unloaded voltage across R_2 in the voltage-divider circuit is

$$V_2 = \left(\frac{R_2}{R_1 + R_2} \right) V_S = \left(\frac{100 \Omega}{280 \Omega} \right) 15 \text{ V} = 5.357 \text{ V}$$

The meter's resistance in parallel with R_2 is

$$R_2 \parallel R_M = \left(\frac{R_2 R_M}{R_2 + R_M} \right) = \frac{(100 \Omega)(10 \text{ M}\Omega)}{10.0001 \text{ M}\Omega} = 99.999 \Omega$$

The voltage actually measured by the meter is

$$V_2 = \left(\frac{R_2 \parallel R_M}{R_1 + R_2 \parallel R_M} \right) V_S = \left(\frac{99.999 \Omega}{279.999 \Omega} \right) 15 \text{ V} = 5.357 \text{ V}$$

The voltmeter has no measurable loading effect.

- (b) Refer to Figure 7-36(b).

$$V_2 = \left(\frac{R_2}{R_1 + R_2} \right) V_S = \left(\frac{100 \text{ k}\Omega}{280 \text{ k}\Omega} \right) 15 \text{ V} = 5.357 \text{ V}$$

$$R_2 \parallel R_M = \frac{R_2 R_M}{R_2 + R_M} = \frac{(100 \text{ k}\Omega)(10 \text{ M}\Omega)}{10.1 \text{ M}\Omega} = 99.01 \text{ k}\Omega$$

The voltage actually measured by the meter is

$$V_2 = \left(\frac{R_2 \parallel R_M}{R_1 + R_2 \parallel R_M} \right) V_S = \left(\frac{99.01 \text{ k}\Omega}{279.01 \text{ k}\Omega} \right) 15 \text{ V} = 5.323 \text{ V}$$

The loading effect of the voltmeter reduces the voltage by a very small amount.

(c) Refer to Figure 7–36(c).

$$V_2 = \left(\frac{R_2}{R_1 + R_2} \right) V_S = \left(\frac{1.0 \text{ M}\Omega}{2.8 \text{ M}\Omega} \right) 15 \text{ V} = 5.357 \text{ V}$$

$$R_2 \parallel R_M = \frac{R_2 R_M}{R_2 + R_M} = \frac{(1.0 \text{ M}\Omega)(10 \text{ M}\Omega)}{11 \text{ M}\Omega} = 909.09 \text{ k}\Omega$$

The voltage actually measured is

$$V_2 = \left(\frac{R_2 \parallel R_M}{R_1 + R_2 \parallel R_M} \right) V_S = \left(\frac{909.09 \text{ k}\Omega}{2.709 \text{ M}\Omega} \right) 15 \text{ V} = 5.034 \text{ V}$$

The loading effect of the voltmeter reduces the voltage by a noticeable amount. As you can see, the higher the resistance across which a voltage is measured, the more the loading effect.

Related Problem Calculate the voltage across R_2 in Figure 7–36(c) if the meter resistance is 20 MΩ.

Open Multisim file E07-16 and verify the calculated results by using a 10 MΩ resistor to simulate the meter load.



SECTION 7–4 CHECKUP

1. Explain why a voltmeter can potentially load a circuit.
2. If a voltmeter with a 10 MΩ internal resistance is measuring the voltage across a 10 kΩ resistor, should you normally be concerned about the loading effect?
3. If a voltmeter with a 10 MΩ resistance is measuring the voltage across a 3.3 MΩ resistor, should you be concerned about the loading effect?
4. What is the internal series resistance of a 20,000 Ω/V VOM if it is on the 200 V range?

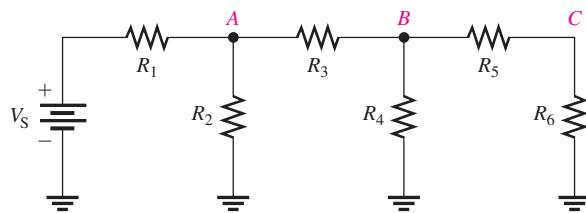
7–5 LADDER NETWORKS

A resistive ladder network is a special type of series-parallel circuit. The $R/2R$ resistive ladder network is introduced in this section. It is commonly used to scale down a set of binary input voltages to certain weighted values for digital-to-analog conversion. The complete circuit includes an operational amplifier (*op-amp*) that determines the maximum output voltage. This introduction only includes the $R/2R$ ladder portion of the circuit.

After completing this section, you should be able to

- ◆ Analyze ladder networks
 - ◆ Determine the voltages in a three-step ladder network
 - ◆ Analyze an $R/2R$ ladder

One approach to the analysis of a ladder network such as the one shown in Figure 7–37 is to simplify it one step at a time, starting at the side farthest from the source. In this way, you can determine the current in any branch or the voltage at any node.



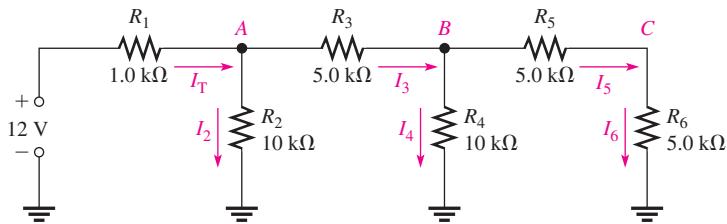
▲ FIGURE 7-37

Basic three-step ladder network.

Ladder networks such as the one in Figure 7-37 can be useful in certain cases. For example, you can use a ladder to provide certain reference voltages. See if you can figure out V_A , V_B , and V_C in the following example before looking at the solution.

EXAMPLE 7-17

Determine the current through each resistor and the voltage at each labeled node with respect to ground in the ladder network of Figure 7-38.



▲ FIGURE 7-38

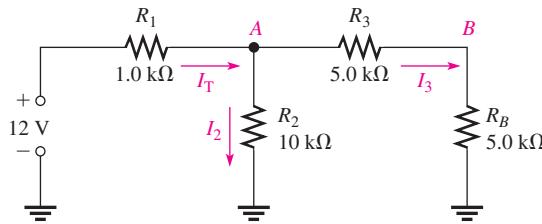
Solution

To find the current through each resistor, you must know the total current from the source (I_T). To obtain I_T , you must find the total resistance “seen” by the source.

Determine R_T in a step-by-step process, starting at the right of the circuit diagram. First, notice that R_5 and R_6 are in series across R_4 . Neglecting the circuit to the left of node B , the resistance from node B to ground is

$$R_B = \frac{R_4(R_5 + R_6)}{R_4 + (R_5 + R_6)} = \frac{(10 \text{ k}\Omega)(10 \text{ k}\Omega)}{20.0 \text{ k}\Omega} = 5.0 \text{ k}\Omega$$

Using R_B , you can draw the equivalent circuit as shown in Figure 7-39.



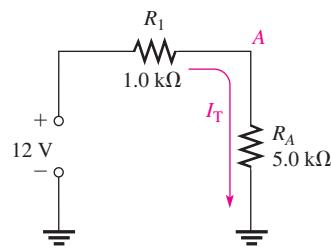
▲ FIGURE 7-39

Next, neglecting the circuit to the left of node A , the resistance from node A to ground (R_A) is R_2 in parallel with the series combination of R_3 and R_B . Calculate resistance R_A .

$$R_A = \frac{R_2(R_3 + R_B)}{R_2 + (R_3 + R_B)} = \frac{(10 \text{ k}\Omega)(10 \text{ k}\Omega)}{20 \text{ k}\Omega} = 5.0 \text{ k}\Omega$$

Using R_A , you can further simplify the equivalent circuit of Figure 7–39 as shown in Figure 7–40.

► FIGURE 7–40



Finally, the total resistance “seen” by the source is R_1 in series with R_A .

$$R_T = R_1 + R_A = 1.0 \text{ k}\Omega + 5.0 \text{ k}\Omega = 6.0 \text{ k}\Omega$$

The total circuit current is

$$I_T = \frac{V_S}{R_T} = \frac{12 \text{ V}}{6.0 \text{ k}\Omega} = 2.00 \text{ mA}$$

As indicated in Figure 7–39, I_T is into node A and divides between R_2 and the branch containing $R_3 + R_B$. Since the branch resistances are equal in this particular example, half the total current is through R_2 and half into node B . So the currents through R_2 and R_3 are

$$\begin{aligned} I_2 &= 1.00 \text{ mA} \\ I_3 &= 1.00 \text{ mA} \end{aligned}$$

If the branch resistances are not equal, use the current-divider formula. As indicated in Figure 7–38, I_3 is into node B and is divided between R_4 and the branch containing $R_5 + R_6$. Therefore, the currents through R_4 , R_5 , and R_6 can be calculated.

$$I_4 = \left(\frac{R_5 + R_6}{R_4 + (R_5 + R_6)} \right) I_3 = \left(\frac{10 \text{ k}\Omega}{20 \text{ k}\Omega} \right) 1.00 \text{ mA} = 0.50 \text{ mA}$$

$$I_5 = I_6 = I_3 - I_4 = 1.00 \text{ mA} - 0.50 \text{ mA} = 0.50 \text{ mA}$$

To determine V_A , V_B , and V_C , apply Ohm’s law.

$$V_A = I_2 R_2 = (1.00 \text{ mA})(10 \text{ k}\Omega) = 10.0 \text{ V}$$

$$V_B = I_4 R_4 = (0.50 \text{ mA})(10 \text{ k}\Omega) = 5.00 \text{ V}$$

$$V_C = I_6 R_6 = (0.50 \text{ mA})(5.0 \text{ k}\Omega) = 2.50 \text{ V}$$

Related Problem Recalculate V_A , V_B , and V_C in Figure 7–38 if R_1 is increased to 5.0 kΩ.

Use Multisim file E07-17 to verify the calculated results in this example and to confirm your calculations for the related problem.

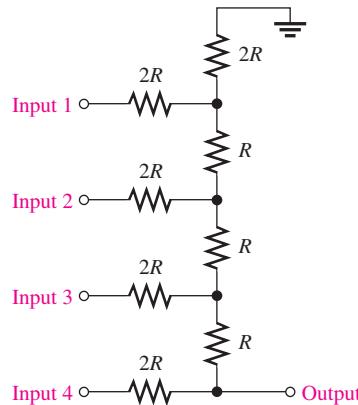


The $R/2R$ Ladder Network

The general form for an $R/2R$ ladder network is shown in Figure 7–41. As you can see, the name comes from the relationship of the resistor values. R represents a common value, and one set of resistors has twice the value of the others. This type of ladder

► FIGURE 7-41

A basic four-step $R/2R$ ladder network.

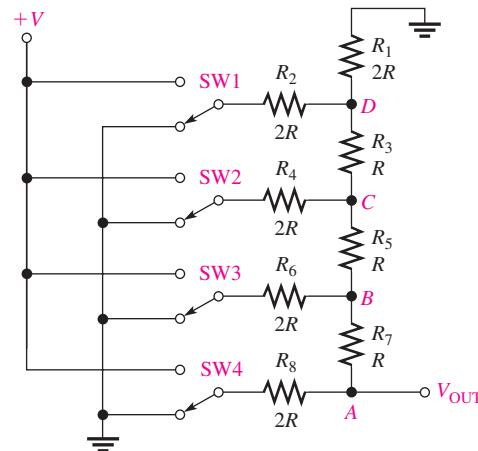


network is used in applications where digital codes are converted to speech, music, or other types of analog signals as found, for example, in the area of digital recording and reproduction. This application is called *digital-to-analog (D/A) conversion*.

Let's examine the general operation of a basic $R/2R$ ladder using the four-step circuit in Figure 7-42. In a later course in digital fundamentals, you will learn specifically how this type of circuit is used in D/A conversion.

► FIGURE 7-42

$R/2R$ ladder with switch inputs to simulate a two-level (digital) code.



The switches used in this illustration simulate the digital (two-level) inputs. One switch position is connected to ground (0 V), and the other position is connected to a positive voltage (V). The analysis is as follows: Start by assuming that switch SW4 in Figure 7-42 is at the V position and the others are at ground so that the inputs are as shown in Figure 7-43(a).

The total resistance from node A to ground is found by first combining R_1 and R_2 in parallel from node D to ground. The simplified circuit is shown in Figure 7-43(b).

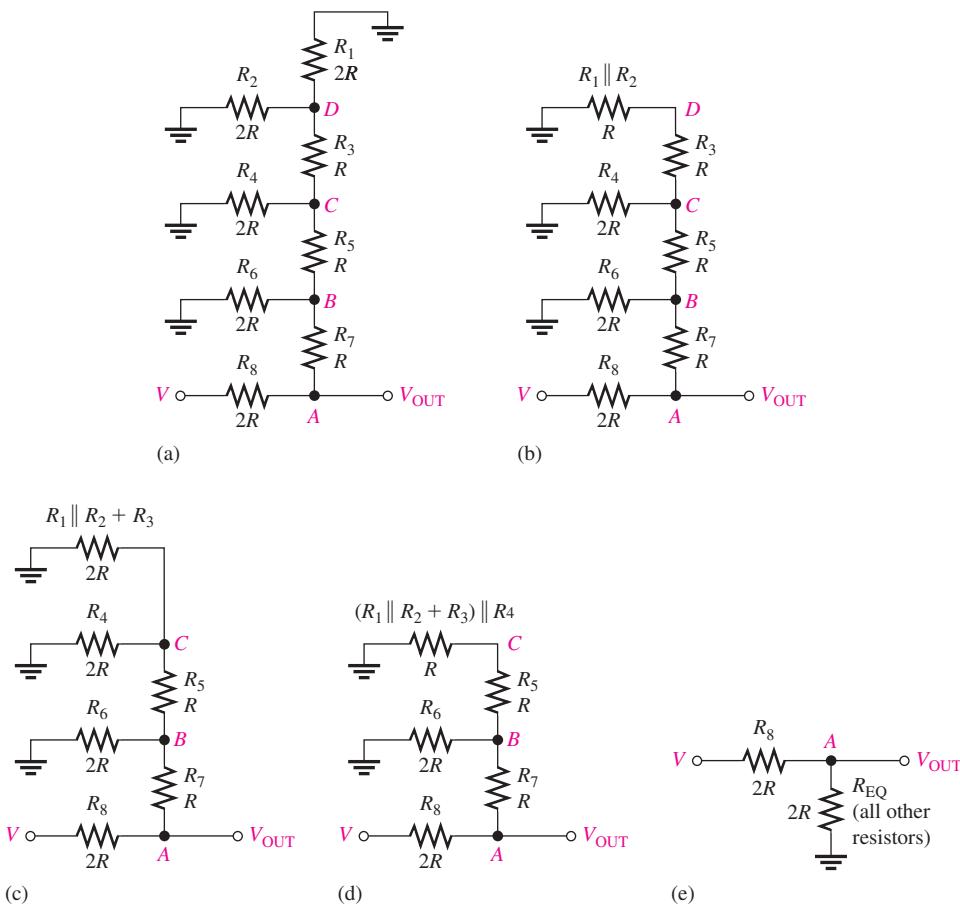
$$R_1 \parallel R_2 = \frac{2R}{2} = R$$

$R_1 \parallel R_2$ is in series with R_3 from node C to ground as illustrated in part (c).

$$R_1 \parallel R_2 + R_3 = R + R = 2R$$

Next, this combination is in parallel with R_4 from node C to ground as shown in part (d).

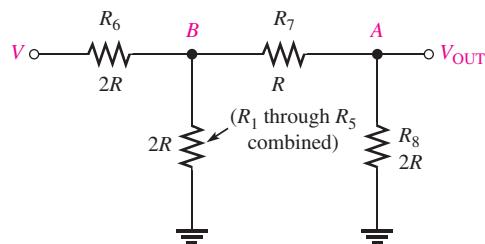
$$(R_1 \parallel R_2 + R_3) \parallel R_4 = 2R \parallel 2R = \frac{2R}{2} = R$$

**▲ FIGURE 7-43**Simplification of $R/2R$ ladder for analysis.

Continuing this simplification process results in the circuit in part (e) in which the output voltage can be expressed using the voltage-divider formula as

$$V_{\text{OUT}} = \left(\frac{2R}{4R} \right) V = \frac{V}{2}$$

A similar analysis, except with switch SW3 in Figure 7-42 connected to V and the other switches connected to ground, results in the simplified circuit shown in Figure 7-44.

**▲ FIGURE 7-44**Simplified ladder with only V input at SW3 in Figure 7-42.

The analysis for this case is as follows: The resistance from node *B* to ground is

$$R_B = (R_7 + R_8) \parallel 2R = 3R \parallel 2R = \frac{6R}{5}$$

Using the voltage-divider formula, we can express the voltage at node *B* with respect to ground as

$$\begin{aligned} V_B &= \left(\frac{R_B}{R_6 + R_B} \right) V = \left(\frac{6R/5}{2R + 6R/5} \right) V = \left(\frac{6R/5}{10R/5 + 6R/5} \right) V = \left(\frac{6R/5}{16R/5} \right) V \\ &= \left(\frac{6R}{16R} \right) V = \frac{3V}{8} \end{aligned}$$

The output voltage is, therefore,

$$V_{\text{OUT}} = \left(\frac{R_8}{R_7 + R_8} \right) V_B = \left(\frac{2R}{3R} \right) \left(\frac{3V}{8} \right) = \frac{V}{4}$$

Notice that the output voltage in this case ($V/4$) is one-half the output voltage ($V/2$) for the case where V is connected at switch SW4.

A similar analysis for each of the remaining switch inputs in Figure 7–42 results in output voltages as follows: For SW2 connected to V and the other switches connected to ground,

$$V_{\text{OUT}} = \frac{V}{8}$$

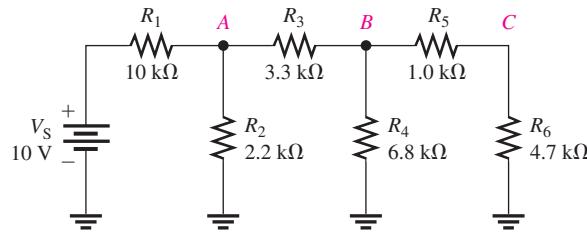
For SW1 connected to V and the other switches connected to ground,

$$V_{\text{OUT}} = \frac{V}{16}$$

When more than one input at a time are connected to V , the total output is the sum of the individual outputs, according to the superposition theorem that is covered in Section 8–4. These particular relationships among the output voltages for the various levels of inputs are important in the application of $R/2R$ ladder networks to digital-to-analog conversion.

SECTION 7–5 CHECKUP

1. Draw a basic four-step ladder network.
2. Determine the total circuit resistance presented to the source by the ladder network of Figure 7–45.
3. What is the total current in Figure 7–45?
4. What is the current through R_2 in Figure 7–45?
5. What is the voltage at node *A* with respect to ground in Figure 7–45?



▲ FIGURE 7–45

7–6 THE WHEATSTONE BRIDGE

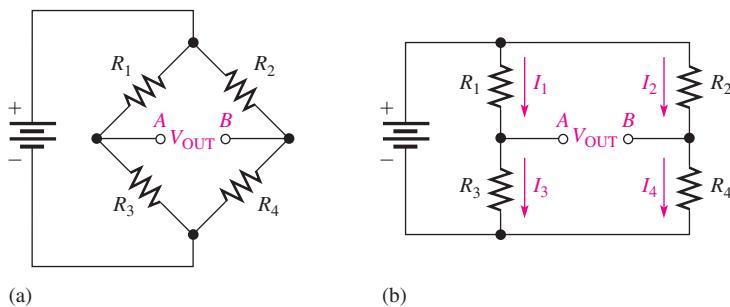
The Wheatstone bridge circuit can be used to precisely measure resistance. However, the bridge is most commonly used in conjunction with transducers to measure physical quantities such as strain, temperature, and pressure.

Transducers are devices that sense a change in a physical parameter and convert that change into an electrical quantity such as a change in resistance. For example, a strain gauge exhibits a change in resistance when it is exposed to mechanical factors such as force, pressure, or displacement. A thermistor exhibits a change in its resistance when it is exposed to a change in temperature. The Wheatstone bridge can be operated in a balanced or an unbalanced condition. The condition of operation depends on the type of application.

After completing this section, you should be able to

- ◆ **Analyze and apply a Wheatstone bridge**
 - ◆ Determine when a bridge is balanced
 - ◆ Determine an unknown resistance with a balanced bridge
 - ◆ Determine when a bridge is unbalanced
 - ◆ Discuss measurements using an unbalanced bridge

A **Wheatstone bridge** circuit is shown in its most common “diamond” configuration in Figure 7–46(a). It consists of four resistors and a dc voltage source connected across the top and bottom points of the “diamond.” The output voltage is taken across the left and right points of the “diamond” between *A* and *B*. In part (b), the circuit is drawn in a slightly different way to more clearly show its series-parallel configuration.



▲ FIGURE 7–46

Wheatstone bridge. Notice that the bridge forms two back-to-back voltage dividers.

The Balanced Wheatstone Bridge

The Wheatstone bridge in Figure 7–46 is in the **balanced bridge** condition when the output voltage (V_{OUT}) between terminals *A* and *B* is equal to zero.

$$V_{\text{OUT}} = 0 \text{ V}$$

When the bridge is balanced, the voltages across R_1 and R_2 are equal ($V_1 = V_2$) and the voltages across R_3 and R_4 are equal ($V_3 = V_4$). Therefore, the voltage ratios can be written as

$$\frac{V_1}{V_3} = \frac{V_2}{V_4}$$

Substituting IR for V by Ohm's law gives

$$\frac{I_1 R_1}{I_3 R_3} = \frac{I_2 R_2}{I_4 R_4}$$

HISTORY NOTE



Sir Charles
Wheatstone
1802–1875

Wheatstone was a British scientist and inventor of many scientific breakthroughs, including the English concertina, the stereoscope (a device for displaying three-dimensional images), and an encryption cipher technique. Wheatstone is best known for his contributions in the development of the Wheatstone bridge, originally invented by Samuel Hunter Christie, and as a major figure in the development of telegraphy. Wheatstone gave full credit for the invention to Christie, but because of his work in developing and applying the bridge, it became known as the Wheatstone bridge. (Photo credit: AIP Emilio Segrè Visual Archives, Brittle Books Collection.)

Since $I_1 = I_3$ and $I_2 = I_4$, all the current terms cancel, leaving the resistor ratios.

$$\frac{R_1}{R_3} = \frac{R_2}{R_4}$$

Solving for R_1 results in the following formula:

$$R_1 = R_3 \left(\frac{R_2}{R_4} \right)$$

This formula allows you to find the value of resistor R_1 in terms of the other resistor values when the bridge is balanced. You can also find the value of any other resistor in a similar way.

Using the Balanced Wheatstone Bridge to Find an Unknown Resistance Assume that R_1 in Figure 7–46 has an unknown value, which we call R_X . Resistors R_2 and R_4 have fixed values so that their ratio, R_2/R_4 , also has a fixed value. Since R_X can be any value, R_3 must be adjusted to make $R_1/R_3 = R_2/R_4$ in order to create a balanced or *null* condition. A null measurement is a very precise comparison method in which the voltage difference between the arms of the bridge is zero. Therefore, R_3 is a variable resistor, which we will call R_V . When R_X is placed in the bridge, R_V is adjusted until the bridge is balanced as indicated by a zero output voltage. Then, the unknown resistance is found as

Equation 7–2

$$R_X = R_V \left(\frac{R_2}{R_4} \right)$$

The ratio R_2/R_4 is the scale factor. The accuracy of the measurement is dependent only on the accuracy of the bridge resistors and any stray wiring resistance as any loading effect is negligible.

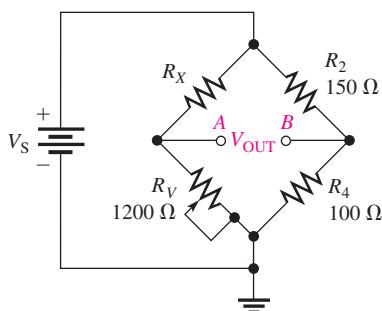
An older type of measuring instrument called a *galvanometer* can be connected between the output terminals A and B to detect a balanced condition. The galvanometer is essentially a very sensitive analog ammeter that senses current in either direction. It differs from a regular ammeter in that the midscale point is zero. In modern instruments, an amplifier connected across the bridge output indicates a balanced condition when its output is 0 V. Typically, the amplifier is an operational amplifier (op-amp) that can significantly increase the sensitivity of the bridge. This idea is illustrated in the application assignment for this chapter. In demanding applications high precision micro-adjustable resistors can be used. The micro-adjustable resistors enable fine adjustment of the bridge resistors during manufacture for applications such as medical sensors, scales, and precision measurements.

From Equation 7–2, the value of R_V at balance multiplied by the scale factor R_2/R_4 is the actual resistance value of R_X . If $R_2/R_4 = 1$, then $R_X = R_V$; if $R_2/R_4 = 0.5$, then $R_X = 0.5R_V$; and so on. In a practical bridge circuit, the position of the R_V adjustment can be calibrated to indicate the actual value of R_X on a scale or with some other method of display.

EXAMPLE 7–18

Determine the value of R_X in the balanced bridge shown in Figure 7–47.

► FIGURE 7–47



Solution The scale factor is

$$\frac{R_2}{R_4} = \frac{150 \Omega}{100 \Omega} = 1.5$$

The bridge is balanced ($V_{\text{OUT}} = 0 \text{ V}$) when R_V is set at $1,200 \Omega$, so the unknown resistance is

$$R_X = R_V \left(\frac{R_2}{R_4} \right) = (1,200 \Omega)(1.5) = 1,800 \Omega$$

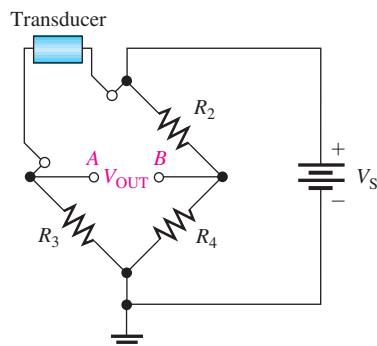
Related Problem If R_V must be adjusted to $2.2 \text{ k}\Omega$ to balance the bridge in Figure 7–47, what is R_X ?



Use Multisim file E07-18 to verify the calculated results in this example and to confirm your calculation for the related problem.

The Unbalanced Wheatstone Bridge

An **unbalanced bridge** condition occurs when V_{OUT} is not equal to zero. The unbalanced bridge is used to measure several types of physical quantities such as mechanical strain, temperature, or pressure. This can be done by connecting a transducer in one leg of the bridge, as shown in Figure 7–48. The resistance of the transducer changes proportionally to the changes in the parameter that it is measuring. If the bridge is balanced at a known point, then the amount of deviation from the balanced condition, as indicated by the output voltage, indicates the amount of change in the parameter being measured. Therefore, the value of the parameter being measured can be determined by the amount that the bridge is unbalanced.



◀ FIGURE 7–48

A bridge circuit for measuring a physical parameter using a transducer.

A Bridge Circuit for Measuring Temperature If temperature is to be measured, the transducer can be a thermistor, which is a temperature-sensitive resistor. The thermistor resistance changes in a predictable way as the temperature changes. A change in temperature causes a change in thermistor resistance, which causes a corresponding change in the output voltage of the bridge as it becomes unbalanced. The output voltage is proportional to the temperature; therefore, either a voltmeter connected across the output can be calibrated to show the temperature or the output voltage can be amplified and converted to digital form to drive a readout display of the temperature.

A bridge circuit used to measure temperature is designed so that it is balanced at a reference temperature and becomes unbalanced at a measured temperature. For example, let's say the bridge is to be balanced at 25°C . A thermistor will have a known value of resistance at 25°C . For simplicity, let's assume the other three bridge resistors are equal to the thermistor resistance at 25°C , so $R_{\text{therm}} = R_2 = R_3 = R_4$. For this particular case,

the change in output voltage (ΔV_{OUT}) can be shown to be related to the change in R_{therm} by the following formula:

Equation 7-3

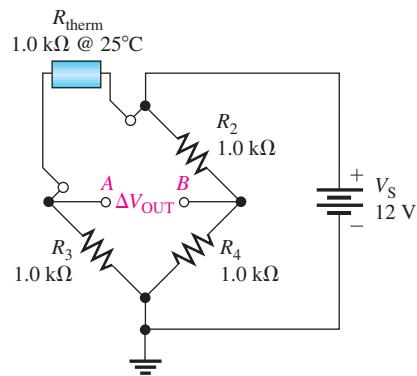
$$\Delta V_{\text{OUT}} \approx \Delta R_{\text{therm}} \left(\frac{V_s}{4R} \right)$$

The Δ (Greek letter delta) in front of a variable means a change in the variable. This formula applies only to the case where all resistances in the bridge are equal when the bridge is balanced. A derivation is provided in Appendix B. Keep in mind that the bridge can be initially balanced without having all the resistors equal as long as $R_1 = R_2$ and $R_3 = R_4$ (see Figure 7-46), but the formula for ΔV_{OUT} would be more complicated.

EXAMPLE 7-19

Determine the output voltage of the temperature-measuring bridge circuit in Figure 7-49 if the thermistor is exposed to a temperature of 50°C and its resistance at 25°C is 1.0 kΩ. Assume the resistance of the thermistor decreases to 900 Ω at 50°C.

► FIGURE 7-49

*Solution*

$$\Delta R_{\text{therm}} = 1.0 \text{ k}\Omega - 900 \Omega = 100 \Omega$$

$$\Delta V_{\text{OUT}} \approx \Delta R_{\text{therm}} \left(\frac{V_s}{4R} \right) = 100 \Omega \left(\frac{12 \text{ V}}{4 \text{ k}\Omega} \right) = 0.3 \text{ V}$$

Since $V_{\text{OUT}} = 0 \text{ V}$ when the bridge is balanced at 25°C and it changes 0.3 V, then

$$V_{\text{OUT}} = 0.3 \text{ V}$$

when the temperature is 50°C.

Related Problem

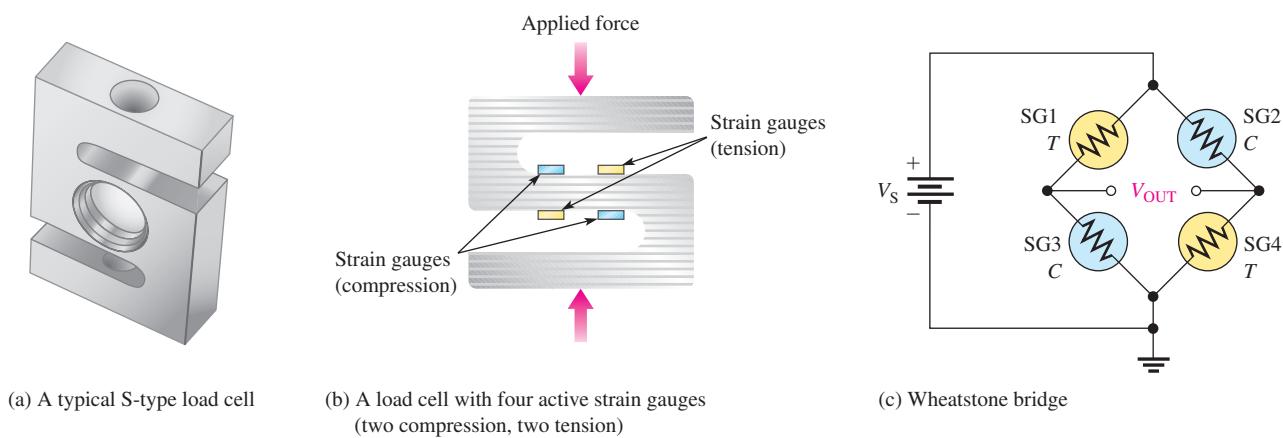
If the temperature is increased to 60°C, causing the thermistor resistance in Figure 7-49 to decrease to 850 Ω, what is V_{OUT} ?

Other Unbalanced Wheatstone Bridge Applications A Wheatstone bridge with a strain gauge can be used to measure forces. A strain gauge is a device that exhibits a change in resistance when it is compressed or stretched by the application of an external force. As the resistance of the strain gauge changes, the previously balanced bridge becomes unbalanced. This unbalance causes the output voltage to change from zero, and this change can be measured to determine the amount of strain. In strain gauges, the resistance change is extremely small. This tiny change unbalances a Wheatstone bridge and can be detected because of its high sensitivity.

Strain gauges are one of the most useful resistive transducers that convert the stretching or compression of a fine wire into a change in resistance. When strain causes the wire

in the gauge to stretch, the resistance increases a small amount; and when it compresses, the resistance of the wire decreases.

Strain gauges are used in many types of scales, from those that are used for weighing small parts to those for weighing huge trucks. Typically, the gauges are mounted on a special block of aluminum that deforms when a weight is on the scale. The strain gauges are extremely delicate and must be mounted properly, so the entire assembly is generally prepared as a single unit called a *load cell*. A **load cell** is a transducer that uses strain gauges to convert mechanical force into an electrical signal. A wide variety of load cells with different shapes and sizes are available from manufacturers depending on the application. A typical S-type load cell for a weighing application is shown Figure 7–50(a). The load cell has four strain gauges as illustrated in Figure 7–50(b). The gauges are mounted so that two of the gauges stretch (tension) when a load is placed on the scale and two of the gauges compress.



(a) A typical S-type load cell

(b) A load cell with four active strain gauges
(two compression, two tension)

(c) Wheatstone bridge

▲ FIGURE 7-50

Wheatstone bridge in a scale application.

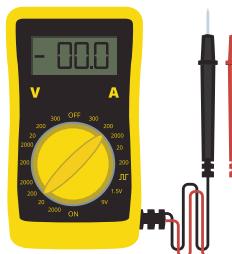
Load cells are usually connected to a Wheatstone bridge as shown in Figure 7–50(c) with strain gauges (SG) in tension (T) and compression (C) in opposite diagonal legs as shown. The output of the bridge is normally digitized and converted to a reading for a display or sent to a computer for processing. The major advantage of the Wheatstone bridge circuit is that it is capable of accurately measuring very small differences in resistance. The use of four active transducers increases the sensitivity of the measurement and makes the bridge the ideal circuit for instrumentation. The Wheatstone bridge circuit has the added benefit of compensating for temperature variations and wire resistance of connecting wires that would otherwise contribute to inaccuracies.

In addition to scales, strain gauges are used with Wheatstone bridges in other types of measurements including pressure measurements, displacement and acceleration measurements to name a few. In pressure measurements, the strain gauges are bonded to a flexible diaphragm that stretches when pressure is applied to the transducer. The amount of flexing is related to the pressure, which again converts to a very small resistance change.

SECTION 7-6 CHECKUP

1. Draw a basic Wheatstone bridge circuit.
2. Under what condition is a bridge balanced?
3. What is the unknown resistance in Figure 7–47 when $R_V = 3.3 \text{ k}\Omega$, $R_2 = 10 \text{ k}\Omega$, and $R_4 = 2.2 \text{ k}\Omega$?
4. How is a Wheatstone bridge used in the unbalanced condition?
5. What is a load cell?

7-7 TROUBLESHOOTING



As you know, troubleshooting is the process of identifying and locating a failure or problem in a circuit. Some troubleshooting techniques and the application of logical thought have already been discussed in relation to both series circuits and parallel circuits. A basic premise of troubleshooting is that you must know what to look for before you can successfully troubleshoot a circuit.

After completing this section, you should be able to

- ◆ **Troubleshoot series-parallel circuits**
 - ◆ Determine the effects of an open in a circuit
 - ◆ Determine the effects of a short in a circuit
 - ◆ Locate opens and shorts

Opens and shorts are typical problems that occur in electric circuits. As mentioned in Chapter 5, if a resistor burns out, it will normally produce an open. Bad solder connections, broken wires, and poor contacts can also be causes of open paths. Pieces of foreign material, such as solder splashes and broken insulation on wires, can lead to shorts in a circuit. A short is considered to be a zero resistance path between two points.

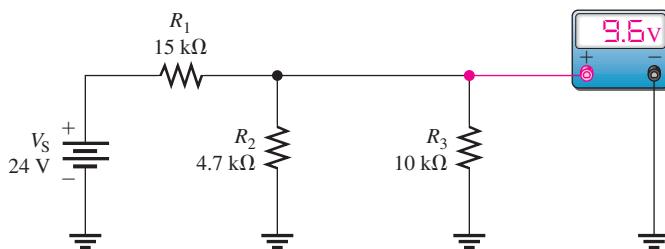
In addition to complete opens or shorts, partial opens or partial shorts can develop in a circuit. A partial open would be a much higher than normal resistance, but not infinitely large. A partial short would be a much lower than normal resistance, but not zero.

The following three examples illustrate troubleshooting series-parallel circuits.

EXAMPLE 7-20

From the indicated voltmeter reading in Figure 7-51, determine if there is a fault by applying the APM approach. If there is a fault, identify it as either a short or an open.

► FIGURE 7-51



Solution **Step 1:** Analysis

Determine what the voltmeter should be indicating as follows. Since R_2 and R_3 are in parallel, their combined resistance is

$$R_{2\parallel 3} = \frac{R_2 R_3}{R_2 + R_3} = \frac{(4.7 \text{ k}\Omega)(10 \text{ k}\Omega)}{14.7 \text{ k}\Omega} = 3.20 \text{ k}\Omega$$

Determine the voltage across the parallel combination by the voltage-divider formula.

$$V_{2\parallel 3} = \left(\frac{R_{2\parallel 3}}{R_1 + R_{2\parallel 3}} \right) V_S = \left(\frac{3.2 \text{ k}\Omega}{18.2 \text{ k}\Omega} \right) 24 \text{ V} = 4.22 \text{ V}$$

This calculation shows that 4.22 V is the voltage reading that you should get on the meter. However, the meter reads 9.6 V across $R_2 \parallel R_3$. This value is incorrect, and because it is higher than it should be, either R_2 or R_3 is probably open. Why? Because if either of these two resistors is open, the resistance across which the meter is connected is larger than expected. A higher resistance will drop a higher voltage in this circuit.

There are other ways to analyze the problem. For example, you might decide to find the total current in the circuit by applying Ohm's law to R_1 . The total resistance of $R_2 \parallel R_3$ would be 10 k Ω , which matches R_3 . Thus R_2 must be open.

Step 2: Planning

Start trying to find the open resistor by assuming that R_2 is open. If it is, the voltage across R_3 is

$$V_3 = \left(\frac{R_3}{R_1 + R_3} \right) V_S = \left(\frac{10 \text{ k}\Omega}{25 \text{ k}\Omega} \right) 24 \text{ V} = 9.6 \text{ V}$$

Since the measured voltage is also 9.6 V, this calculation shows that R_2 is probably open.

Step 3: Measurement

Disconnect power and remove R_2 . Measure its resistance to verify it is open. If it is not, inspect the wiring, solder, or connections around R_2 , looking for the open.

Related Problem

What would be the voltmeter reading if R_3 were open in Figure 7–51? If R_1 were open?

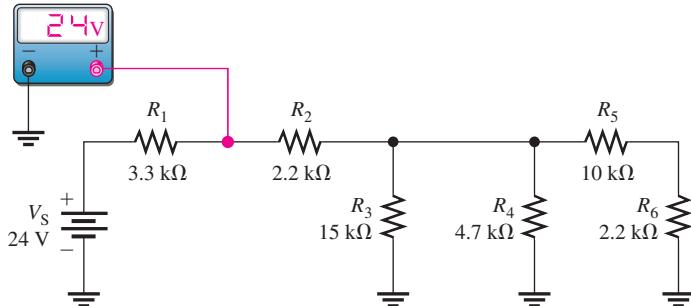


Open Multisim file E07-20 and verify the result in this example by removing R_2 to simulate an open and observe the meter reading.

EXAMPLE 7–21

Suppose that you measure 24 V with the voltmeter in Figure 7–52. Determine if there is a fault, and, if there is, identify it.

► FIGURE 7–52



Solution

Step 1: Analysis

There is no voltage drop across R_1 because both sides of the resistor are at +24 V. Either there is no current through R_1 from the source, which tells you that R_2 is open in the circuit, or R_1 is shorted.

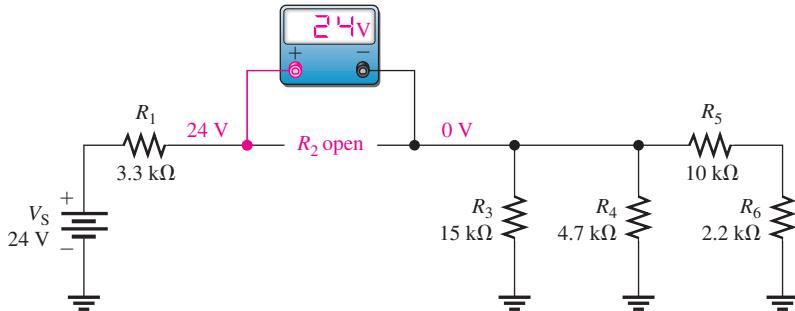
Step 2: Planning

The most probable failure is an open R_2 . If it is open, then there will be no current from the source. To verify this, measure across R_2 with the voltmeter. If R_2 is open, the meter will indicate 24 V. The right side of R_2 will be at zero volts because there is no current through any of the other resistors to cause a voltage drop across them.

Step 3: Measurement

The measurement to verify that R_2 is open is shown in Figure 7–53.

► FIGURE 7-53

**Related Problem**

What would be the voltage across an open R_5 in Figure 7-52 assuming no other faults?

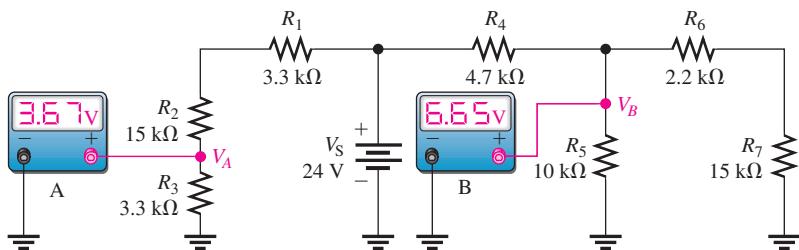


Open Multisim file E07-21 and confirm the fault by measurement.

EXAMPLE 7-22

The two voltmeters in Figure 7-54 indicate the voltages shown. Apply logical thought and your knowledge of circuit operation to determine if there are any opens or shorts in the circuit and, if so, where they are located.

► FIGURE 7-54

**Solution**

Step 1: Determine if the voltmeter readings are correct. R_1 , R_2 , and R_3 act as a voltage divider. Calculate the voltage (V_A) across R_3 as follows:

$$V_A = \left(\frac{R_3}{R_1 + R_2 + R_3} \right) V_S = \left(\frac{3.3 \text{ k}\Omega}{21.6 \text{ k}\Omega} \right) 24 \text{ V} = 3.67 \text{ V}$$

The voltmeter A reading is correct. This indicates that R_1 , R_2 , and R_3 are connected and are not faulty.

Step 2: See if the voltmeter B reading is correct. $R_6 + R_7$ is in parallel with R_5 . The series-parallel combination of R_5 , R_6 , and R_7 is in series with R_4 . Calculate the resistance of the R_5 , R_6 , and R_7 combination as follows:

$$R_{5\parallel(6+7)} = \frac{R_5(R_6 + R_7)}{R_5 + R_6 + R_7} = \frac{(10 \text{ k}\Omega)(17.2 \text{ k}\Omega)}{27.2 \text{ k}\Omega} = 6.32 \text{ k}\Omega$$

$R_{5\parallel(6+7)}$ and R_4 form a voltage divider, and voltmeter B measures the voltage across $R_{5\parallel(6+7)}$. Is it correct? Check as follows:

$$V_B = \left(\frac{R_{5\parallel(6+7)}}{R_4 + R_{5\parallel(6+7)}} \right) V_S = \left(\frac{6.32 \text{ k}\Omega}{11 \text{ k}\Omega} \right) 24 \text{ V} = 13.8 \text{ V}$$

Thus, the actual measured voltage (6.65 V) at this point is incorrect. Some logical thinking will help to isolate the problem.

Step 3: R_4 is not open, because if it were, the meter would read 0 V. If there were a short across it, the meter would read 24 V. Since the actual voltage is much less than it should be, $R_{5\parallel(6+7)}$ must be less than the calculated value of 6.32 k Ω . The most likely problem is a short across R_7 . If there is a short from the top of R_7 to ground, R_6 is effectively in parallel with R_5 . In this case,

$$R_{5\parallel R_6} = \frac{R_5 R_6}{R_5 + R_6} = \frac{(10 \text{ k}\Omega)(2.2 \text{ k}\Omega)}{12.2 \text{ k}\Omega} = 1.80 \text{ k}\Omega$$

Then V_B is

$$V_B = \left(\frac{1.80 \text{ k}\Omega}{6.5 \text{ k}\Omega} \right) 24 \text{ V} = 6.65 \text{ V}$$

This value for V_B agrees with the voltmeter B reading. So there is a short across R_7 . If this were an actual circuit, you would try to find the physical cause of the short.

Related Problem

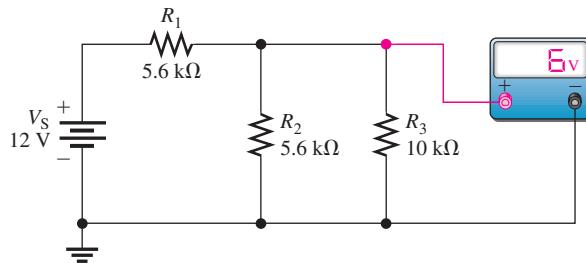
If the only fault in Figure 7–54 is that R_2 is shorted, what will voltmeter A read? What will voltmeter B read?

Open Multisim file E07-22 and verify the fault by measurement.



SECTION 7–7 CHECKUP

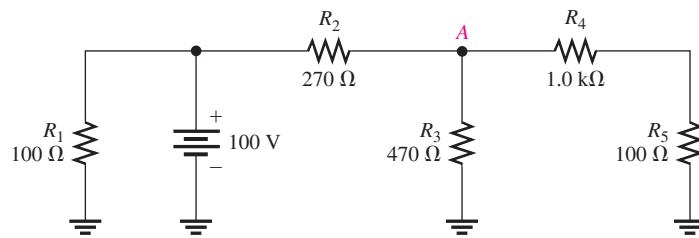
1. Name two types of common circuit faults.
2. In Figure 7–55, one of the resistors in the circuit is open. Based on the meter reading, determine which is the open resistor.



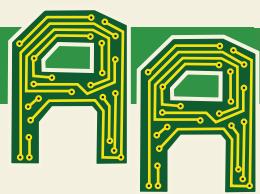
▲ FIGURE 7-55

3. For the following faults in Figure 7–56, what voltage would be measured at node A with respect to ground?

- (a) No faults (b) R_1 open (c) Short across R_5 (d) R_3 and R_4 open
- (e) R_2 open



▲ FIGURE 7-56



Application Activity

The Wheatstone bridge is widely used in measurement applications that use sensors to convert a physical parameter to a change in resistance. Modern Wheatstone bridges are automated; with intelligent interface modules, the output can be conditioned and converted to any desired unit for display or processing (for example, the output might be displayed in pounds for a scale application).

The Wheatstone bridge provides a null measurement, which enables it to have great sensitivity. It can also be designed to compensate for changes in temperature, a great advantage for many resistive measurements, particularly when the resistance change of the sensor is very small. Usually, the output voltage of the bridge is increased by an amplifier that has a minimum loading effect on the bridge.

Temperature Controller

In this application activity, a Wheatstone bridge circuit is used in a temperature controller. The sensor is a thermistor ("thermal resistor"), which is a resistive sensor that changes resistance as temperature changes. Thermistors are available with positive or negative resistance characteristics as a function of temperature. The thermistor in this circuit is one of the resistors in a Wheatstone bridge but is located a short distance from the circuit board for sensing the temperature at a point off the board. The threshold voltage for the output to change is controlled by the $10\text{ k}\Omega$ potentiometer, R_3 .

The operational amplifier (741C) and LEDs are shown only to provide a context for this Wheatstone bridge application and are not intended to be the focus. The op-amp in this circuit is configured as a *comparator*, which is used to compare the voltage on one side of the bridge with the voltage on the other.

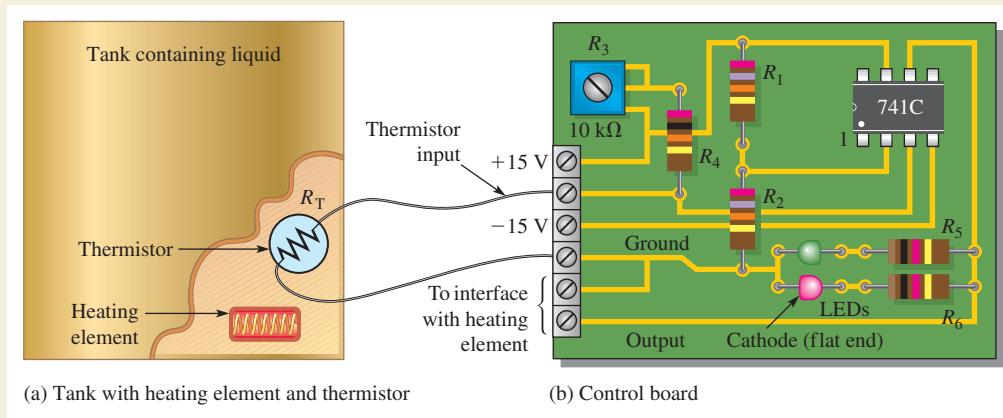
The advantage of a comparator is that it is extremely sensitive to an unbalanced bridge and produces a large output when the bridge is unbalanced. In fact, it is so sensitive, it is virtually impossible to adjust the bridge for perfect balance. Even the tiniest imbalance will cause the output to go to a voltage near either the maximum or minimum possible (the power supply voltages). This is handy for turning on a heater or other device based on the temperature; however, there is a drawback to this circuit. The sensitivity of the op-amp is such that it can oscillate about the trip point. For purposes of this Application Assignment, we will assume that this does not matter. In courses on devices, you will learn a simple fix to this issue.

The Control Circuit

This application has a tank containing a liquid that needs to be held at a warm temperature, as illustrated in Figure 7–57(a). The circuit board for the temperature controller is shown in Figure 7–57(b). The circuit board controls a heating unit (through an interface, which is not shown) when the temperature is too cold. The thermistor, which is located in the tank, is connected between one of the amplifier inputs and ground as illustrated.

The op-amp has two inputs, an output, and connections for positive and negative supply voltages. The schematic symbol for the op-amp with light-emitting diodes (LEDs) connected to the output is shown in Figure 7–58. The red LED is on when the op-amp output is a positive voltage, indicating the heater is on. The green LED is on when the output is a negative voltage, indicating the heater is off.

1. Using the circuit board as a guide, complete the schematic in Figure 7–58. The op-amp inputs are connected to a Wheatstone bridge. Show the values for all resistors.



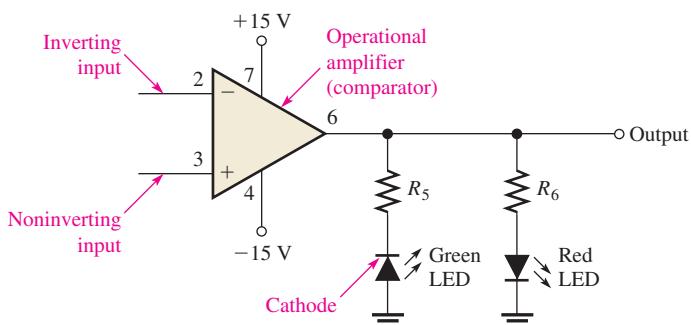
(a) Tank with heating element and thermistor

(b) Control board

▲ FIGURE 7-57

► FIGURE 7–58

The op-amp and output LED indicators.



The Thermistor

The thermistor is a mixture of two metal-oxides, which exhibit a large resistance change as a function of temperature. The thermistor in the temperature controller circuit is located off the board near the point where temperature is to be sensed in the tank and is connected between the thermistor input and ground.

Thermistors have a nonlinear resistance-temperature characteristic described by the exponential equation:

$$R_T = R_0 e^{\beta \left(\frac{T_0 - T}{T_0 T} \right)}$$

Where:

R_T = the resistance at a given temperature

R_0 = the resistance at a reference temperature

T_0 = the reference temperature in Kelvin (K), typically 298 K, which is 25°C

T = temperature in K

β = a constant (K) provided by the manufacturer

This exponential equation where e is the base of natural logarithms can be solved easily on a scientific calculator. Exponential equations are studied in Chapters 12 and 13.

The thermistor in this application is a Thermometrics RL2006-13.3K-140-D1 thermistor with a specified resistance of 25 kΩ at 25°C and a β of 4,615 K. For convenience, the resistance of this thermistor is plotted as a function of temperature in Figure 7–59. Notice that the negative slope indicates that this thermistor has a negative temperature coefficient (NTC); that is, its resistance decreases as the temperature increases. In the Application Activity for Chapter 8, the graph for this thermistor is plotted using a graphing calculator.

As an example, the calculation for finding the resistance at $T = 50^\circ\text{C}$ is shown. First, convert 50°C to K.

$$T = {}^\circ\text{C} + 273 = 50^\circ\text{C} + 273 = 323 \text{ K}$$

Also,

$$T_0 = {}^\circ\text{C} + 273 = 25^\circ\text{C} + 273 = 298 \text{ K}$$

$$R_0 = 25 \text{ k}\Omega$$

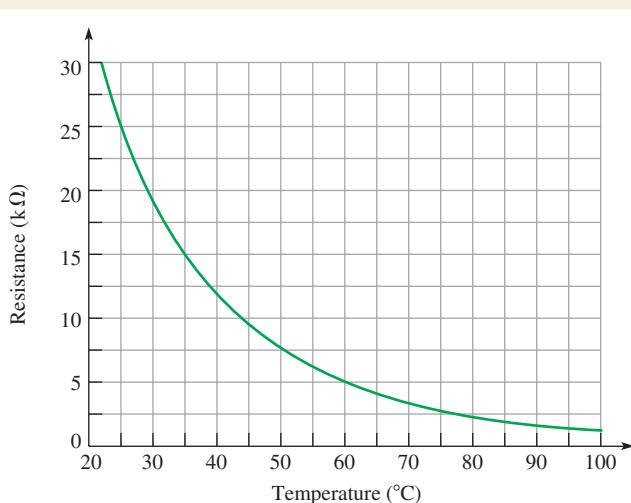
$$R_T = R_0 e^{\beta \left(\frac{T_0 - T}{T_0 T} \right)}$$

$$= (25 \text{ k}\Omega) e^{4615 \left(\frac{298 - 323}{298 \times 323} \right)}$$

$$= (25 \text{ k}\Omega) e^{-1.198}$$

$$= (25 \text{ k}\Omega)(0.302)$$

$$= 7.54 \text{ k}\Omega$$

► FIGURE 7–59

Using your calculator, first determine the value of the exponent $\beta(T_0 - T)/(T_0T)$. Next determine the value of the term

$e^{\beta\left(\frac{T_0-T}{T_0T}\right)}$. Finally, multiply by R_0 . On many calculators, e^x is a secondary function.

2. Calculate the resistance of the thermistor at a temperature of 40°C using the exponential equation and confirm that your calculation is correct by comparing your result with Figure 7–59. Remember that temperatures in the equation are in kelvin ($K = ^\circ C + 273$).
3. Calculate the resistance setting of R_3 to balance the bridge at 25°C.
4. Calculate the output voltage of the bridge (input to the op-amp) when the temperature of the thermistor is 40°C. Assume the bridge was balanced at 25°C and that the only change is the resistance of the thermistor.

5. If you needed to set the reference temperature to 0°C what simple change would you make to the circuit? Show with a calculation that your change will work and draw the revised schematic.

Review

6. At 25°C, the thermistor will have about 7.5 V across it. Calculate the power it dissipates.
7. As the temperature increases, does the thermistor resistance increase or decrease?
8. Can $\frac{1}{8}$ W resistors be used in this application? Explain your answer.
9. Why is only one LED on at a time at the output?

SUMMARY

- A series-parallel circuit is a combination of both series and parallel current paths.
- To determine total resistance in a series-parallel circuit, identify the series and parallel relationships, and then apply the formulas for series resistance and parallel resistance from Chapters 5 and 6.
- To find the total current, apply Ohm's law and divide the total voltage by the total resistance.
- To determine branch currents, apply the current-divider formula, Kirchhoff's current law, or Ohm's law. Consider each circuit problem individually to determine the most appropriate method.
- To determine voltage drops across any portion of a series-parallel circuit, use the voltage-divider formula, Kirchhoff's voltage law, or Ohm's law. Consider each circuit problem individually to determine the most appropriate method.
- When a load resistor is connected across a voltage-divider output, the output voltage decreases.
- The load resistor should be large compared to the resistance across which it is connected, in order that the loading effect may be minimized.
- To find total resistance of a ladder network, start at the point farthest from the source and reduce the resistance in steps.
- A balanced Wheatstone bridge can be used to measure an unknown resistance.
- The Wheatstone bridge is balanced when the output voltage is zero. The balanced condition produces zero current through a load connected across the output terminals of the bridge.
- An unbalanced Wheatstone bridge can be used to measure physical quantities using transducers.
- Opens and shorts are typical circuit faults.

KEY TERMS

These key terms are also in the end-of-book glossary.

Balanced bridge A bridge circuit that is in the balanced state is indicated by 0 V across the output.

Bleeder current The current left after the total load current is subtracted from the total current into the circuit.

Unbalanced bridge A bridge circuit that is in the unbalanced state as indicated by a voltage across the output that is proportional to the amount of deviation from the balanced state.

Wheatstone bridge A 4-legged type of bridge circuit with which an unknown resistance can be accurately measured using the balanced state. Deviations in resistance can be measured using the unbalanced state.

FORMULAS

- 7-1 $I_{\text{BLEEDER}} = I_T - I_{RL1} - I_{RL2}$ Bleeder current
- 7-2 $R_X = R_V \left(\frac{R_2}{R_4} \right)$ Unknown resistance in a Wheatstone bridge
- 7-3 $\Delta V_{\text{OUT}} \approx \Delta R_{\text{therm}} \left(\frac{V_S}{4R} \right)$ Thermistor output voltage change in a Wheatstone bridge

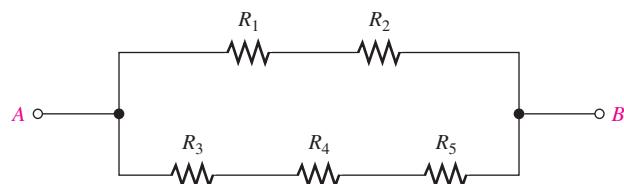
TRUE/FALSE QUIZ**Answers are at the end of the chapter.**

1. Parallel resistors are always connected between the same pair of nodes.
2. If one resistor is connected in series with a parallel combination, the series resistor will always have a larger voltage drop than the parallel resistors.
3. In a series-parallel combinational circuit, the same current will always be in parallel resistors.
4. A larger load resistor has a smaller loading effect on a voltage-divider.
5. Normally, a DMM will have a small loading effect on a circuit.
6. When measuring dc voltage, the input resistance of a DMM is the same no matter what scale it is used on.
7. When measuring dc voltage, the input resistance of an analog multimeter is the same no matter what scale it is used on.
8. An $R/2R$ ladder is used in digital-to-analog converters.
9. A Wheatstone bridge is balanced when its output voltage is negative.
10. A balanced Wheatstone bridge used to measure an unknown resistance is an example of a null measurement.
11. A strain gauge is a type of transducer that is frequently used in weighing scales.
12. A load cell is the same thing as a strain gauge.

SELF-TEST**Answers are at the end of the chapter.**

1. Which of the following statements are true concerning Figure 7–60?
 - (a) R_1 and R_2 are in series with R_3 , R_4 , and R_5 .
 - (b) R_1 and R_2 are in series.
 - (c) R_1 is in parallel with R_3 .
 - (d) none of the above
2. The total resistance of Figure 7–60 can be found with which of the following formulas?
 - (a) $R_1 + R_2 + R_3 \parallel R_4 \parallel R_5$
 - (b) $R_1 \parallel R_2 + R_3 \parallel R_4 + R_5$
 - (c) $(R_1 + R_2) \parallel (R_3 + R_4 + R_5)$
 - (d) none of these answers
3. If all of the resistors in Figure 7–60 have the same value, when voltage is applied across terminals A and B, the current is

<ol style="list-style-type: none"> (a) greatest in R_5 (c) greatest in R_1 and R_2 	<ol style="list-style-type: none"> (b) greatest in R_3, R_4, and R_5 (d) the same in all the resistors
---	---

**▲ FIGURE 7–60**

4. Two $1.0\text{ k}\Omega$ resistors are in series and this series combination is in parallel with a $2.2\text{ k}\Omega$ resistor. The voltage across one of the $1.0\text{ k}\Omega$ resistors is 6 V. The voltage across the $2.2\text{ k}\Omega$ resistor is
 (a) 6 V (b) 3 V (c) 12 V (d) 13.2 V
5. The parallel combination of a $330\text{ }\Omega$ resistor and a $470\text{ }\Omega$ resistor is in series with the parallel combination of four $1.0\text{ k}\Omega$ resistors. A 100 V source is connected across the circuit. The resistor with the most current has a value of
 (a) $1.0\text{ k}\Omega$ (b) $330\text{ }\Omega$ (c) $470\text{ }\Omega$
6. In the circuit described in Question 5, the resistor(s) with the most voltage has (have) a value of
 (a) $1.0\text{ k}\Omega$ (b) $470\text{ }\Omega$ (c) $330\text{ }\Omega$
7. In the circuit of Question 5, the percentage of the total current through any single $1.0\text{ k}\Omega$ resistor is
 (a) 100% (b) 25% (c) 50% (d) 31.3%
8. The output of a certain voltage divider is 9 V with no load. When a load is connected, the output voltage
 (a) increases (b) decreases (c) remains the same (d) becomes zero
9. A certain voltage divider consists of two $10\text{ k}\Omega$ resistors in series. Which of the following load resistors will have the most effect on the output voltage?
 (a) $1.0\text{ M}\Omega$ (b) $20\text{ k}\Omega$ (c) $100\text{ k}\Omega$ (d) $10\text{ k}\Omega$
10. When a load resistance is connected to the output of a voltage-divider circuit, the current drawn from the source
 (a) decreases (b) increases (c) remains the same (d) is cut off
11. In a ladder network, simplification should begin at
 (a) the source (b) the resistor farthest from the source
 (c) the center (d) the resistor closest to the source
12. In a certain four-step $R/2R$ ladder network, the smallest resistor value is $10\text{ k}\Omega$. The largest value is
 (a) indeterminable (b) $20\text{ k}\Omega$ (c) $80\text{ k}\Omega$ (d) $160\text{ k}\Omega$
13. The output voltage of a balanced Wheatstone bridge is
 (a) equal to the source voltage
 (b) equal to zero
 (c) dependent on all of the resistor values in the bridge
 (d) dependent on the value of the unknown resistor
14. A Wheatstone bridge has a variable resistor $R_V = 8\text{ k}\Omega$ when balanced. The right side of the bridge has values of $R_2 = 680\text{ }\Omega$ and $R_4 = 2.2\text{ k}\Omega$. The unknown resistance is
 (a) $2.47\text{ k}\Omega$ (b) $25.9\text{ k}\Omega$ (c) $187\text{ }\Omega$ (d) $2.89\text{ k}\Omega$
15. You are measuring the voltage at a given point in a circuit that has very high resistance values and the measured voltage is a little lower than it should be. This is possibly because of
 (a) one or more of the resistance values being off
 (b) the loading effect of the voltmeter
 (c) the source voltage is too low
 (d) all of these answers

CIRCUIT DYNAMICS QUIZ

Answers are at the end of the chapter.

Refer to Figure 7–61(b).

1. If R_2 opens, the total current
 (a) increases (b) decreases (c) stays the same
2. If R_3 opens, the current in R_2
 (a) increases (b) decreases (c) stays the same

3. If R_4 opens, the voltage across it
 - (a) increases
 - (b) decreases
 - (c) stays the same
4. If R_4 is shorted, the total current
 - (a) increases
 - (b) decreases
 - (c) stays the same

Refer to Figure 7-63.

5. If R_{10} opens, with 10 V applied between terminals A and B , the total current
 - (a) increases
 - (b) decreases
 - (c) stays the same
6. If R_1 opens with 10 V applied between terminals A and B , the voltage across R_1
 - (a) increases
 - (b) decreases
 - (c) stays the same
7. If there is a short between the left contact of R_3 and the bottom contact of R_5 , the total resistance between A and B
 - (a) increases
 - (b) decreases
 - (c) stays the same

Refer to Figure 7-67.

8. If R_4 opens, the voltage at point C
 - (a) increases
 - (b) decreases
 - (c) stays the same
9. If there is a short from point D to ground, the voltage from A to B
 - (a) increases
 - (b) decreases
 - (c) stays the same
10. If R_5 opens, the current through R_1
 - (a) increases
 - (b) decreases
 - (c) stays the same

Refer to Figure 7-73.

11. If a $10\text{ k}\Omega$ load resistor is connected across the output terminals A and B , the output voltage
 - (a) increases
 - (b) decreases
 - (c) stays the same
12. If the $10\text{ k}\Omega$ load resistor mentioned in Question 11 is replaced by a $100\text{ k}\Omega$ load resistor, V_{OUT}
 - (a) increases
 - (b) decreases
 - (c) stays the same

Refer to Figure 7-74.

13. If there is a short between the V_2 and V_3 terminals of the switch, the voltage V_1 with respect to ground
 - (a) increases
 - (b) decreases
 - (c) stays the same
14. If the switch is in the position shown and if the V_3 terminal of the switch is shorted to ground, the voltage across R_L
 - (a) increases
 - (b) decreases
 - (c) stays the same
15. If R_4 opens with the switch in the position shown, the voltage across R_L
 - (a) increases
 - (b) decreases
 - (c) stays the same

Refer to Figure 7-79.

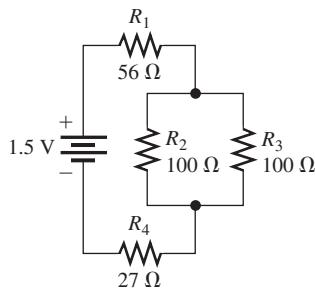
16. If R_4 opens, V_{OUT}
 - (a) increases
 - (b) decreases
 - (c) stays the same
17. If R_7 is shorted to ground, V_{OUT}
 - (a) increases
 - (b) decreases
 - (c) stays the same

PROBLEMS

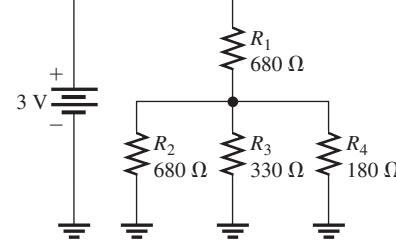
More difficult problems are indicated by an asterisk (*).
Answers to odd-numbered problems are at the end of the book.

SECTION 7–1 Identifying Series-Parallel Relationships

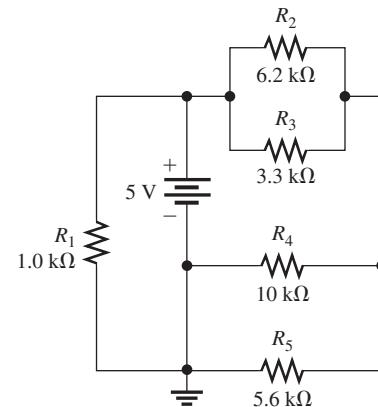
1. Visualize and draw the following series-parallel combinations:
 - (a) R_1 in series with the parallel combination of R_2 and R_3
 - (b) R_1 in parallel with the series combination of R_2 and R_3
 - (c) R_1 in parallel with a branch containing R_2 in series with a parallel combination of four other resistors
2. Visualize and draw the following series-parallel circuits:
 - (a) A parallel combination of three branches, each containing two series resistors
 - (b) A series combination of three parallel circuits, each containing two resistors
3. In each circuit of Figure 7–61, identify the series and parallel relationships of the resistors viewed from the source.



(a)



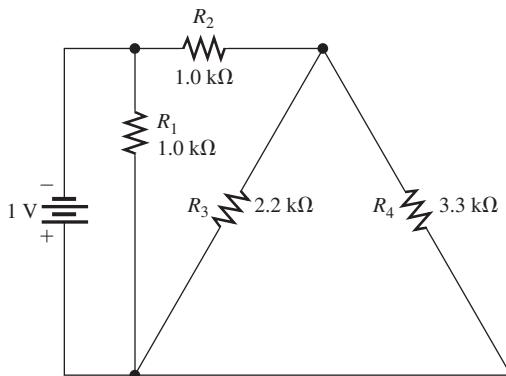
(b)



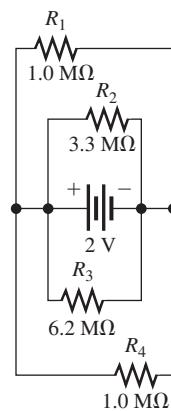
(c)

FIGURE 7–61

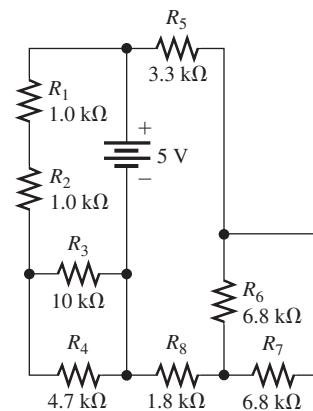
4. For each circuit in Figure 7–62, identify the series and parallel relationships of the resistors viewed from the source.



(a)



(b)

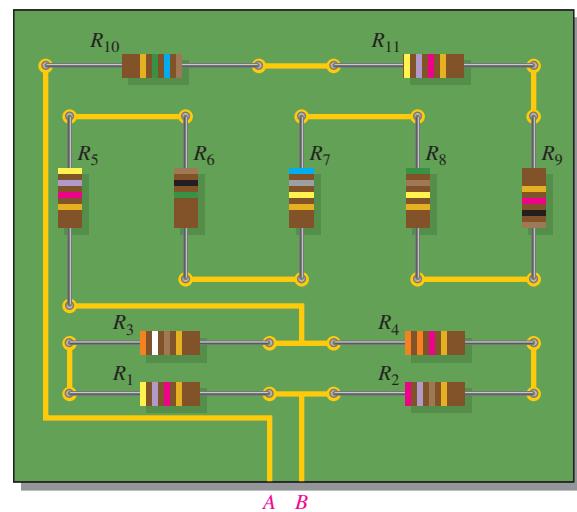


(c)

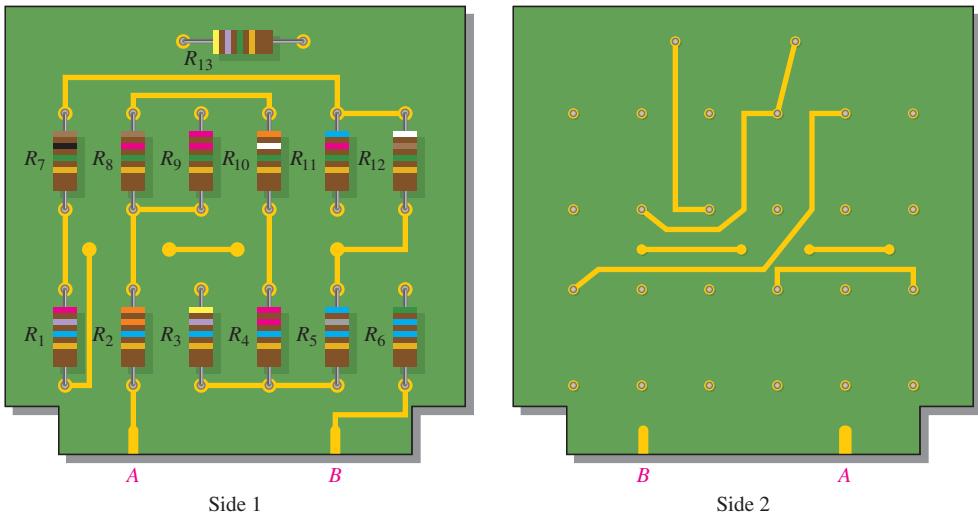
FIGURE 7–62

5. Draw the schematic of the PC board layout in Figure 7–63 showing resistor values and identify the series-parallel relationships.

► FIGURE 7–63



- *6. Develop a schematic for the double-sided PC board in Figure 7–64 and label the resistor values.
 *7. Lay out a PC board for the circuit in Figure 7–62(c). The battery is to be connected external to the board.

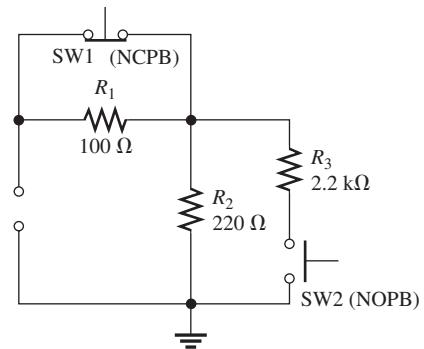


▲ FIGURE 7–64

SECTION 7–2 Analysis of Series-Parallel Resistive Circuits

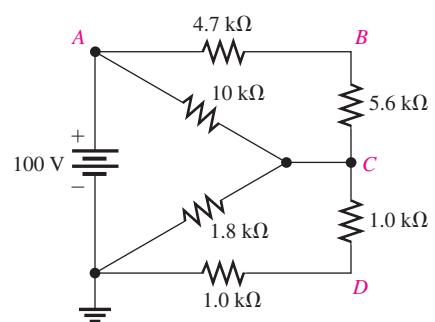
8. A certain circuit is composed of two parallel resistors. The total resistance is 667Ω . One of the resistors is $1.0 \text{ k}\Omega$. What is the other resistor?
 9. For each circuit in Figure 7–61, determine the total resistance presented to the source.
 10. Repeat Problem 9 for each circuit in Figure 7–62.
 11. Determine the current through each resistor in each circuit in Figure 7–61; then calculate each voltage drop.
 12. Determine the current through each resistor in each circuit in Figure 7–62; then calculate each voltage drop.
 13. Find R_T for all combinations of the switches in Figure 7–65.

► FIGURE 7-65



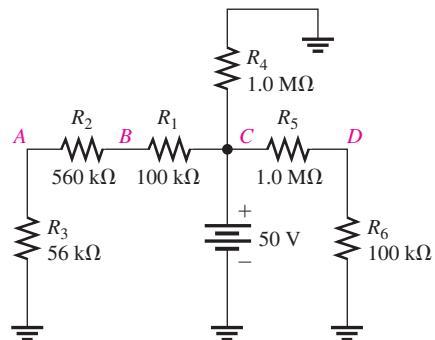
14. Determine the resistance between *A* and *B* in Figure 7-66 with the source removed.
 15. Determine the voltage at each node with respect to ground in Figure 7-66.

► FIGURE 7-66



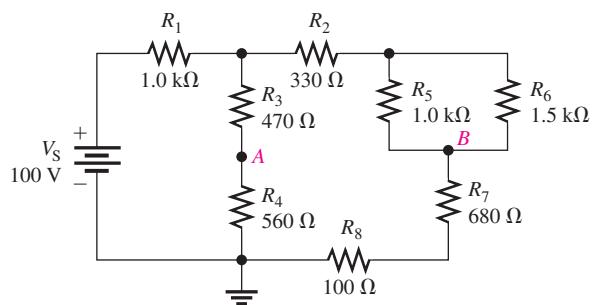
16. Determine the voltage at each node with respect to ground in Figure 7-67.
 17. In Figure 7-67, how would you determine the voltage across R_2 by measuring without connecting a meter directly across the resistor?
 18. Determine the resistance of the circuit in Figure 7-66 as seen from the voltage source.
 19. Determine the resistance of the circuit in Figure 7-67 as seen from the voltage source.

► FIGURE 7-67



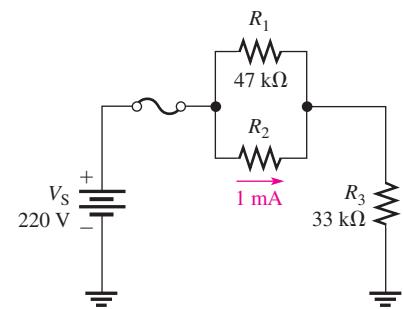
20. Determine the voltage, V_{AB} , in Figure 7-68.

► FIGURE 7-68

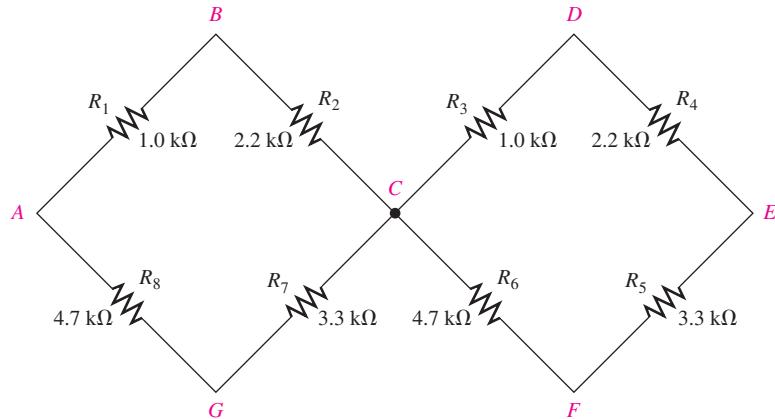


*21. (a) Find the value of R_2 in Figure 7–69. (b) Determine the power in R_2 .

► FIGURE 7–69



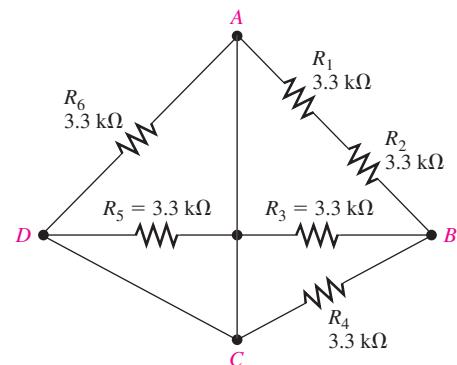
*22. Find the resistance between node A and each of the other nodes (R_{AB} , R_{AC} , R_{AD} , R_{AE} , R_{AF} , and R_{AG}) in Figure 7–70.



▲ FIGURE 7–70

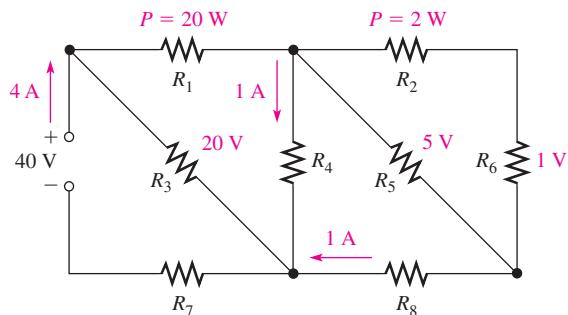
*23. Find the resistance between each of the following sets of nodes in Figure 7–71: AB , BC , and CD .

► FIGURE 7–71



*24. Determine the value of each resistor in Figure 7–72.

► FIGURE 7–72

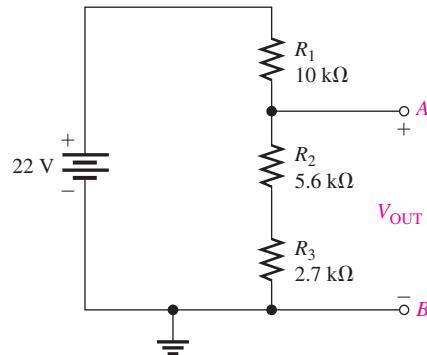


SECTION 7–3

Voltage Dividers with Resistive Loads

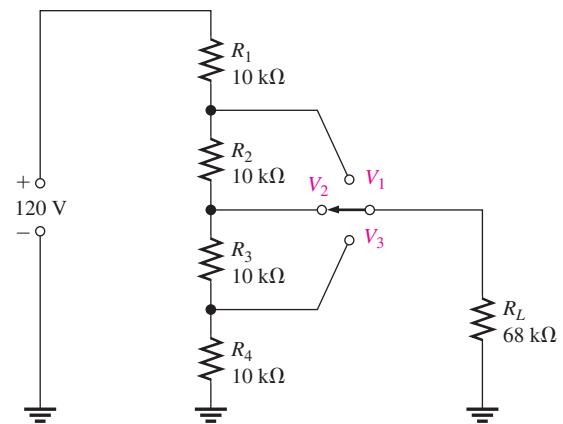
25. A voltage divider consists of two $56 \text{ k}\Omega$ resistors and a 15 V source. Calculate the unloaded output voltage. What will the output voltage be if a load resistor of $1.0 \text{ M}\Omega$ is connected to the output?
26. A 12 V battery output is divided down to obtain two output voltages. Three $3.3 \text{ k}\Omega$ resistors are used to provide the two taps. Determine the output voltages. If a $10 \text{ k}\Omega$ load is connected to the higher of the two outputs, what will its loaded value be?
27. Which will cause a smaller decrease in output voltage for a given voltage divider, a $10 \text{ k}\Omega$ load or a $47 \text{ k}\Omega$ load?
28. In Figure 7–73, determine the output voltage with no load across the output terminals. With a $100 \text{ k}\Omega$ load connected from A to B , what is the output voltage?
29. In Figure 7–73, determine the output voltage with a $33 \text{ k}\Omega$ load connected between A and B .
30. In Figure 7–73, determine the continuous current drawn from the source with no load across the output terminals. With a $33 \text{ k}\Omega$ load, what is the current drain?

► FIGURE 7–73



- *31. Determine the resistance values for a voltage divider that must meet the following specifications: The current drawn from the source under unloaded condition is not to exceed 5 mA . The source voltage is to be 10 V , and the required outputs are to be 5 V and 2.5 V . Sketch the circuit. Determine the effect on the output voltages if a $1.0 \text{ k}\Omega$ load is connected to each tap one at a time.
32. The voltage divider in Figure 7–74 has a switched load. Determine the voltage at each tap (V_1 , V_2 , and V_3) for each position of the switch.

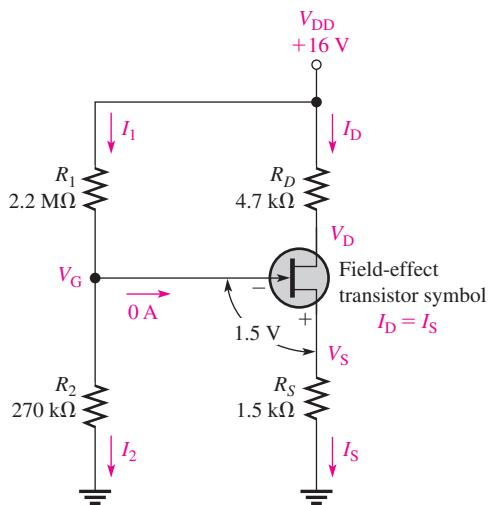
► FIGURE 7–74



- *33. Figure 7–75 shows a dc biasing arrangement for a field-effect transistor amplifier. Biasing is a common method for setting up certain dc voltage levels required for proper amplifier operation. Although you are not expected to be familiar with transistor amplifiers at this point, the dc voltages and currents in the circuit can be determined using methods that you already know.

(a) Find V_G and V_S (b) Determine I_1 , I_2 , I_D , and I_S (c) Find V_{DS} and V_{DG}

► FIGURE 7–75



- *34. Design a voltage divider to provide a 6 V output with no load and a minimum of 5.5 V across a 1.0 k Ω load. The source voltage is 24 V, and the unloaded current drain is not to exceed 100 mA.

SECTION 7–4

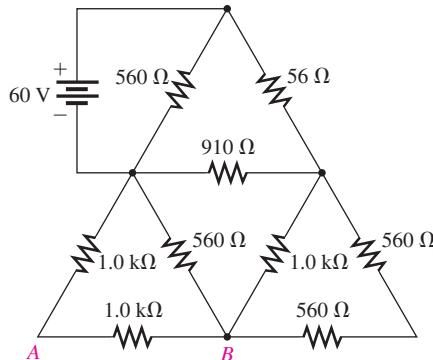
Loading Effect of a Voltmeter

35. On which one of the following voltage range settings will a voltmeter present the minimum load on a circuit?
- (a) 1 V (b) 10 V (c) 100 V (d) 1,000 V
36. Determine the internal resistance of a 20,000 Ω/V voltmeter on each of the following range settings.
- (a) 0.5 V (b) 1 V (c) 5 V (d) 50 V (e) 100 V (f) 1,000 V
37. The voltmeter described in Problem 36 is used to measure the voltage across R_4 in Figure 7–61(a).
- (a) What range should be used?
 (b) How much less is the voltage measured by the meter than the actual voltage?
38. Repeat Problem 37 if the voltmeter is used to measure the voltage across R_4 in the circuit of Figure 7–61(b).
39. A 10,000 Ω/V analog multimeter is used on the 10 V scale to measure the output of a voltage divider. If the divider consists of two series 100 k Ω resistors, what fraction of the source voltage will be measured across one of the resistors?
40. If a DMM with 10 M Ω input resistance is used instead of the analog multimeter in Problem 39, what percentage of the source voltage will be measured by the DMM?

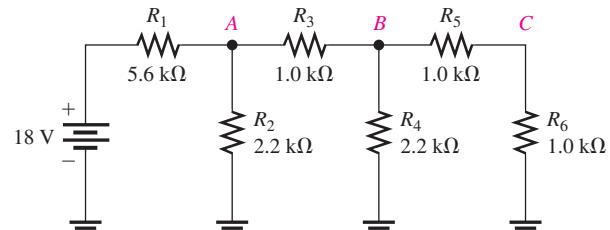
SECTION 7–5

Ladder Networks

41. For the circuit shown in Figure 7–76, calculate the following:
- (a) Total resistance across the source (b) Total current from the source
 (c) Current through the 910 Ω resistor (d) Voltage from A to point B
42. Determine the total resistance and the voltage at nodes A , B , and C in the ladder network of Figure 7–77.



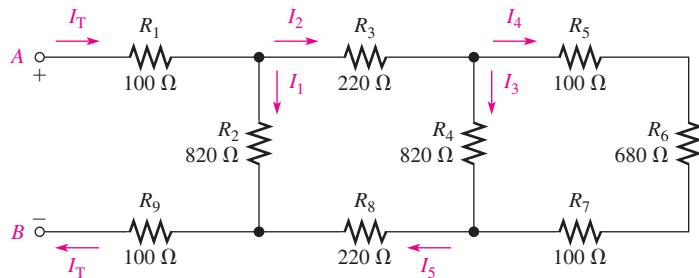
▲ FIGURE 7-76



▲ FIGURE 7-77

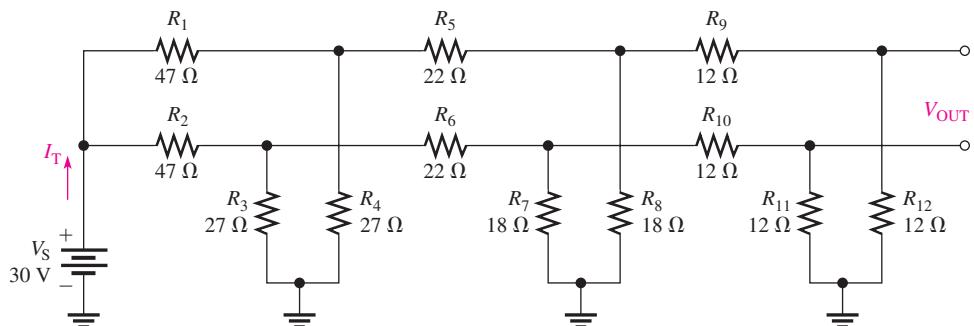
*43. Determine the total resistance between terminals *A* and *B* of the ladder network in Figure 7-78. Also calculate the current in each branch with 10 V between *A* and *B*.

44. What is the voltage across each resistor in Figure 7-78 with 10 V between *A* and *B*?



▲ FIGURE 7-78

*45. Find I_T and V_{OUT} in Figure 7-79.

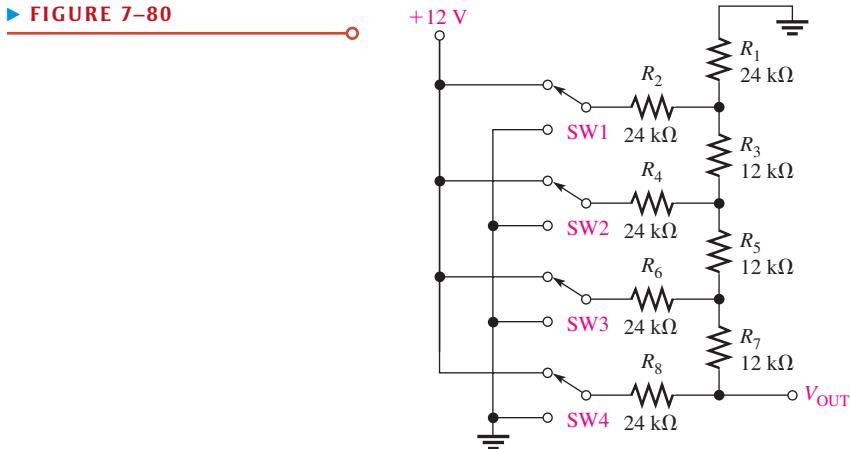


▲ FIGURE 7-79

46. Determine V_{OUT} for the $R/2R$ ladder network in Figure 7–80 for the following conditions:

- (a) Switch SW2 connected to +12 V and the others connected to ground
- (b) Switch SW1 connected to +12 V and the others connected to ground

► FIGURE 7–80



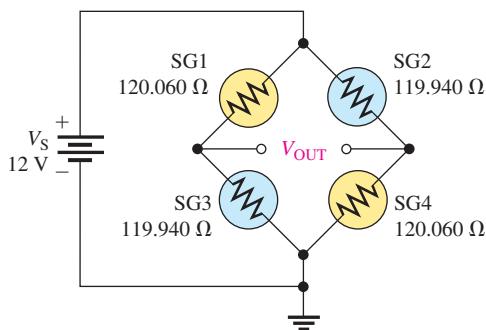
47. Repeat Problem 46 for the following conditions:

- (a) SW3 and SW4 to +12 V, SW1 and SW2 to ground
- (b) SW3 and SW1 to +12 V, SW2 and SW4 to ground
- (c) All switches to +12 V

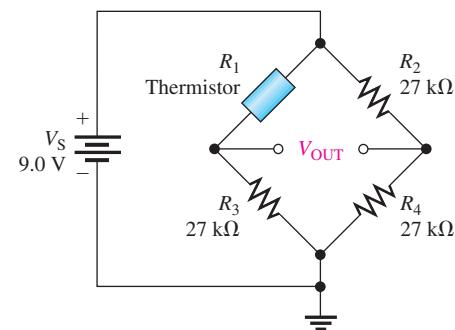
SECTION 7–6

The Wheatstone Bridge

48. A resistor of unknown value is connected to a Wheatstone bridge circuit like the one in Figure 7–47. The bridge parameters for a balanced condition are set as follows: $R_V = 18 \text{ k}\Omega$ and $R_2/R_4 = 0.02$. What is R_X ?
49. A load cell has four identical strain gauges with an unstrained resistance of 120.000Ω for each gauge (a standard value). When a load is added, the gauges in tension increase their resistance by $60 \text{ m}\Omega$ to 120.060Ω and the gauges in compression decrease their resistance by $60 \text{ m}\Omega$ to 119.940Ω as shown in Figure 7–81. What is the output voltage under load?
50. Determine the output voltage for the unbalanced bridge in Figure 7–82 for a temperature of 60°C . The temperature resistance characteristic for the thermistor is shown in Figure 7–59.



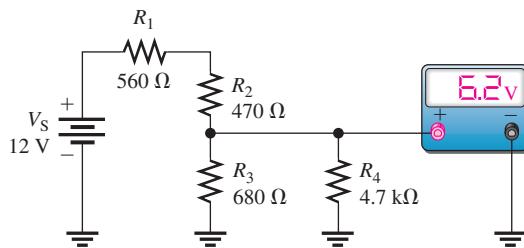
▲ FIGURE 7–81



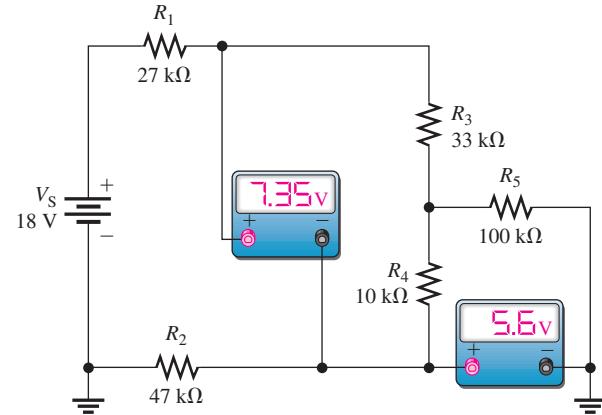
▲ FIGURE 7–82

SECTION 7–7 Troubleshooting

51. Is the voltmeter reading in Figure 7–83 correct?
 52. Are the meter readings in Figure 7–84 correct?

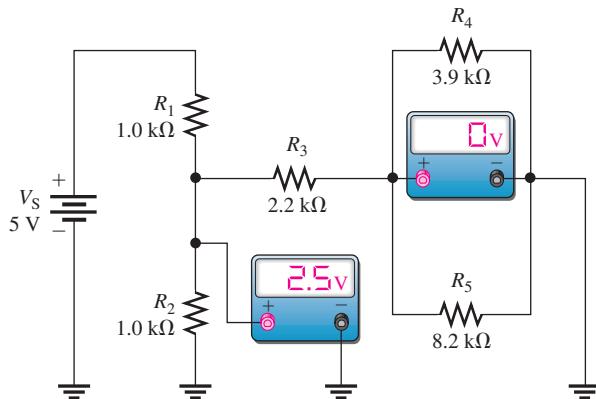


▲ FIGURE 7–83



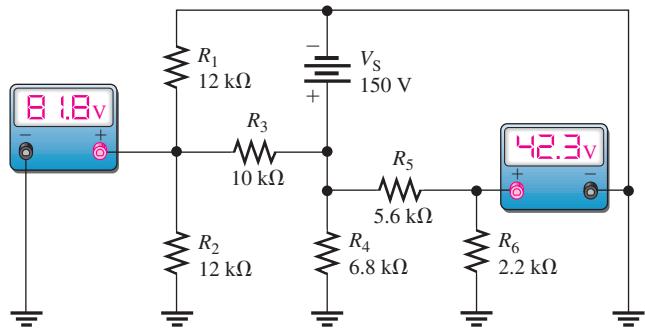
▲ FIGURE 7–84

53. There is one fault in Figure 7–85. Based on the meter indications, determine what the fault is.



▲ FIGURE 7–85

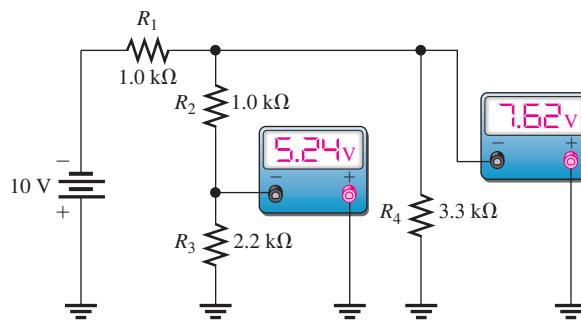
54. Look at the meters in Figure 7–86 and determine if there is a fault in the circuit. If there is a fault, identify it.



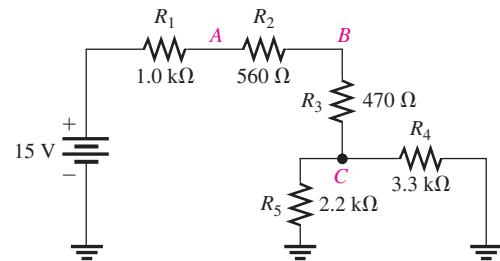
▲ FIGURE 7–86

55. Check the meter readings in Figure 7–87 and locate any fault that may exist.

56. If R_2 in Figure 7–88 opens, what voltages will be read at points A, B, and C?



▲ FIGURE 7-87



▲ FIGURE 7-88



Multisim Troubleshooting and Analysis

These problems require Multisim.

57. Open file P07-57 and measure the total resistance.
58. Open file P07-58. Determine by measurement if there is an open resistor and, if so, which one.
59. Open file P07-59 and determine the unspecified resistance value.
60. Open file P07-60 and determine how much the load resistance affects each of the resistor voltages.
61. Open file P07-61 and find the shorted resistor, if there is one.
62. Open file P07-62 and adjust the value of R_X until the bridge is approximately balanced.

ANSWERS

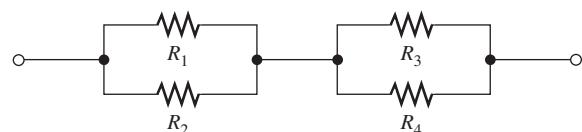
SECTION CHECKUPS

SECTION 7-1

Identifying Series-Parallel Relationships

1. A series-parallel resistive circuit is a circuit consisting of both series and parallel connections.
2. See Figure 7-89.
3. Resistors R_1 and R_2 are in series with the parallel combination of R_3 and R_4 .
4. R_3 , R_4 , and R_5 are in parallel. Also the series-parallel combination $R_2 + (R_3 \parallel R_4 \parallel R_5)$ is in parallel with R_1 .
5. Resistors R_1 and R_2 are in parallel; R_3 and R_4 are in parallel.
6. Yes, the parallel combinations are in series.

► FIGURE 7-89



SECTION 7-2

Analysis of Series-Parallel Resistive Circuits

1. Voltage-divider and current-divider formulas, Kirchhoff's laws, and Ohm's law can be used in series-parallel analysis.
2. $R_T = R_1 + R_2 \parallel R_3 + R_4 = 608 \Omega$
3. $I_3 = [R_2/(R_2 + R_3)]I_T = 11.1 \text{ mA}$
4. $V_2 = I_2 R_2 = 3.65 \text{ V}$
5. $R_T = 47 \Omega + 27 \Omega + (27 \Omega + 27 \Omega) \parallel 47 \Omega = 99.1 \Omega; I_T = 1 \text{ V}/99.1 \Omega = 10.1 \text{ mA}$

SECTION 7-3 Voltage Dividers with Resistive Loads

1. The load resistor decreases the output voltage.
2. True
3. $V_{\text{OUT(unloaded)}} = (6.8 \text{ k}\Omega / 39.8 \text{ k}\Omega) 12 \text{ V} = 2.05 \text{ V}$;

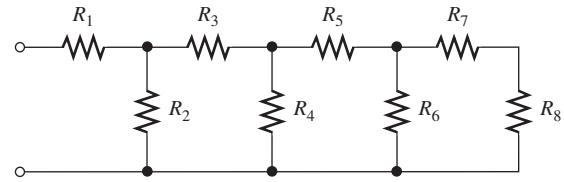
SECTION 7-4 Loading Effect of a Voltmeter

1. A voltmeter loads a circuit because the internal resistance of the meter appears in parallel with the circuit resistance across which it is connected, reducing the resistance between those two points of the circuit and drawing current from the circuit.
2. No, because the meter resistance is much larger than $1.0 \text{ k}\Omega$.
3. Yes.
4. $40 \text{ M}\Omega$

SECTION 7-5 Ladder Networks

1. See Figure 7-90.
2. $R_T = 11.6 \text{ k}\Omega$
3. $I_T = 10 \text{ V} / 11.6 \text{ k}\Omega = 859 \mu\text{A}$
4. $I_2 = 640 \mu\text{A}$
5. $V_A = 1.41 \text{ V}$

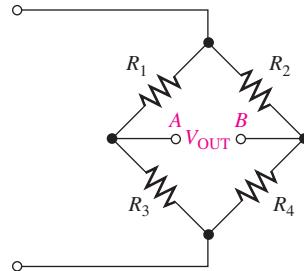
► FIGURE 7-90



SECTION 7-6 The Wheatstone Bridge

1. See Figure 7-91.
2. The bridge is balanced when $V_A = V_B$; that is, when $V_{\text{OUT}} = 0$
3. $R_X = 15 \text{ k}\Omega$
4. An unbalanced bridge is used to measure transducer-sensed quantities.
5. A load cell is a transducer that uses strain gauges to convert mechanical force into an electrical signal.

► FIGURE 7-91



SECTION 7-7 Troubleshooting

1. Common circuit faults are opens and shorts.
2. The $10 \text{ k}\Omega$ resistor (R_3) is open.
3. (a) $V_A = 55 \text{ V}$ (b) $V_A = 55 \text{ V}$ (c) $V_A = 54.2 \text{ V}$ (d) $V_A = 100 \text{ V}$ (e) $V_A = 0 \text{ V}$

RELATED PROBLEMS FOR EXAMPLES

- 7-1** The new resistor is in parallel with $R_4 + R_2 \parallel R_3$.
7-2 The resistor has no effect because it is shorted.
7-3 The new resistor is in parallel with R_5 .
7-4 *A* to gnd: $R_T = R_4 + R_3 \parallel (R_1 + R_2)$
B to gnd: $R_T = R_4 + R_2 \parallel (R_1 + R_3)$
C to gnd: $R_T = R_4$
7-5 R_3 and R_6 are in series.
7-6 $55.1\ \Omega$
7-7 $128.3\ \Omega$
7-8 $2.38\ \text{mA}$
7-9 $I_1 = 3.57\ \text{mA}; I_3 = 2.34\ \text{mA}$
7-10 $V_A = 44.8\ \text{V}; V_1 = 35.2\ \text{V}$
7-11 $2.04\ \text{V}$
7-12 $V_{AB} = 5.48\ \text{V}; V_{BC} = 1.66\ \text{V}; V_{CD} = 0.86\ \text{V}$
7-13 $3.39\ \text{V}$
7-14 $0.308\ \text{mA}$
7-15 The current will increase to $59\ \mu\text{A}$.
7-16 $5.19\ \text{V}$
7-17 $V_A = 6.0\ \text{V}, V_B = 3.0\ \text{V}, V_C = 1.5\ \text{V}$
7-18 $3.3\ \text{k}\Omega$
7-19 $0.45\ \text{V}$
7-20 $5.73\ \text{V}; 0\ \text{V}$
7-21 $9.46\ \text{V}$
7-22 $V_A = 12\ \text{V}; V_B = 13.8\ \text{V}$

TRUE/FALSE QUIZ

1. T 2. F 3. F 4. T 5. T 6. T 7. F 8. T 9. F 10. T 11. T 12. F

SELF-TEST

- | | | | | | | | |
|--------|---------|---------|---------|---------|---------|---------|--------|
| 1. (b) | 2. (c) | 3. (c) | 4. (c) | 5. (b) | 6. (a) | 7. (b) | 8. (b) |
| 9. (d) | 10. (b) | 11. (b) | 12. (b) | 13. (b) | 14. (a) | 15. (d) | |

CIRCUIT DYNAMICS QUIZ

- | | | | | | |
|---------|---------|---------|---------|---------|---------|
| 1. (b) | 2. (a) | 3. (a) | 4. (a) | 5. (b) | 6. (a) |
| 7. (b) | 8. (c) | 9. (c) | 10. (c) | 11. (b) | 12. (a) |
| 13. (b) | 14. (b) | 15. (a) | 16. (a) | 17. (a) | |