

Meeting Agenda

Location: HC group rooms

Date: 21/3 2011

Time: 15.15

Facilitator: Jennifer Panditha

Participants: Jennifer Panditha, Anton Lin, Boel Nelson and Robert Kaufmann

1. Objectives

Brainstorming for ideas that we possibly can develop in a limited period of time.

2. Reports

None so far.

3. Discussion items

Figure out a project to work with.

4. Outcomes and assignments

We have decided to develop a program that simulates logic gates and our project is to be called Boolean Circuits.

Next time, we should start discussing the RAD and start writing the RAD. Assign different member roles.

5. Wrap up

Next meeting is Friday the 23rd of March.