



## ARM ASSEMBLY LAB-3

By

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## STATUS UPDATE

The ARM assembly code was written for 32 bit processor and verified using ARMSim simulator successfully.

## EXERCISE – FIND MAX WEIGHT AND MAX WEIGHT STRING FROM RANGE OF DATA LOCATIONS IN MEMORY

The main part of objective/problem statement of the exercise is as follows:

Weight of a 32-bit number is defined as the total number of bits that are set in the 32-bit number. Given a series of 32-bit numbers (in hexadecimal form), write an assembly program to determine which element in the series has the largest weight and store the number in NUM and its weight in WEIGHT.

There are multiple ways to produce the mnemonics.

- Option 1 - Use Brian Kernighan Algorithm ( $n = n \& (n-1)$ )
- Option 2 - Keep shifting left and check the msb. If 1 then increase the count.
- Option 3 - Use Neon SIMD (vector) instructions.
- Option 4 - Using lookup tables.
- Option 5 - Using 64 bit instructions.
- Option 6 - Counting bits set in parallel.

And few more.

For this exercise, algorithm from Brian Kernighan is used. Most of the tricks are present in “Hacker’s Delight” by Henry S. Warren, Jr.

**The algorithm is hand-coded in 32 bit ARM Assembly and verified on ARMSim Simulator.**

## INITIAL STATE SCREENSHOT

The sample output screenshot for the logic is – (Registers listed on the left are to be noted. All are zero to begin with). Similarly, the memory view in bottom pane of the Simulator Window.

The screenshot displays the ARM Simulator interface. On the left, the 'RegistersView' pane shows the initial state of all 16 registers (R0-R15), CPSR, and other system registers, all set to zero. The 'CodeView' pane shows the assembly code for 'lab3.o', which includes global variables, data section, and a loop that calculates the sum of weights. The 'MemoryView' pane at the bottom shows the initial state of memory, with all values set to zero.

```
.global _END
.global _FINAL_UPDATE
.global _DATA_START
.global _DATA_END
.global _WEIGHT
.global _NUM

# Following is the data section.
# Data is placed from _DATA_START to _DATA_END

.data
    _DATA_START: .word 0x00000000
    00001088:01000000 .word 0x71567898
    00001090:645890FF .word 0x645890FF
    00001094:37F6F5FA .word 0x37F6F5FA
    00001098:5100000A .word 0x5100000A
    _DATA_END: .word 0x00000000
    _NUM: .word 0x0
    _WEIGHT: .word 0x0

.text
    # Program starts here
    _START:
    # Read the data start address
    00001000:E3A00000 MOV R0, #0 # Here we will store max weight globally.
    00001004:E3A01000 MOV R1, #0
    00001008:E59F3048 LDR R3, =_DATA_START
    0000100C:E59F4068 LDR R4, =_DATA_END
    00001010:E59F6068 LDR R6, =_WEIGHT
    # Let us get the total count of 32 bit data.
    00001014:E2433004 SUB R3, R3, #4
    00001018:R0449003 SUB R9, R4, R3
    0000101C:E1A09129 MOV R9, R9, LSR#2
    # R9 Contains the data count. We will loop through
    # R9 times so that we cover all the data items in memory.
```

## INTERMEDIATE STATE SCREENSHOT

Intermediate execution screenshot is as follows:

The screenshot displays the ARMv8-M simulator interface. The top menu bar includes File, View, Cache, Debug, Watch, and Help. The main window is divided into three panes: RegistersView, CodeView, and MemoryView.

**RegistersView:** Shows the current state of registers R0 through R15, CPSR, and system flags. The registers are listed in hexadecimal and decimal formats. The CPSR register shows the status of various flags: Negative (N), Zero (Z), Carry (C), Overflow (V), IRQ Disable, FIQ Disable, Thumb (T), and CPU Mode (System).

**CodeView:** Shows the assembly code being executed. The code is for a function named `lab3.o`. It includes comments explaining the logic, such as "We are using Algorithm from Brian Kernighan (n = n & (n-1)) while n <> 0". The code includes instructions like `ADD R3, #4`, `LDR R1, [R3]`, `MOV R7, #0`, `SUB R0, R1, #1`, `ANDS R1, R1, R8`, `ADD R7, R7, #1`, `BNE _LOOP`, `CMPEQ R7, R0`, `MOV R0, R7`, `LDR R5, [R3]`, `SUB R9, R9, #1`, `CMPEQ R9, #0`, `BNE _LOOP_OUTER`, `STR R0, [R4]`, and `STR R0, [R4]`.

**MemoryView:** Shows the memory contents at the current address. The memory is displayed in hexadecimal and decimal formats. The address range shown is from 00001010 to 00001230.

## FINAL STATE SCREENSHOT

At the end, the memory location updated with values & is as follows (and it is as per expectations). The final data is circled for the memory addresses.

The screenshot displays the ARM simulator interface with the following components:

- RegistersView:** Shows the state of 16 registers (R0-R15) and CPSR. R15 (pc) is highlighted in red and contains the value 00001078.
- CodeView:** Displays the assembly code for 'lab3.o'. It includes a data section with labels DATA\_START and DATA\_END, and a text section with a START label. Comments indicate the program's purpose: to read the data start address and store the maximum weight globally.
- MemoryView:** Shows the memory dump starting from address 00001000. The value 00001078 is circled in red, corresponding to the value in R15.

```
ARMsim# - The ARM Simulator Dept. of Computer Science
File View Cache Debug Watch Help

RegistersView
General Purpose Floating Point
Hexadecimal
Unsigned Decimal
Signed Decimal
R0 : 00000017
R1 : 37f6f5fa
R2 : 00000000
R3 : 00001098
R4 : 00001098
R5 : 0000109c
R6 : 000010a0
R7 : 00000005
R8 : 3fffffff
R9 : 00000000
R10 (s1): 00000000
R11 (fp): 00000000
R12 (ip): 00000000
R13 (sp): 00011400
R14 (lr): 00000000
R15 (pc): 00001078
-----
CPSR Register
Negative (N): 0
Zero (Z): 1
Carry (C): 1
Overflow (V): 0
IRQ Disable: 1
FIQ Disable: 1
Thumb (T): 0
CPU Mode: System
-----
0x600000df

lab3.o
# Following is the data section.
# Data is placed from DATA_START to DATA_END

.data
DATA_START: .word 0x01000000
            .word 0x7156789b
            .word 0x45890ff
            .word 0x37f6f5fa
DATA_END:   .word 0x5100000a

.text
# Program starts here
_START:
# Read the data start address
MOV R0, #0 # Here we will store max weight globally.
MOV R1, #0

00001008:E59F3068 LDR R3, -DATA_START
0000100C:E59F4068 LDR R4, -DATA_END

MemoryView0
Word Size: 80h 160h 320h
00001008 01000000 7156789b 645890ff 37f6f5fa 5100000a 37f6f5fa 00000017 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101
0000100C 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101 01010101
```