

1. What is the boolean expression for the output  $f$  of the combinational logic circuit of *NOR* gates given below?

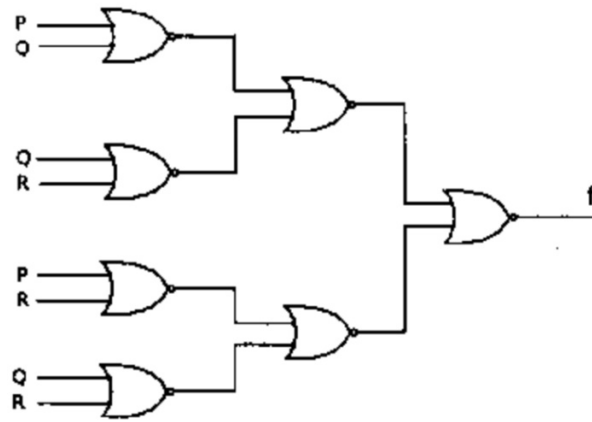


Figure 1:

- (a)  $Q + R$
- (b)  $P + Q$
- (c)  $P + R$
- (d)  $P + Q + R$