GATE

1. Wthat is the boolean expression for the output f of the combinational logic circuit of NOR gates given below?
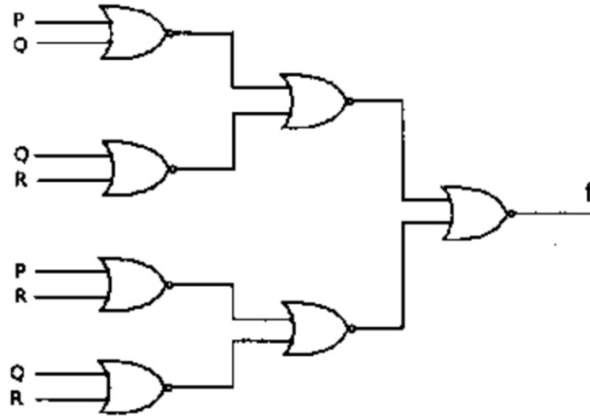

Figure 1:

(a) $\mathbf{Q} + \mathbf{R}$

(b) $\mathbf{P} + \mathbf{Q}$

(c) $\mathbf{P} + \mathbf{R}$

(d) $\mathbf{P} + \mathbf{Q} + \mathbf{R}$