

Digital ~~Asses~~ Digital Assignment

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Course: Operating system

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```

1.4 let
P0
flag[2];
int turn=0;
flag[i]=1;
while(flag[i])
{
    if(turn==j)
    {
        flag[i]=0;
        while(turn==j);
        flag[i]=1;
    }
    turn=j;
    flag[i]=0;

```

P1

```

flag[2];
int turn=0;
flag[j]=1;
while(flag[j])
{
    if(turn==i)
    {
        flag[j]=0;
        while(turn==i);
        flag[j]=1;
    }
    turn=i;
    flag[j]=0;

```

①

Solution for critical section:-

- * Mutual exclusion, * Progress,
- * Bounded waiting

i) Mutual Exclusion:

(2)

In the above process P_0 & P_1 If the process P_0 is executing in its critical section then no other process P_1 can be execution

C.S. Like wise, if P_1 is executing in its critical section, then P_0 can't be execute in C.S.

ii) Progress:-

Case i:-

If P_0 is showing interest it is executed n times when P_1 is not showing interest to execute

P_0	P_1
✓	x
✓	x
✓	x
⋮	⋮

Case ii:-

If P_1 is showing interest it is executed n times when P_0 is not showing interest to execute.

P_0	P_1
x	✓
x	✓
x	✓
⋮	⋮

iii) Bounded waiting

A bound must exist on the no. of times that P_0 is allowed to enter its critical section after P_1 has

made a request to enter its critical section and before that request is granted:

(3)

∴ All the 3 condition satisfies the solution for critical section.

2) Let.

P₀

while (true)

{ state[i] = interested;

turn = i;

while (state[i] == interested && turn == i);

<< critical section >>

state[i] = not interested;

<< code outside critical section >>

}

* In the above code it will enter into

while loop. Then state[i] will be assigned as interested and turn as i.

* After that using while loop it will check the condition.

$$\text{While}(\underbrace{\text{state}[j] == \text{interested}}_{\text{true}} \text{ || } \underbrace{\text{turn} == j}_{\text{false}}) \quad \textcircled{1}$$

→ If both are ~~true~~ the opp is true.
otherwise false.

* The condition fails and enter into critical section.

→ Then, ^{state[i]} is assigned as not interested

The above code satisfies all the conditions for critical section, as follows:

* Mutual Exclusion

The process P_0 is executing in its critical section, then no other process can be execute in its critical section.

Progress

If a process is showing interest to execute then the other process will not show interest to execute, and Vice Versa.

Bounded waiting:-

(5)

A bound must exist on the no. of times that other process is allowed to enter its critical section after a process has made a request to enter its critical section and before that request is granted.

③ Process P_i :

```
do
{
    flag[i] = TRUE;
    turn = i;
    while (flag[j] && turn == j);
    <critical section>
    flag[i] = FALSE;
    <remainder section>
} while (True);
```

Nothing is wrong in the following variation of Peterson's algorithm.

Since it satisfies all the 3 conditions for critical section, as follows.

① Mutual Exclusion:-

(b)

If the Process P_i is executing in its critical section then no other Process can be executing in this Critical section.

② Progress:-

If no Process is under execution in its critical section and there allow the process that wish to enter to its critical section, then the selection of the Processes that will enter into the critical section and next can't be Postponed indefinitely.

③ Bounded waiting:-

A bound must exist on the no. of times that other processes are allowed to ~~at~~ enter their critical section after a process has made a request to enter its critical section and before that request is granted.