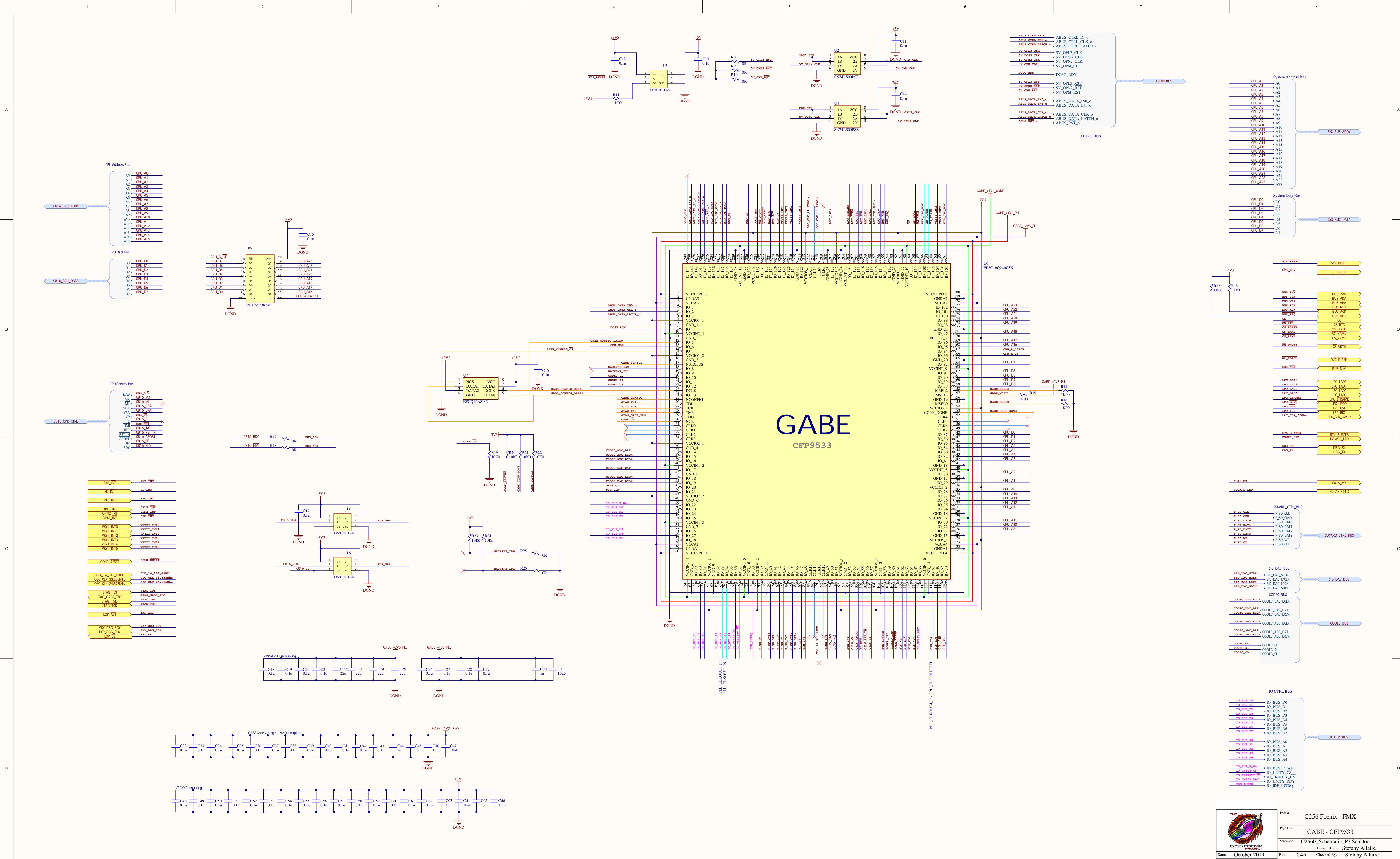
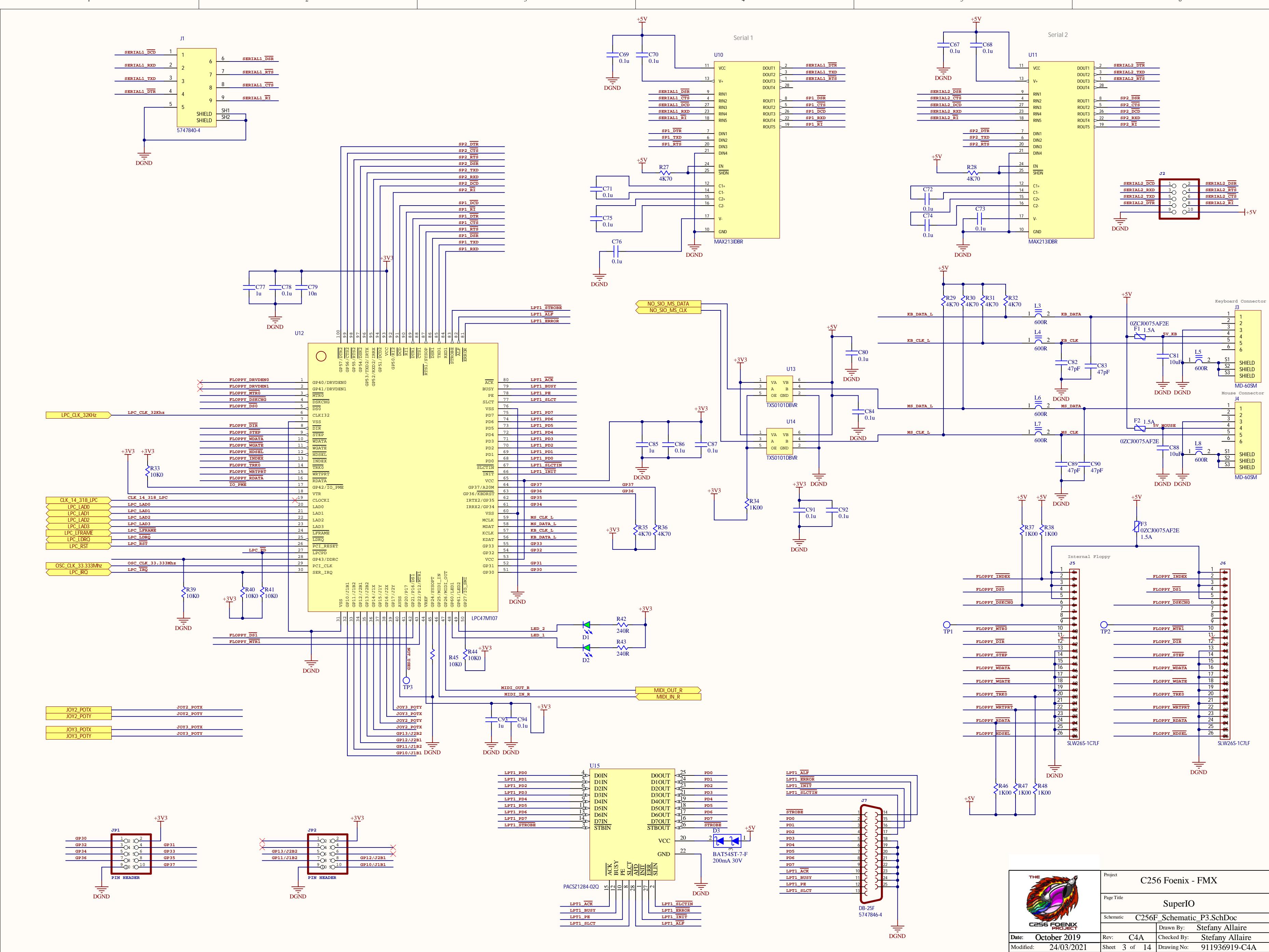
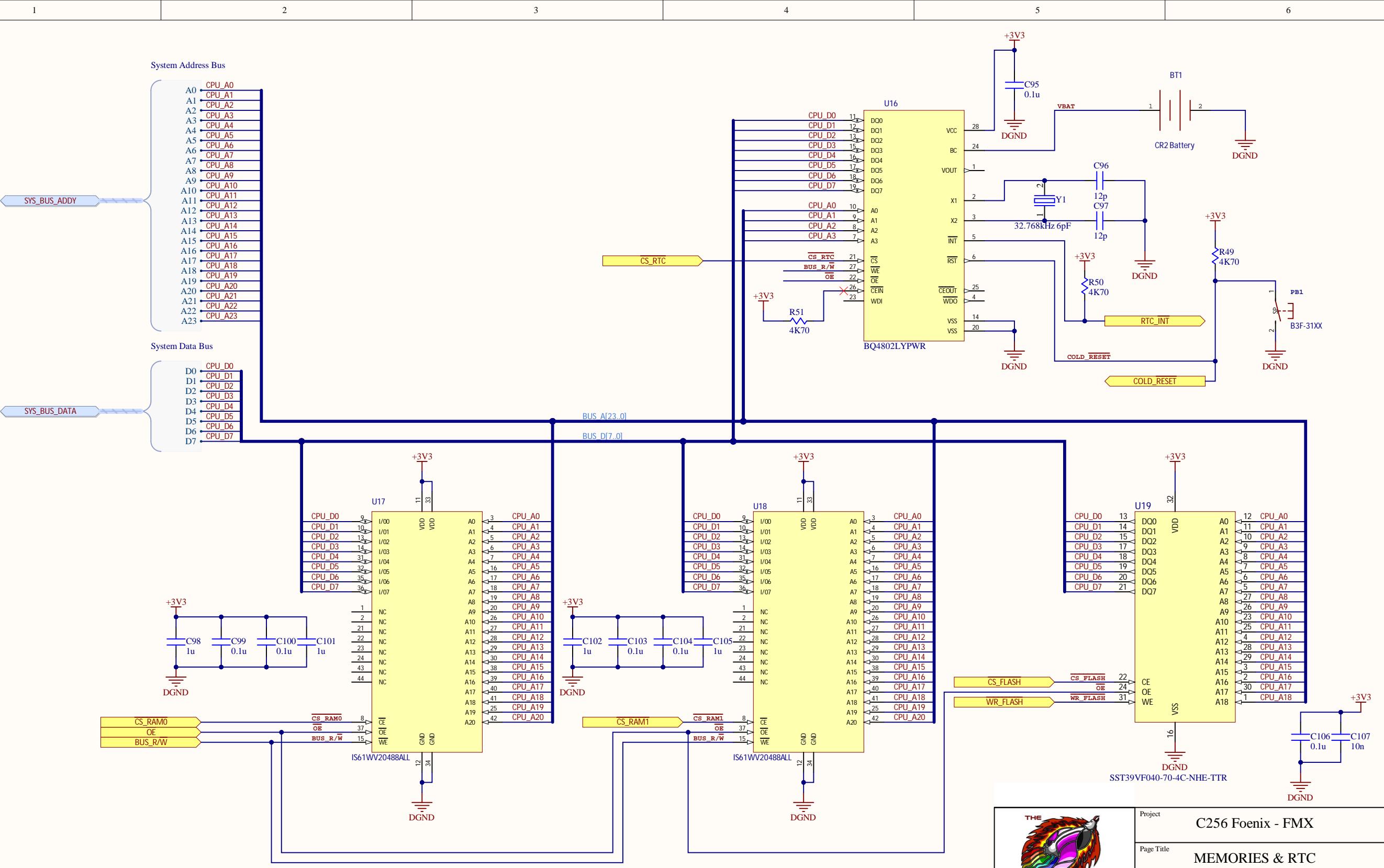


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Drawn By:	Stefany Allaire
Date:	October 2019
Rev:	C4A
Modified:	24/03/2021
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Project  
**C256 Foenix - FMX**

Page Title  
**MEMORIES & RTC**

Schematic **C256F\_Schematic\_P4.SchDoc**

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Date: **October 2019**

Rev: **C4A**

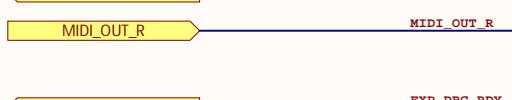
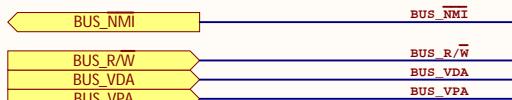
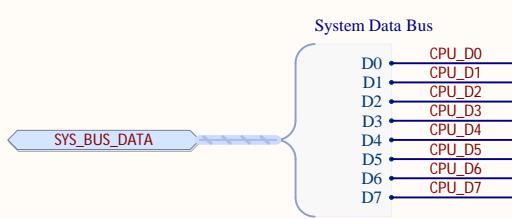
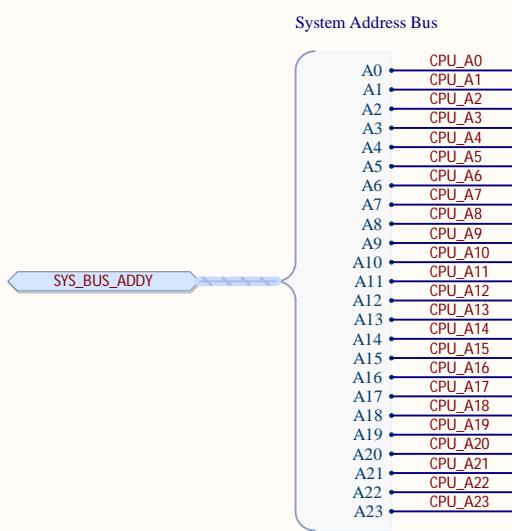
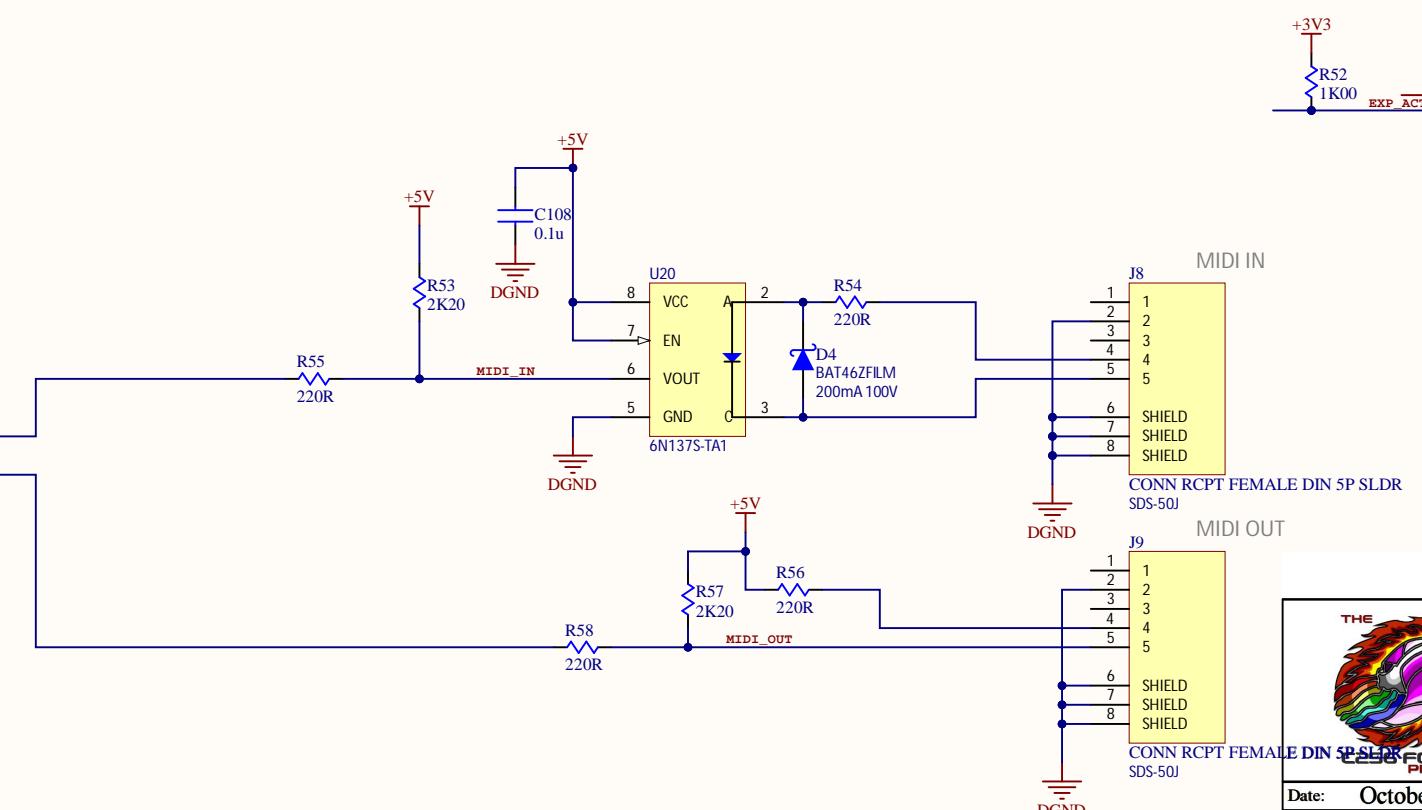
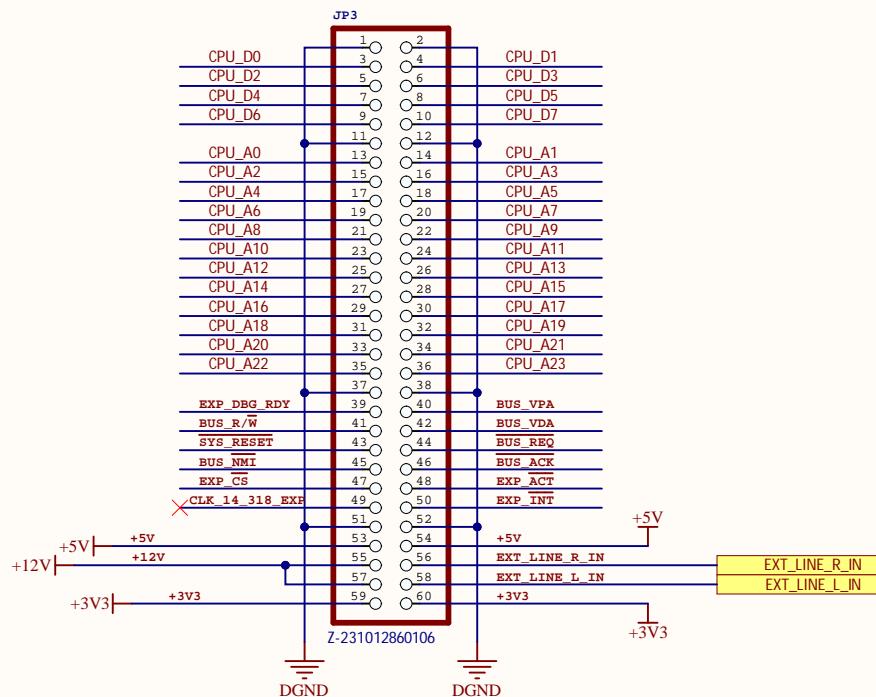
Checked By: **Stefany Allaire**

Modified: **24/03/2021**

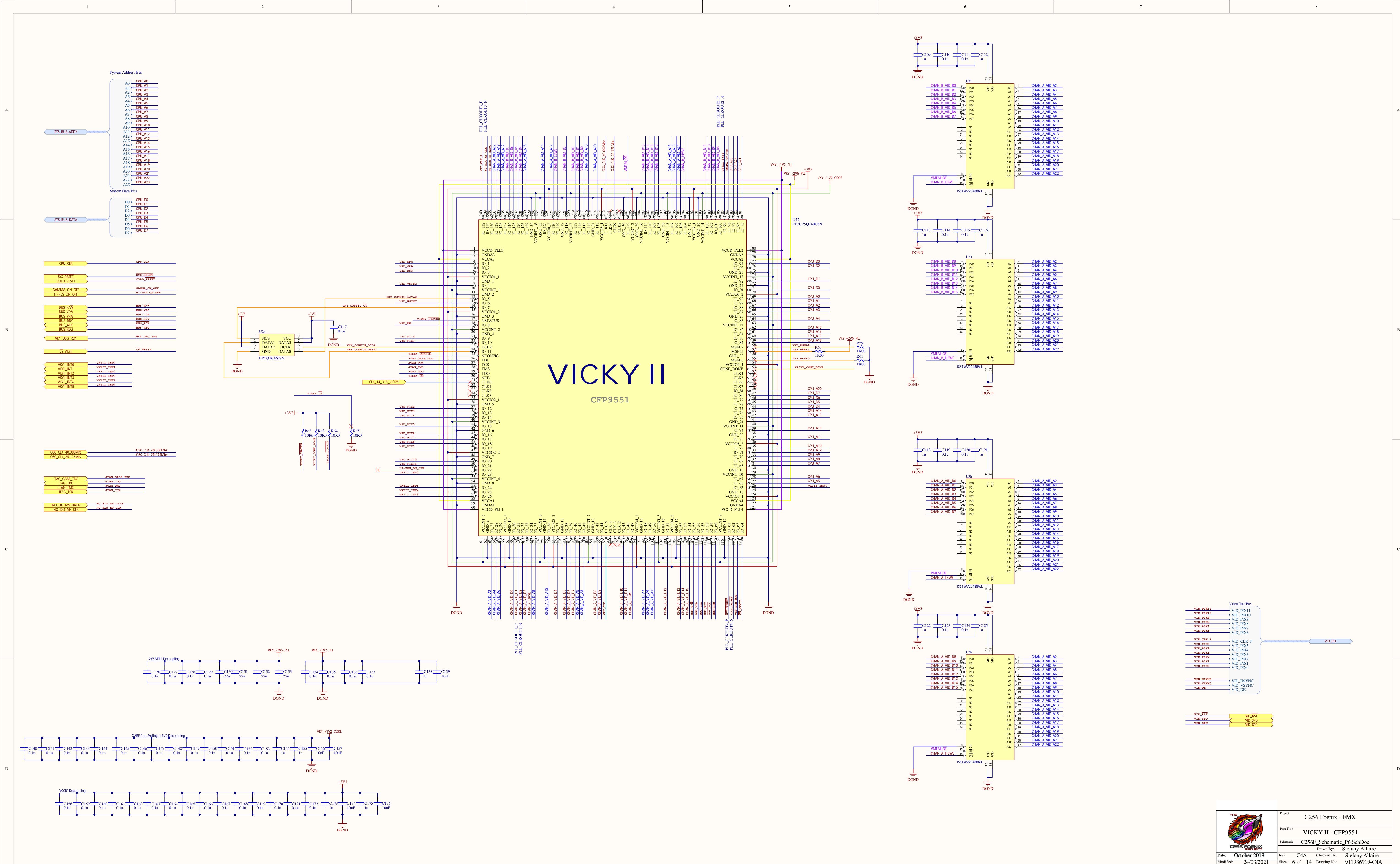
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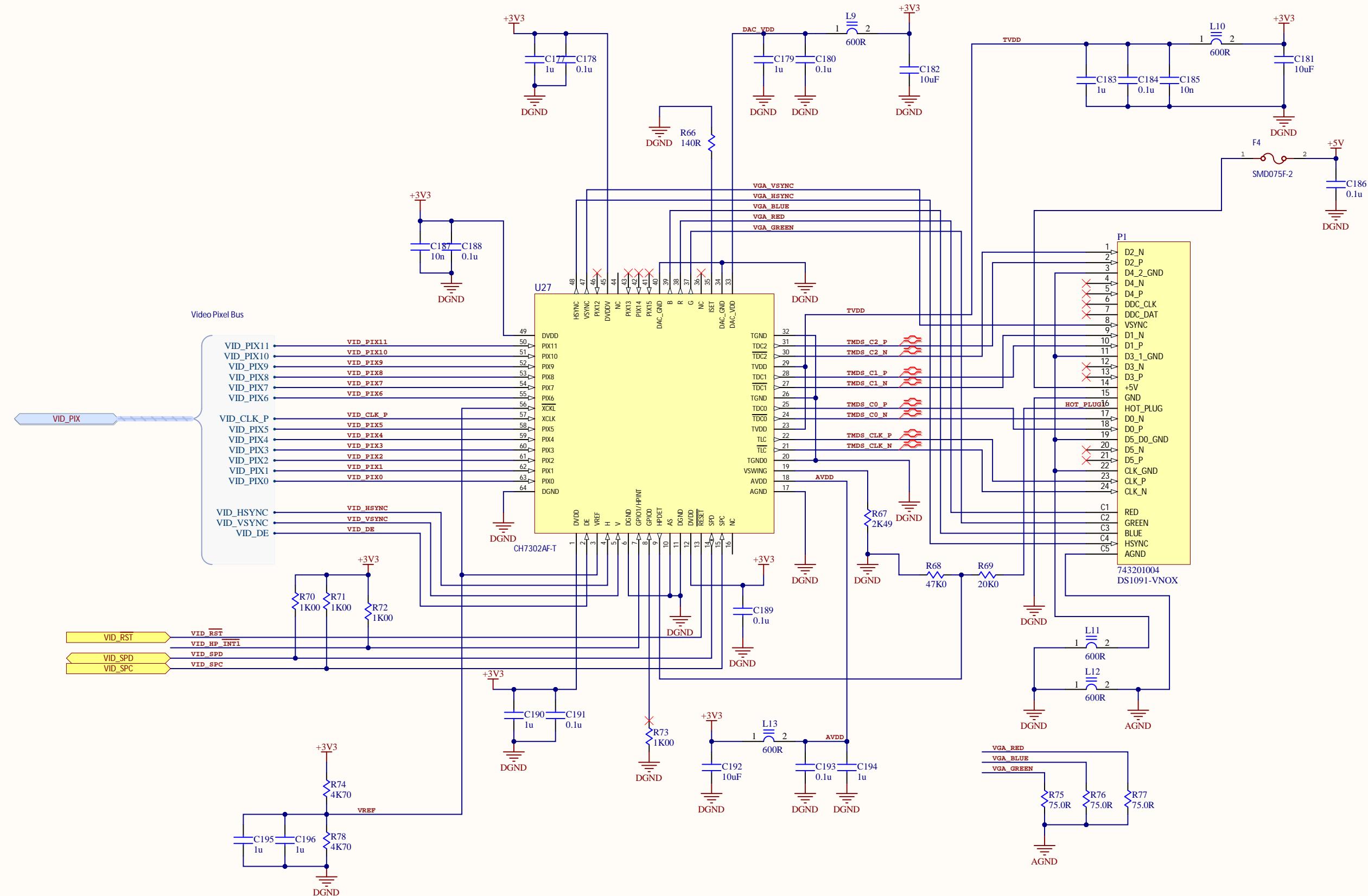
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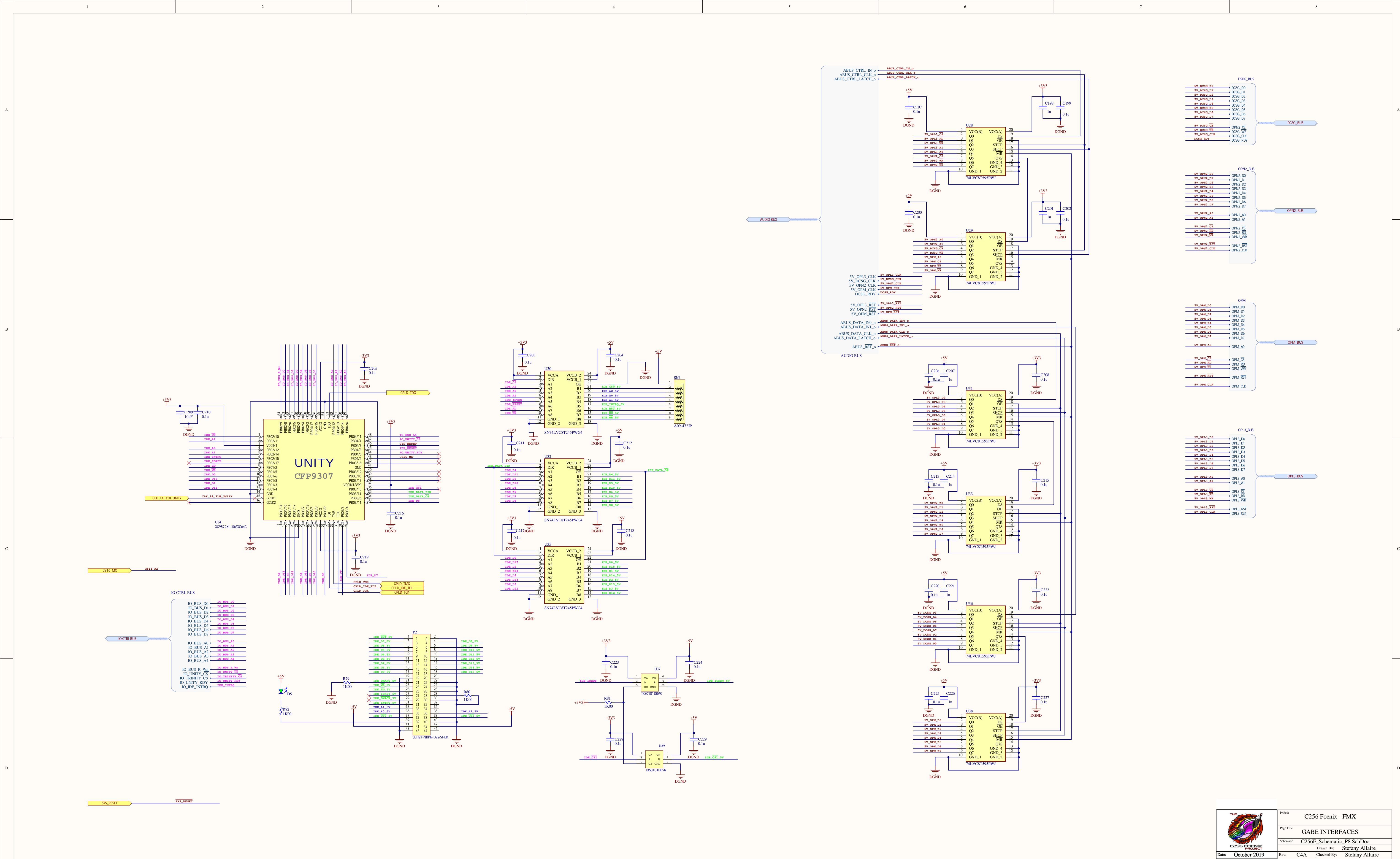
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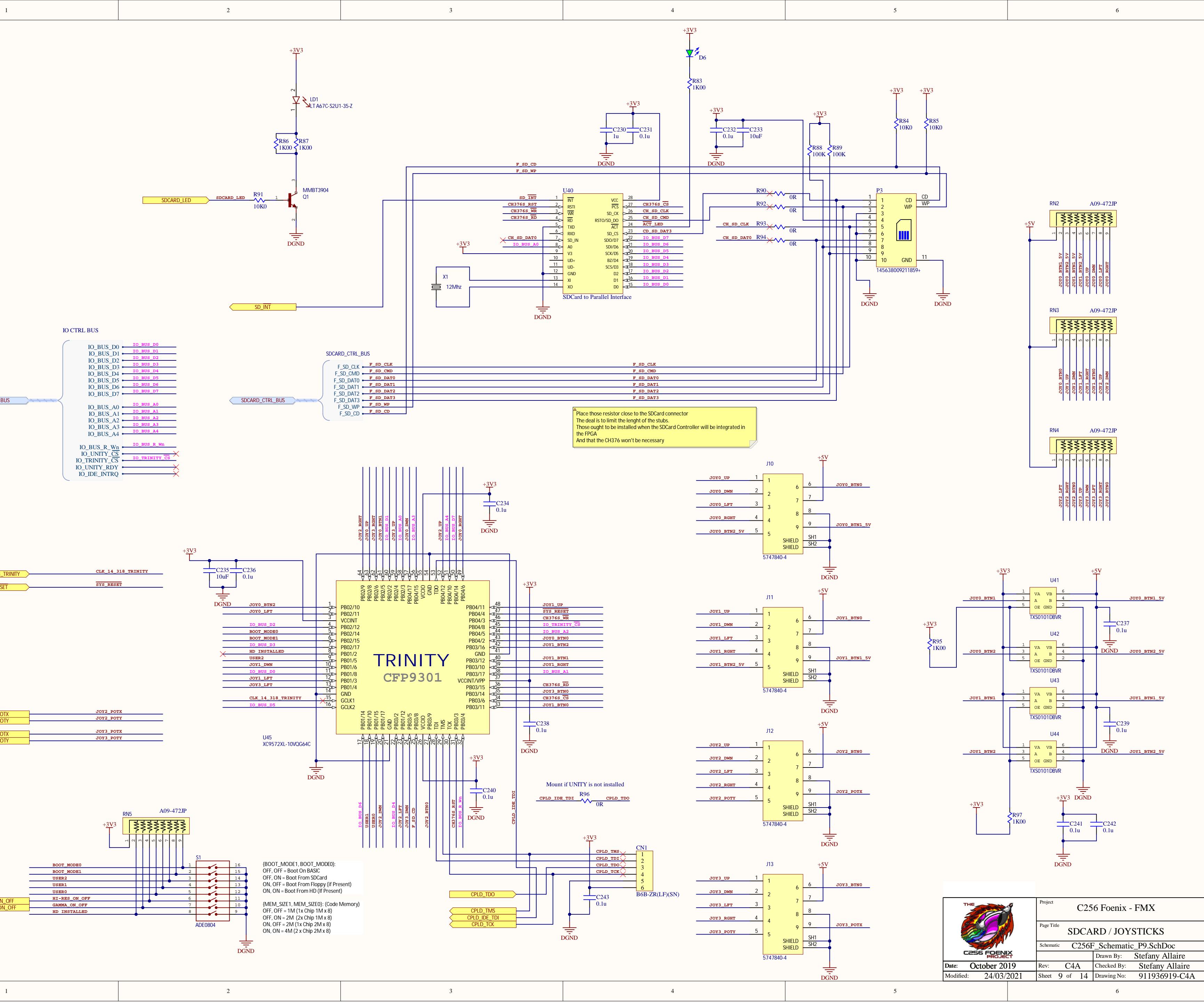
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Schematic	C256F_Schematic_P5.SchDoc	Drawn By:	Stefany Allaire
Date:	October 2019	Rev:	C4A
Modified:	24/03/2021	Checked By:	Stefany Allaire
Sheet	5 of 14	Drawing No:	911936919-C4A

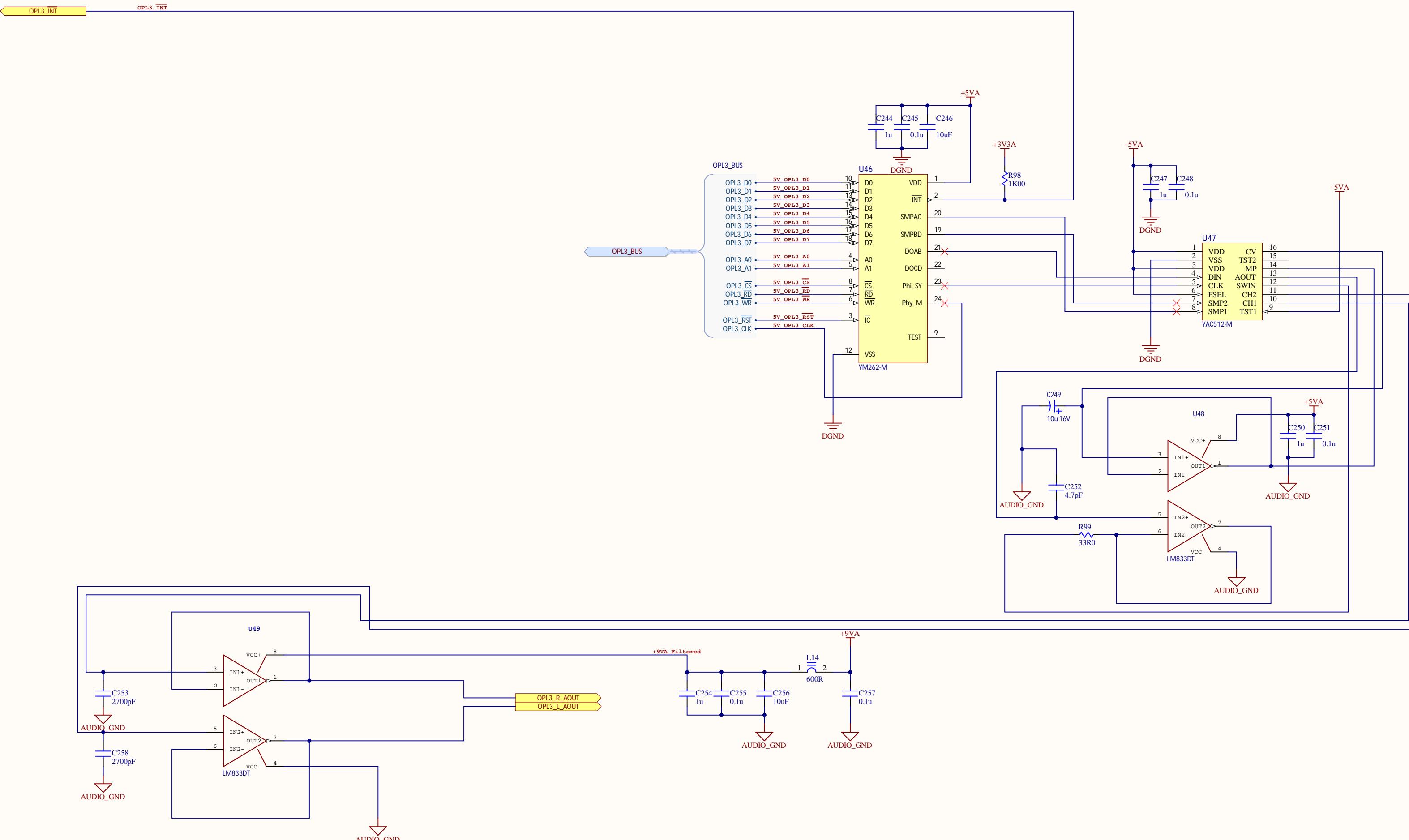




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Page Title	VIDEO DAC
Schematic	C256F_Schematic_P7.SchDoc
Drawn By:	Stefany Allaire
Date:	September 2019
Rev:	C4A
Checked By:	Stefany Allaire
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	Project <b>C256 Foenix - FMX</b>
Page Title	<b>OPL3</b>
Schematic	<b>C256F_Schematic_P10.SchDoc</b>
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Date:	October 2019
Rev:	C4A
Checked By:	Stefany Allaire
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Drawing No:	911936919-C4A

