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Career Objective:

I want to secure a position in a company that challenges my skills, updates my current knowledge, and sharpens my training. I want to broaden my horizons and gain a different perspective so that I can look at the same thing from a variety of angles.

Educational Qualifications:

- Currently pursuing Bachelor's Degree in Technology with core specialization in Electronics and Communication Engineering during the period 2018-2022 from SRMAP. Completed VI Semester, Secured 8.5 CGPA.
- Intermediate: Sri Chaitanya junior college,secured 91.5%.
- High school: DR.K.K.R's Gowtham School, secured 8.7 CGPA.

Technical Skills:

Core concepts: Signals and Systems, DSP, Analog Communication

Operating Systems: Windows, Linux

Programming Languages: C, Python, VHDL

Front-end Technologies: HTML, CSS, JavaScript

Artificial Intelligence: Machine Learning and Digital Image Processing

Tools: EDA, Cadence Virtuoso, Photoshop CC, Adobe XD, Blender, Premiere Pro.

Projects Handled:

Project 1: Density Based Traffic Control System

About the project: Now a days traffic getting worse and traffic is controlled according to the time for that we came up with an innovative idea that is the density based traffic control system with this we can control traffic by density not by time

Tools Used: Arduino Uno

Project 2: Image Enhancement Using VHDL

About the project: The project basically deals with image enhancement techniques. The enhancement operations are done using VHDL. The main theme is to enhance image using Threshold Operation, Invert Operation, Brightness Operation..

Tools Used: Xilinx Vivado

Project 3: Design of Analog to Digital Converter (ADC)

About the project: Designing of Analog-to-digital converter using comparators and priority encoder to convert analog signal to digital signal. Reduced the power consumption, complexity and increased efficiency.

Tools Used: Cadence Virtuoso

Project 4: Design and Implementation of SPI Protocol

About the project: Design and development of SPI by using the verilog code and creating the Master and Slave. The Finite StateMachines (FSM) and the state diagrams of both Master and Slave are used for verification purposes. From the RTL synthesis the schematic diagram and the synthesis results are covered

Tools Used: Xilinx Vivado

Certifications:

- Very Large-Scale Integration Certificate (ECIL)
- AWS Cloud Computing (APSSDC)
- Electric and Hybrid Vehicle Engineering Certificate (Haritha TechLogix)
- Machine Learning Certificate (Kyrion Technologies)
- Career Edge - Knockdown the Lockdown Certificate (TCS ION)

Extracurricular Activities:

- Participated in Dance at SRMAP Fresher's Day and Cultural fest
- Participated in Undergraduate Physics Research Skills Seminar by UCI
- Member of SRMAP Photography Club
- Volunteer of Food for Life
- Volunteer of SRMAP Tech fest
- Volunteered at NSS camp Amaravati
- Participated in SRMAP Swachh Bharat

Declaration: I hereby declare the information as correct and true to my knowledge.

Place: Guntur

Date : **Signature**