JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

VISION MISSION PROGRAMME EDUCATIONAL OBJECTIVES

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Vision:

To remain a symbol of pride in the fields of Electronics and Communication Engineering by producing holistic and diligent Engineers for industrial and societal needs.

Mission:

1. To produce high quality learners who are globally competitive and professionally challenged in the field of electronics and communication engineering.
2. To offer educational programmes that imparts inventive knowledge with high levels of ethical and human values.
3. To provide a platform to acquire and implement innovative ideas in research and development.
4. To build up the state of art laboratories and centres of excellence in different areas of electronics and communication engineering.
5. To train the students and faculty to update their knowledge in pioneering technologies to meet industrial requirements.

Programme Educational Objectives:

PEO 1	Do extremely well in professional career and higher education by attaining knowledge in mathematical, computing and engineering principles.
PEO 2	Analyze real life problems, design systems appropriate to its solutions in the field of electronics and communication engineering that ate technically sound, economically feasible and socially acceptable.
PEO 3	Possess good communication skills and ethical attitude with ability to work in teams and adapt to current trends by engaging in lifelong learning.

PROGRAMME OUTCOMES

PO1: Engineering knowledge: Apply the knowledge of Mathematics, Science, Engineering Fundamentals, and an Engineering Specialization to the solution of Complex Engineering Problems.

PO2: Problem analysis: Identify, Formulate, Review Research Literature, and analyze complex Engineering Problems reaching substantiated conclusions using first principles of Mathematics, Natural Sciences, and Engineering Sciences

PO3: Design/development of solutions: Design solutions for complex Engineering Problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, Societal, and Environmental considerations.

PO4: Conduct investigations of complex problems: Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.

PO5: Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.

PO6: The Engineer and Society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.

PO7: Environment and sustainability: Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.

PO8: Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.

PO9: Individual and team work: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.

PO10: Communication: Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

PO11: Project management and finance: Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.

PO12: Life-long learning: Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

PROGRAM SPECIFIC OUTCOMES

PSO1 : To improve the quality of human existence, analyse and create electronic electrical circuits and communication systems.

PSO2: To develop cutting-edge, environmentally mindful technologies to ensure human survival.

PSO3: To train students for the design and testing of Electronic systems devices.

PSO4: To analyze, design, simulate and implement computer hardware / software and use basic analog/digital circuits, VLSI design electronic systems for various computing and communication system. Intra and inter disciplinary applications

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

R20 COURSE STRUCTURE

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

I B.Tech I Semester

S.No	Course Name	Category	L	T	P	Credits
1	Mathematics - I	BSC	3	0	0	3
2	Applied Chemistry	BSC	3	0	0	3
3	Communicative English	HSMC	3	0	0	3
4	Programming For Problem Solving Using C	ESC	3	0	0	3
5	Network Analysis	ESC	3	0	0	3
6	English Communications Skills Lab	HSMC	0	0	3	1.5
7	Applied Chemistry Lab	BSC	0	0	3	1.5
8	Programming For Problem Solving using C LAB	ESC	0	0	3	1.5
9	Physical Fitness Activities	MC	0	0	2	0
		Total				19.5

I B.Tech II Semester

S.No	Course Name	Category	L	T	P	Credits
1	Mathematics – II	BSC	3	0	0	3
2	Applied Physics	BSC	3	0	0	3
3	Object Oriented Design & Programming using java	ESC	3	0	0	3
4	Engineering Drawing	ESC	3	0	0	3
5	Basic Electrical Engineering	ESC	3	0	0	3
6	Electronic workshop Lab	ESC	0	0	3	1.5
7	Applied Physics Laboratory	BSC	0	0	3	1.5
8	Basic Electrical Engineering lab	ESC	0	0	3	1.5
9	Applied Physics Virtual Laboratory	BSC	0	0	2	0
10	Constitution of India	MC	2	0	0	0
11	Engineering Exploration Project- Design Thinking	MC	0	0	1	0
		Total				19.5

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II B.Tech I Semester

S.No	Course Name	Category	L	T	P	Credits
1	Mathematics III	BS	3	0	0	3
2	Electronics Devices and Circuits	BS	3	0	0	3
3	Switching Theory and Logic Design	HS	3	0	0	3
4	Signals and Systems	ES	3	0	0	3
5	Random Variables and Stochastic Process	ES	3	0	0	3
6	Electronics Devices and Circuits - Lab	HS	0	0	3	1.5
7	Switching Theory and Logic Design - Lab	BS	0	0	3	1.5
8	Object Oriented Design & Programming using Java lab	ES	0	0	3	1.5
9	Skill oriented course*		1	0	2	2
10	Indian Traditional Knowledge		2	0	0	0
		Total				21.5

.

SKILL ORIENTED COURSES
Python Programming.

II B.Tech II Semester

S.No	Course Name	Category	L	T	P	Credits
1	Mathematics -4	BS	3	0	0	3
2	Linear I C Applications	ES	3	0	0	3
3	Electronics Circuit Analysis	PC	3	0	0	3
4	Analog Communications	PC	3	0	0	3
5	Managerial Economics & Financial Analysis	HS	3	0	0	3
6	Linear I C Applications Lab	ES	0	0	3	1.5
7	Analog Communications - Lab	PC	0	0	3	1.5
8	Electronics Circuit Analysis - Lab	PC	0	0	3	1.5
9	Skill oriented course*		1	0	2	2
		Total				21.5
Internship 2 Months (Mandatory) during summer vacation						
Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)			**4**	**0**	**0**	**4**

Honor Courses	Minor Courses	Skill Oriented Course
Artificial Neural Networks	Electronics Devices and Circuits	Scientific Computing
Nano Electronics	Signals and Systems	

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III B.Tech I Semester

S No	**Course Name**	Category	**L**	**T**	**P**	Credits
1	Digital I C Applications	PC	3	0	0	3
2	Micro Processors & Micro Controllers	PC	3	0	0	3
3	Electromagnetic Waves and Transmission Lines	PC	3	0	0	3
4	Professional Elective courses (PE1)	PE	3	0	0	3
5	Open Elective (OE1)	OE	2	0	2	3
6	Microprocessor and Microcontrollers - Lab	LC	0	0	3	1.5
7	Digital I C Applications Lab	LC	0	0	3	1.5
8	Skill advanced course/ soft skill course*		**1**	**0**	**2**	**2**
9	Environmental Science	MC	2	0	0	0
Summer Internship 2 Months (Mandatory) after second year (to be evaluated during V semester			0	0	0	1.5
		Total credits				21.5
	Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)	MC	4	0	0	4

PE1: 1.Control Systems 2.Electronic Measurements and Instrumentation 3.Intetnet of Things	**OE1:** 1.Principles of Electronics 2.EMI/EMC 3.Principles of Communications	**HONOR COURSES** 1.Computer Networks 2.Artificial Intelligence
SKILL ADVANCED COURSES 1.SCILAB 2.Machine learning using Scikit		**MINOR COURSES** 1.Switching Theory and Logic Design 2.Analog Communications

III B.Tech II Sem

S.No	**Course Name**	Category	L	T	P	Credits
1	VLSI Design	PC	3	0	0	3
2	Digital Signal Processing	PC	3	0	0	3
3	Digital Communications	PC	3	0	0	3
4	Professional Elective courses(PE2)	PE	3	0	0	3
5	Open Elective (OE2)	OE	2	0	2	3
6	VLSI Design Lab	LC	0	0	3	1.5
7	Digital Signal Processing Lab	LC	0	0	3	1.5
8	Digital Communications Lab	LC	0	0	3	1.5
9	Skill advanced course/ soft skill course*		**1**	**0**	**2**	**2**
10	Research Methodology	MC	2	0	0	0
Total credits						**21.5**
	Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)		**4**	0	0	**4**
Industrial/Research Internship (Mandatory) 2 Months during summer vacation						

PE2: 1.Antenna and Wave Propagation 2.Computer Architecture and Organization 3.Soft computing techniques	**OE2:** 1.Biomedical Instrumentation 2.Electronic Measurements and Instrumentation 3.Display Devices	**HONOR COURSES** 1.Machine Learning 2.Digital Control Systems	**MINOR COURSES** 1.Electronic Circuits 2.Linear Integrated Circuits

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV B.Tech I Semester

S.No	**Course Name**	Category	L	T	P	Credits
1	Professional Elective courses(PE3)	PE	3	0	0	3
2	Professional Elective courses(PE4)	PE	3	0	0	3
3	Professional Elective courses(PE5)	PE	3	0	0	3
4	Open Elective (OE3)	OE	2	0	2	3
5	Open Elective (OE4)	OE	2	0	2	3
6.	Universal Human Values 2: Understanding Harmony	MC	3	0	0	3
7.	Skill advanced course/ soft skill course*		1	0	2	2
Industrial/Research Internship 2 Months (Mandatory) after third year (to be evaluated during VII semester			0	0	0	3
					Total credits	**23**
Honors/Minor courses (The hours distribution can be 3-0-2 or 3-1-0 also)			**4**		**0**	**4**

***There is a provision for the Universities/Institutions to implement AICTE mandatory course "Universal Human Values 2: Understanding Harmony" under Humanities and social science Elective in seventh semester for 3 credits.**

PE3: 1.Analog IC Design 2.Microwave Engineering 3.Information Theory & Coding	OE3: 1.VLSI Technology 2.Software Defined Radio 3.Biomedical signal processing	SKILL ADVANCED COURSES/ SOFT SKILL COURSES 1.Introduction to Data Analytics 2.Interfacing with Arduino
PE4: 1.Data Communications & Computer Networks 2.Low power VLSI Design 3.Digital Image Processing	OE4: 1.Principles of Sensors 2. Consumer Electronics 3.Basics of IC Technology	Minor Courses 1.Digital Signal Processing 2.Digital Communications
PE5: 1.DSP processors and Architectures 2.Radar Engineering 3.Embedded Systems	HONOR COURSES 1.Pattern Recognition 2.Image and Video Processing	

IV B.Tech II semester

S.No.	Category	Code	Course Title	Hours per week			Credits
1	Major Project	PROJ	Project Project work, seminar and internship in industry	-	-	-	12
	INTERNSHIP (6 MONTHS)						
						Total credits	**12**

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

DETAILED SYLLABUS

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

I YEAR I SEM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

I Year - I Semester		L	T	P	C
		3	0	0	3
NETWORK ANALYSIS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Gain the knowledge on basic network elements	K5
CO2	Will analyze the RLC circuits behavior in detailed	K3
CO3	Analyze the performance of periodic waveforms	K3
CO4	Gain the knowledge in characteristics of two port network parameters (Z, Y, ABCD, h & g).	K5
CO5	Analyze the filter design concepts in real world applications	K3

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**											**M**				
CO2				**M**									**M**			
CO3														**H**	**H**	
CO4		**M**									**L**					**L**
CO5					**H**											

UNIT	CONTENTS	Hours
UNIT – 1	**Introduction to Electrical Circuits** : Network elements classification, Electric charge and current, Electric energy and potential, Resistance parameter – series and parallel combination, Inductance parameter – series and parallel combination, Capacitance parameter – series and parallel combination. Energy sources: Ideal, Non-ideal, Independent and dependent sources, Source transformation, Kirchoff's laws, Mesh analysis and Nodal analysis problem solving with resistances only including dependent sources also. (Text Books: 1,2,3, Reference Books: 3) **A.C Fundamentals and Network Topology**: Definitions of terms associated with periodic functions: Time period, Angular velocity and frequency, RMS value, Average value, Form factor and peak factor- problem solving, Phase angle, Phasor representation, Addition and subtraction of phasors, mathematical representation of sinusoidal quantities, explanation with relevant theory, problem solving. Principal of Duality with examples. **Network Topology:** Definitions of branch, node, tree, planar, non-planar graph, incidence matrix, basic tie set schedule, basic cut set schedule. (Text Books: 2,3, Reference Books: 3)	12
UNIT – 2	**Transients:** First order differential equations, Definition of time constants, R-L circuit, R-C circuit with DC excitation, Evaluating initial conditions procedure, second order differential equations, homogeneous, non-homogenous, problem solving using R-L-C elements with DC excitation and AC excitation, Response as related to s-plane rotation of roots. Solutions using Laplace transform method. (Text Books: 1,2,3, Reference Books: 1,3)	12
UNIT – 3	**Steady State Analysis of A.C Circuits:** Impedance concept, phase angle, series R-L, R-C, R-L-C circuits problem solving. Complex impedance and phasor notation for R-L, R-C, R-L-C problem solving using mesh and nodal analysis, Star-Delta conversion, problem solving. (Text Books: 1,2, Reference Books: 3) **Coupled Circuits:** Coupled Circuits: Self inductance, Mutual inductance, Coefficient of coupling, analysis of coupled circuits, Natural current, Dot rule of coupled circuits, Conductively coupled equivalent circuits- problem solving.	12
UNIT – 4	**Resonance:** Introduction, Definition of Q, Series resonance, Bandwidth of series resonance, Parallel resonance, Condition for maximum impedance, current in anti resonance, Bandwidth of parallel resonance, general case-resistance present in both branches, anti resonance at all frequencies. (Text Books:2,3, Reference Books: 3) **Network Theorems:** Thevinin's, Norton's, Milliman's, Reciprocity, Compensation, Substitution, Superposition, Max Power Transfer, Tellegens- problem solving using dependent sources also. (Text Books: 1,2,3, Reference Books: 2)	12
UNIT – 5	**Two-port Networks**: Relationship of two port networks, Z-parameters, Y-parameters, Transmission line parameters, h-parameters, Inverse h-parameters, Inverse Transmission line parameters, Relationship between parameter sets, Parallel connection of two port networks, Cascading of two port networks, series connection of two port networks, problem solving including dependent sources also. (Text Books: 1,2, Reference Books: 1,3)	12
	Total	60

TEXT BOOKS:1. Network Analysis – ME Van Valkenburg, Prentice Hall of India, 3rd Edition, 2000.
2. Network Analysis by K.Satya Prasad and S Sivanagaraju, Cengage Learning, **3.** Electric Circuit Analysis by Hayt and Kimmarle, TMH

REFERENCES:
1. Network lines and Fields by John. D. Ryder 2nd edition, Asia publishing house.
2. Basic Circuit Analysis by DR Cunninghan, Jaico Publishers.
3.Network Analysis and Filter Design by Chadha, Umesh Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

I Year - I Semester		L	T	P	C
		3	0	0	3

PROGRAMMING FOR PROBLEM SOLVING USING C

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	To write algorithms and to draw flowcharts for solving problems	K2
CO2	To convert flowcharts/algorithms to C Programs, compile and debug programs	K2
CO3	To use different operators, data types and write programs that use two-way/ multi-way selection	K4
CO4	To select the best loop construct for a given problem	K1
CO5	To design and implement programs to analyze the different pointer applications	K1

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**											**M**				
CO2				**M**									**M**			
CO3														**H**	**H**	
CO4		**M**									**L**					**L**
CO5					**H**											

UNIT	CONTENTS	**Contact Hours**
UNIT - 1	**Introduction to Computers:** Creating and running Programs, Computer Numbering System, Storing Integers, Storing Real Numbers, **Introduction to the C Language:** Background, C Programs, Identifiers, Types, Variable, Constants, Input/output, Programming Examples, Scope, Storage Classes and Type Qualifiers. Pre-Processor Statements, Header Files **Structure of a C Program:** Expressions Precedence and Associativity, Side Effects, Evaluating Expressions, Type Conversion Statements, Simple Programs, Command Line Arguments.	12
UNIT - 2	**Bitwise Operators:** Exact Size Integer Types, Logical Bitwise Operators, Shift Operators. **Selection & Making Decisions:** Logical Data and Operators, Two Way Selection, Multiway Selection, More Standard Functions **Repetition:** Concept of Loop, Pretest and Post-test Loops, Initialization and Updating, Event and Counter Controlled Loops, Loops in C, Other Statements Related to Looping, Looping Applications, and Programming Examples.	12
UNIT - 3	**Arrays:** Concepts, Using Array in C, Array Application, Two Dimensional Arrays, Multidimensional Arrays, Programming Example – Calculate Averages **Strings:** String Concepts, C String, String Input / Output Functions, Arrays of Strings, String Manipulation Functions String/ Data Conversion, A Programming Example – Morse Code **Enumerated, Structure, and Union:** The Type Definition (Type def), Enumerated Types, Structure, Unions, and Programming Application	12
UNIT - 4	**Pointers:** Introduction, Pointers to pointers, Compatibility, L value and R value **Pointer Applications:** Arrays, and Pointers, Pointer Arithmetic and Arrays, Memory Allocation Function, Array of Pointers, Programming Application **Processor Commands**: Processor Commands	12
UNIT - 5	**Functions:** Designing, Structured Programs, Function in C, User Defined Functions, Inter-Function Communication, Standard Functions, Passing Array to Functions, Passing Pointers to Functions, Recursion **Text Input / Output:** Files, Streams, Standard Library Input / Output Functions, Formatting Input / Output Functions, Character Input / Output Functions **Binary Input / Output:** Text versus Binary Streams, Standard Library, Functions for Files, Converting File Type.	12
	Total	60

Text Books:

1. Programming for Problem Solving, Behrouz A. Forouzan, Richard F.Gilberg, CENGAGE
2. The C Programming Language, Brian W.Kernighan, Dennis M. Ritchie, 2e, Pearson

Reference Books:

1. Computer Fundamentals and Programming, Sumithabha Das, Mc Graw Hill
2. Programming in C, Ashok N. Kamthane, Amit Kamthane, Pearson
3. Computer Fundamentals and Programming in C, Pradip Dey, Manas Ghosh, OXFORD

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

I Year - I Semester		L	T	P	C
		0	0	3	1.5
PROGRAMMING FOR PROBLEM SOLVING USING C Lab					

Exercise 1
a. Write a C program to calculate the area of a triangle.
b. Write a C program to find the largest of three numbers using ternary operator.
c. Write a C program to swap two numbers without using temporary variable.

Exercise 2
a. Write a C program to find the 2's complement of a binary number.
b. Write a C program to find the roots of a quadratic equation.
c. Write a C program to implement simple calculator using switch statement.

Exercise 3
a. Write a C program to find the sum of individual digits of a positive integer and also find the reverse of the given number.
b. Write a C program to generate the first n terms of the Fibonacci sequence.
c. Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user.

Exercise 4
a. Write a C program to print the Multiplication table of a given number.
b. Write a C program to read a decimal number and find it's equivalent binary number.
c. Write a C program to check whether the given number is Armstrong number or not.

Exercise 5
a. Write a C program to interchange the largest and smallest numbers in the given array.
b. Write a C program to implement Towers of Hanoi.

Exercise 6
a. Write a C program to implement sorting an array of elements.
b. Write a C program to implement matrix addition and multiplication.
c. Write a C program to print the upper case matrix using Arrays.

Exercise 7
Write a C program that uses functions to perform the following operations.
a. To insert a sub string into given main string at a given position.
b. To delete n characters from a given position in a given string.
c. To replace a character of a string either from beginning or ending or at a Specified location

Exercise 8
Write a C program that uses functions to perform the following operations using Structure:
a. Reading a complex number
b. Writing a complex number
c. Addition of two complex numbers
d. Multiplication of two complex numbers

Exercise 9
Write a C program for the following string operations without using the built-in functions.
a. To concatenate two strings
b. To append a string to another string
c. To compare two strings

Exercise 10
a. Write a C program to find the number of characters in a given string including and excluding spaces.
b. Write a C program to copy the contents of one string to another string without using string handling functions.
c. Write a C program to find whether a given string is palindrome or not.

Exercise 11
Write a C program using recursion for the following:
a. To display sum of digits of a given number
b. To find the factorial of a given integer
c. To find the GCD (Greatest Common Divisor) of two given integers
d. To find Fibonacci sequence.

Exercise 12

a. Write a C program to reverse a string using pointers.

b. Write a C program to compare two 2D arrays using pointers.

c. Write a C program consisting of Pointer based function to exchange value of two integers using passing by address .

Exercise 13

a. Write a C program to find both the largest and smallest number of an array of integers using call by value and call by reference.

b. Write a C program to implement student details using Structures.

Exercise 14

a. Write a C program which copies one file to another.

b. Write a C program to count the number of characters and number of lines in a file.

c. Write a C program to merge two files into a third file. The names of the files must be entered using command line arguments.

Exercise 15

Write a C program to implement Different Storage classes.

a. Auto

b. Static

c. Register

d. External

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

I YEAR II SEM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

I Year - II Semester		L	T	P	C
		0	0	3	1.5
ELECTRONIC WORKSHOP Lab					

I. Identification of components
II. Laboratory equipment
III. Soldering practice
IV. PCB Layout
V. Testing of Components
VI. CRO

I. Identification of components:

- Resistors:- Types of Resistors, Value of Resistance using color code, DRBS.
- Capacitors:- Types of capacitors, value of capacitance using color code, DCBS.
- Inductors:- Types of Inductors, DLB
- Rheostats:- Types of Rheostats, Types of potentiometers, Relays.
- Switches:- Types of Switches.
- Cables: Types of Cables.
- Types of Instruments used.

Identification of active elements.

(Two Terminal, Three Terminal Devices)

- (SC diode, Zener diode, D.AC)
- Three Terminal Devices: BJT, UJT, SCR, FET, MOSFET, TRIAC.
- Digital and Analog ICs. (TO and Flat packages) IC regulators types.
- Testing of above components using Multi metros.

II. **Laboratory Equipment:**

A) Meters:-

- Types of Voltmeters, Types of Ammeters both Analog and Digital.
- Types of Multi meters (Analog & Digital)
- AVO Meters.
- FET input Voltmeter.

B) Laboratory Function Generators and Audio Oscillators.
C) Power Supplies.
D) RF generators.
E) Different Types of Transformers.
(Power, AF, RF, etc..)

III. Soldering practice

Tools kit including soldering iron
Tools Kit:

- Insulated nose player
- Insulated cutting player
- Screw driver kit
- Electrical tester
- Soldering iron, Lead, Flex

IV. PCB layout and Design.
Materials required, centimeter graph sheets, marker.

V. Testing of Components.
Active and Passive Components

VI. CRO
Acquaintance with CRO
Measurements on CRO

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II YEAR I SEM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

		L	T	P	C
II Year - I Semester		**3**	**0**	**0**	**3**
ELECTRONIC DEVICES AND CIRCUITS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Apply the basic concepts of semiconductor physics.	K4
CO2	Understand the formation of p-n junction and how it can be used as a p-n junction as diode in different modes of operation.	K5
CO3	Know the construction, working principle of rectifiers with and without filters with relevant expressions and necessary comparisons..	K1
CO4	Understand the construction, principle of operation of transistors, BJT and FET with their V-I characteristics in different configurations.	K1
CO5	Know the need of transistor biasing, various biasing techniques for BJT and FET and stabilization concepts with necessary expressions & small signal low frequency transistor amplifier circuits using BJT and FET in different configurations	K6

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1		**H**														M
CO2						**H**										
CO3					**M**								L			
CO4				**L**										M		
CO5															H	

UNIT	CONTENTS	Contact Hours
UNIT – 1	**Review of Semiconductor Physics:** Hall effect, continuity equation, law of junction, Fermi Dirac function, Fermi level in intrinsic and extrinsic Semiconductors **Junction Diode Characteristics :** energy band diagram of PN junction Diode, Open circuited p-n junction, Biased p-n junction, p-n junction diode, current components in PN junction Diode, diode equation, V-I Characteristics, temperature dependence on V-I characteristics, Diode resistance, Diode capacitance.	12
UNIT – 2	**Special Semiconductor Devices**: Zener Diode, Breakdown mechanisms, Zener diode applications, LED, Varactor Diode, Photodiode, Tunnel Diode, UJT, PNPN Diode, SCR. Construction, operation and V-I characteristics. **Rectifiers and Filters:** Basic Rectifier setup**,** half wave rectifier, full wave rectifier, bridge rectifier, derivations of characteristics of rectifiers, rectifier circuits-operation, input and output waveforms, Filters, Inductor filter(Series inductor), Capacitor filter(Stunt inductor), π-Filter, comparison of various filter circuits in terms of ripple factors.	12
UNIT – 3	**Transistor Characteristics: BJT:** Junction transistor, transistor current components, transistor equation, transistor configurations, transistor as an amplifier, characteristics of transistor in Common Base, Common Emitter and Common Collector configurations, Ebers-Moll model of a transistor, punch through/ reach through, Photo transistor, typical transistor junction voltage values. **FET:** FET types, construction, operation, characteristicsμ, g_m $r_d$parameters, MOSFET-types, construction, operation, characteristics, comparison between JFET and MOSFET.	12
UNIT – 4	**Transistor Biasing and Thermal Stabilization :** Need for biasing, operating point, load line analysis, BJT biasing- methods, basic stability, fixed bias, collector to base bias, self bias, Stabilization against variations in V_{BE}, Ic, and β Stability factors, (S,S',S''), Bias compensation, Thermal runaway, Thermal stability.FET Biasing- methods and stabilization.	12
UNIT – 5	**Small Signal Low Frequency Transistor Amplifier Models: BJT:** Two port network, Transistor hybrid model determination of h-parameters, conversion of h-parameters, generalized analysis of transistor amplifier model using h-parameters, Analysis of CB, CE and CC amplifiers using exact and approximate analysis, Comparison of transistor amplifiers.**FET:** Generalized analysis of small signal model, Analysis of CG, CS and CD amplifiers, comparison of FET amplifiers.	12
	Total	60

Text Books:

1. Integrated Electronics-J. Millman, C. Halkias,TataMc-Graw Hill, Second Edition,2009
2. Electronic Devices and Circuits by David A. Bell, Oxford University Press
3. Electronics devices & circuit theory- Robert L.Boylestad and LouiNashelsky, Pearson/Prentice hall, tenth edition,2009

References:

1. Electronic Devices and Circuits- J. Millman, C. Halkias, Tata Mc-Graw Hill, SecondEdition,2007
2. Electronic Devices and Circuits-K. Lal Kishore, BS Publications, Fourth Edition,2016.
3. Electronic Devices and Circuits-Salivahanan, Kumar, Vallavaraj, Tata Mc-Graw Hill, 4th Edition,2008.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
SWITCHING THEORY and LOGIC DESIGN					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Classify different number systems and apply to generate various codes.	K3
CO2	Design different types of combinational logic circuits.	K1
CO3	Apply knowledge of flip-flops in designing of Registers and counters	K4
CO4	The operation and design methodology for synchronous sequential circuits and algorithmic state machines.	K1
CO5	Produce innovative designs by modifying the traditional design techniques & concept of Boolean algebra in minimization of switching functions	K1

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**M**															
CO2				**H**									M			
CO3		**L**												H		
CO4															H	
CO5					**H**											L

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**REVIEW OF NUMBER SYSTEMS &CODES:** Representation of numbers of different radix, conversation from one radix to another radix, r-1's compliments and r's compliments of signed members. Gray code ,4 bit codes; BCD, Excess-3, 2421, 84-2-1 code etc. Error detection & correction codes: parity checking, even parity, odd parity, Hamming code. **BOOLEAN THEOREMS AND LOGIC OPERATIONS:** Boolean theorems, principle of complementation & duality, De-morgan theorems. Logic operations ; Basic logic operations -NOT, OR, AND, Universal Logic operations, EX-OR, EX-NOR operations. Standard SOP and POS Forms, NAND-NAND and NOR-NOR realizations, Realization of three level logic circuits. Study the pin diagram and obtain truth table for the following relevant ICs 7400,7402,7404,7408,7432,7486	12
UNIT – 2	**MINIMIZATION TECHNIQUES:** Minimization and realization of switching functions using Boolean theorems, K-Map (up to 6 variables)and tabular method(Quine-mcCluskey method) with only four variables and single function **COMBINATIONAL LOGIC CIRCUITS DESIGN:** Design of Half adder, full adder, half subtractor, full subtractor applications of full adders; 4-bit adder-subtractor circuit, BCD adder circuit, Excess 3 adder circuit and carry look-a-head adder circuit, Design code converts using Karnaugh method and draw the complete circuit diagrams.	12
UNIT – 3	**COMBINATIONAL LOGIC CIRCUITS DESIGN USING MSI &LSI :**Design of encoder ,decoder, multiplexer and de-multiplexers, Implementation of higher order circuits using lower order circuits .Realization of Boolean functions using decoders and multiplexers. Design of Priority encoder, 4-bit digital comparator and seven segment decoder. . Study the relevant ICs pin diagrams and their functions 7442,7447,7485,74154.**INTRODUCTION OF PLD's :**PLDs: PROM, PAL, PLA -Basics structures, realization of Boolean functions, Programming table.	12
UNIT – 4	**SEQUENTIAL CIRCUITS I:**Classification of sequential circuits (synchronous and asynchronous) , operation of NAND & NOR Latches and flip-flops; truth tables and excitation tables of RS flip-flop, JK flip-flop, T flip-flop, D flip-flop with reset and clear terminals. Conversion from one flip-flop to another flip-flop. Design of 5ripple counters, design of synchronous counters, Johnson counter, ring counter. Design of registers - Buffer register, control buffer register, shift register, bi-directional shift register, universal shift, register. Study the following relevant ICs and their relevant functions 7474,7475,7476,7490,7493,74121.	12
UNIT – 5	**SEQUENTIAL CIRCUITS II :**Finite state machine; state diagrams, state tables, reduction of state tables. Analysis of clocked sequential circuits Mealy to Moore conversion and vice-versa. Realization of sequence generator, Design of Clocked Sequential Circuit to detect the given sequence (with overlapping or without overlapping).	12
	Tota	60

TEXT BOOKS:

1. Switching and finite automata theory Zvi.KOHAVI, Niraj.K.Jha 3rd Edition, Cambridge University Press,2009
2. Digital Design by M.MorrisMano,Michael D Ciletti,4th edition PHI publication,2008
3. Switching theory and logic design by Hill and Peterson**,**Mc-Graw Hill TMH edition, 2012.

REFERENCES:

1. Fundamentals of Logic Design by Charles H. Roth Jr, Jaico Publishers,2006
2. Digital electronics by R S Sedha.S.Chand& company limited,2010
3. Switching Theory and Logic Design by A. AnandKumar,PHI Learning pvt ltd,2016.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
SIGNALS and SYSTEMS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Differentiate the various classifications of signals and systems	K2
CO2	Analyze the frequency domain representation of signals using Fourier concepts	K3
CO3	Classify the systems based on their properties and determine the response of LTI Systems	K3
CO4	Know the sampling process and various types of sampling techniques.	K5
CO5	Apply Laplace and z-transforms to analyze signals and Systems (continuous & discrete).	K4

	PO1	**PO 2**	**PO 3**	**PO 4**	**PO 5**	**PO 6**	**PO 7**	**PO 8**	**PO 9**	**PO 10**	**PO 11**	**PO 12**	**PSO 1**	**PSO 2**	**PSO 3**	**PSO4**
CO1	**L**												L			
CO2			**L**												H	
CO3		**M**												M		L
CO4					**M**											
CO5							**H**									

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**INTRODUCTION:** Definition of Signals and Systems, Classification of Signals, Classification of Systems, Operations on signals: time-shifting, time-scaling, amplitude-shifting, amplitude-scaling. Problems on classification and characteristics of Signals and Systems. Complex exponential and sinusoidal signals, Singularity functions and related functions: impulse function, step function signum function and ramp function. Analogy between vectors and signals, orthogonal signal space, Signal approximation using orthogonal functions, Mean square error, closed or complete set of orthogonal functions, Orthogonality in complex functions. Related Problems.	12
UNIT – 2	**FOURIER SERIES AND FOURIER TRANSFORM:** Fourier series representation of continuous time periodic signals, properties of Fourier series, Dirichlet's conditions, Trigonometric Fourier series and Exponential Fourier series, Relation between Trigonometric and Exponential Fourier series, Complex Fourier spectrum. Deriving Fourier transform from Fourier series, Fourier transform of arbitrary signal, Fourier transform of standard signals, Fourier transform of periodic signals, properties of Fourier transforms, Fourier transforms involving impulse function and Signum function. Introduction to Hilbert Transform. Related Problems.	12
UNIT – 3	**ANALYSIS OF LINEAR SYSTEMS:** Introduction**,** Linear system, impulse response, Response of a linear system, Linear time invariant (LTI) system, Linear time variant (LTV) system, Concept of convolution in time domain and frequency domain, Graphical representation of convolution, Transfer function of a LTI system, Related problems. Filter characteristics of linear systems. Distortion less transmission through a system, Signal bandwidth, system bandwidth, Ideal LPF, HPF and BPF characteristics, Causality and Poly-Wiener criterion for physical realization, relationship between bandwidth and rise time	12
UNIT – 4	**CORRELATION:** Auto-correlation and cross-correlation of functions, properties of correlation function, Energy density spectrum, Parseval's theorem, Power density spectrum, Relation between Convolution and correlation, Detection of periodic signals in the presence of noise by correlation, Extraction of signal from noise by filtering. **SAMPLING THEOREM :** Graphical and analytical proof for Band Limited Signals, impulse sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, effect of under sampling – Aliasing, Introduction to Band Pass sampling, Related problems.	12
UNIT – 5	**LAPLACE TRANSFORMS:** Introduction**,** Concept of region of convergence (ROC) for Laplace transforms, constraints on ROC for various classes of signals, Properties of L.T's, Inverse Laplace transform, Relation between L.T's, and F.T. of a signal. Laplace transform of certain signals using waveform synthesis. **Z–TRANSFORMS:** Concept of Z- Transform of a discrete sequence. Region of convergence in Z-Transform, constraints on ROC for various classes of signals, Inverse Z-transform, properties of Z-transforms. Distinction between Laplace, Fourier and Z transforms.	12
	Total	60

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, BS Publications, 2003.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, PHI, 2ndEdn,1997
3. Signals & Systems - Simon Haykin and Van Veen, Wiley, 2ndEdition,2007

REFERENCE BOOKS:

1. Principles of Linear Systems and Signals – BP Lathi, Oxford University Press,2015
2. Signals and Systems – T K Rawat , Oxford University press,2011

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**

RANDOM VARIABLES and STOCHASTIC PROCESSES

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Mathematically model the random phenomena and solve simple probabilistic problems.	K3
CO2	Identify different types of random variables	K5
CO3	Characterize the random processes in the time and frequency domains.	K3
CO4	Analyze the LTI systems with random inputs.	K4
CO5	Identify different types of statistical averages of the random variables.	K6

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**M**											**L**			
CO2				**L**										**M**		**H**
CO3	**L**														**M**	
CO4					**H**											
CO5							**H**								**M**	

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**THE RANDOM VARIABLE:** Introduction, Review of Probability Theory, Definition of a Random Variable Conditions for a Function to be a Random Variable, Discrete, Continuous and Mixed Random Variables Distribution and Density functions, Properties, Binomial, Poisson, Uniform, Gaussian, Exponential, Rayleigh Conditional Distribution, Conditional Density, Properties.	12
UNIT – 2	**OPERATION ON ONE RANDOM VARIABLE - EXPECTATIONS:** Introduction, Expected Value of a Random Variable, Function of a Random Variable, Moments about the Origin, Central Moments, Variance and Skew, Chebychev's Inequality, Characteristic Function, Moment Generating Function, Transformations of a Random Variable: Monotonic Transformations for a Continuous Random Variable, Non-monotonic Transformations of Continuous Random Variable.	12
UNIT – 3	**MULTIPLE RANDOM VARIABLES:** Vector Random Variables, Joint Distribution Function, Properties of Joint Distribution, Marginal Distribution Functions, Conditional Distribution and Density, Statistical Independence, Sum of Two Random Variables, Sum of Several Random Variables, Central Limit Theorem Unequal Distribution, Equal Distributions. **OPERATIONS ON MULTIPLE RANDOM VARIABLES**: Joint Moments about the Origin, Joint Central Moments, Joint Characteristic Functions, Jointly Gaussian Random Variables: Two Random Variables case, N Random Variables case, Properties, Transformations of Multiple Random Variables, Linear Transformations of Gaussian Random Variables.	12
UNIT – 4	**RANDOM PROCESSES – TEMPORAL CHARACTERISTICS:** The Random Process Concept Classification of Processes, Deterministic and Nondeterministic Processes, Distribution and Density Functions Concept of Stationarity and Statistical Independence. First-Order Stationary Processes, Second-order and Wide-Sense Stationarity, N^{th}-order and Strict -Sense Stationarity, Time Averages and Ergodicity, Autocorrelation Function and its Properties, Cross-Correlation Function and its Properties, Covariance Functions, Gaussian Random Processes, Poisson Random Process.	12
UNIT – 5	**RANDOM PROCESSES - SPECTRAL CHARACTERISTICS:** The Power Density Spectrum: Properties Relationship between Power Density Spectrum and Autocorrelation Function, The Cross-Power Density Spectrum, Properties, Relationship between Cross-Power Density Spectrum and Cross-Correlation Function **LINEAR SYSTEMS WITH RANDOM INPUTS:** Random Signal Response of Linear Systems: System Response – Convolution, Mean and Mean-squared Value of System Response, Autocorrelation Function of Response, Cross-Correlation Functions of Input and Output, Spectral Characteristics of System Response: Power Density Spectrum of Response, Cross-Power Density Spectra of Input and Output, Band pass, Band-Limited and Narrowband Processes, Properties.	12
	Total	60

TEXT BOOKS:

1. Probability, Random Variables & Random Signal Principles, Peyton Z. Peebles, TMH, 4th Edition, 2001.
2. Probability, Random Variables and Stochastic Processes, Athanasios Papoulis and S.Unnikrisha, PHI, 4th Edition, 2002.

REFERENCE BOOKS:

1. Probability and Random Processes with Applications to Signal Processing, Henry Stark and John W. Woods, Pearson Education, 3rd Edition.
2. Schaum's Outline of Probability, Random Variables, and Random Processes.
3. An Introduction to Random Signals and Communication Theory, B.P. Lathi, International Textbook, 1968

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - I Semester		L	T	P	C
		0	0	3	1.5
ELECTRONIC DEVICES AND CIRCUITS LAB					

Note: The students are required to perform the experiment to obtain the V-I characteristics and to determine the relevant parameters from the obtained graphs.

List of Experiments: (Minimum of Ten Experiments has to be performed)

1. P-N Junction Diode Characteristics
   Part A: Germanium Diode (Forward bias& Reverse bias)

   Part B: Silicon Diode (Forward Bias only)

2. Zener Diode Characteristics
   Part A: V-I Characteristics
   Part B: Zener Diode as Voltage Regulator

3. Rectifiers (without and with c-filter)
   Part A: Half-wave Rectifier
   Part B: Full-wave Rectifier

4. BJT Characteristics(CE Configuration)
   Part A: Input Characteristics
   Part B: Output Characteristics

5. FET Characteristics(CS Configuration) Part A: Drain Characteristics
   Part B: Transfer Characteristics

6. SCR Characteristics
7. UJT Characteristics
8. Transistor Biasing
9. CRO Operation and its Measurements
10. BJT-CE Amplifier
11. Emitter Follower-CC Amplifier
12. FET-CS Amplifier

Equipment required:

1. Regulated Power supplies
2. Analog/Digital Storage Oscilloscopes
3. Analog/Digital Function Generators
4. Digital Multi-meters
5. Decade Résistance Boxes/Rheostats
6. Decade Capacitance Boxes
7. Ammeters (Analog or Digital)
8. Voltmeters (Analog or Digital)
9. Active & Passive Electronic Components

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - I Semester		**L**	**T**	**P**	**C**
		0	**0**	**3**	**1.5**

SWITCHING THEORY and LOGIC DESIGN LAB

List of Experiments: (Minimum of Twelve Experiments has to be performed)

1. Verification of truth tables of Logic gates
   Two input (i) OR (ii) AND (iii) NOR (iv) NAND (v) Exclusive OR (vi) Exclusive NOR
2. Design a simple combinational circuit with four variables and obtain minimal SOP expression and verify the truth table using Digital Trainer Kit
3. Verification of functional table of 3 to 8 line Decoder / De-multiplexer
4. 4 variable logic function verification using 8 to 1 multiplexer.
5. Design full adder circuit and verify its functional table.
6. Verification of functional tables of
   (i) J K Edge triggered Flip – Flop
   (ii) J K Master Slave Flip – Flop
   (iii) D Flip - Flop
7. Design a four bit ring counter using D Flip – Flops / JK Flip Flop and verify output
8. Design a four bit Johnson's counter using D Flip-Flops / JK Flip Flops and verify output
9. Verify the operation of 4-bit Universal Shift Register for different Modes of operation.
10. Draw the circuit diagram of MOD-8 ripple counter and construct a circuit using T-Flip-Flops and Test it with a low frequency clock and Sketch the output waveforms.
11. Design MOD – 8 synchronous counter using T Flip-Flop and verify the result and Sketch the output waveforms.
12. (a) Draw the circuit diagram of a single bit comparator and test the output
    (b) Construct 7 Segment Display Circuit Using Decoder and 7 Segment LED and test it.

ADD on Experiments:

1. Design BCD Adder Circuit and Test the Same using Relevant IC
2. Design Excess-3 to 9-Complement convertor using only four Full Adders and test the Circuit.
3. Design an Experimental model to demonstrate the operation of 74154 De-Multiplexer using LEDs for outputs.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - I Semester		L	T	P	C
		0	0	3	1.5

OBJECT ORIENTED DESIGN & PROGRAMMING USING JAVA LAB

List of programs to be executed:

1. The Fibonacci sequence is defined by the following rule. The first 2 values in the sequence are 1, 1. Every subsequent value is the sum of the 2 values preceding it. Write a Java Program that uses both recursive and non-recursive functions to print the nth value of the Fibonacci sequence.
2. Write a Java Program that prompts the user for an integer and then prints out all the prime numbers up to that integer.
3. Write a java program to implement call by value and call by reference mechanisms.
4. Write a Java Program that checks whether a given string is a palindrome or not.
5. Write a Java Program to check the compatibility for multiplication, if compatible multiply two matrices and find its transpose.
6. Write a Java program to implement constructor overloading and method overloading.
7. Write a Java Program that illustrates how runtime polymorphism is achieved.
8. Write a Java Program that illustrates the use of super keyword.
9. Write a Java Program to create and demonstrate packages.
10. Write a Java Program, using StringTokenizer class, which reads a line of integers and then displays each integer and the sum of all integers.
11. Write a Java Program that reads on file name form the user then displays information about whether the file exists, whether the file is readable/ writable, the type of file and the length of the file in bytes and display the content of the using FileInputStream class.
12. Write a Java Program that displays the number of characters, lines and words in a text/text file.
13. Write a Java Program to implement a Queue, using user defined Exception Handling (also make use of throw, throws).
14. Write a Java Program that creates 3 threads by extending Thread class. First thread displays “Good Morning” every 1 sec, the second thread displays “Hello” every 2 seconds and the third displays “Welcome” every 3 seconds. (Repeat the same by implementing Runnable).
15. Write a Java Program demonstrating the life cycle of a thread.
16. Write an Applet that displays the content of a file.
17. Write a Java Program that works as a simple calculator. Use a grid layout to arrange buttons for the digits and for the +-*?% operations. Add a text field to display the result
18. Write a Java Program for handling mouse events, keyboard events.
19. Write a Java Program that allows user to draw lines, rectangles and ovals.
20. Write a Java Program that lets users create Pie charts. Design your own user interface (with Swings & AWT).

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - I Semester		**L**	**T**	**P**	**C**
		1	**0**	**2**	**2**
PYTHON PROGRAMMING (SKILL ORIENTED COURSE)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	To acquire programming skills in core Python.	K1
CO2	To acquire Object Oriented Skills in Python	K5
CO3	To develop the skill of designing Graphical user Interfaces in Python	K1
CO4	To develop the ability to write database applications in Python	K2
CO5	Ability write algorithms and draw flow charts for solving problems	K4

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**					**H**							**L**		**M**	
CO2				**M**										**M**		
CO3			**L**													**H**
CO4		**M**							**H**						**M**	
Co5						**M**										

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	Introduction: History of Python, Need of Python Programming, Applications Basics of Python Programming Using the REPL(Shell), Running Python Scripts, Variables, Assignment, Keywords, Input-Output, Indentation. Types - Integers, Strings, Booleans;.	12
UNIT – 2	Operators and Expressions: Operators- Arithmetic Operators, Comparison (Relational) Operators Assignment Operators, Logical Operators, Bitwise Operators, Membership Operators, Identity Operators, Expressions and order of evaluations. Data Structures Lists - Operations, Slicing, Methods; Tuples, Sets Dictionaries, Sequences. Comprehensions	12
UNIT – 3	Control Flow - if, if-elif-else, for, while, break, continue, pass Functions - Defining Functions, Calling Functions, Passing Arguments, Keyword Arguments, Default Arguments, Variable-length arguments Anonymous Functions, Fruitful Functions(Function Returning Values), Scope of the Variables in a Function - Global and Local Variables.	12
UNIT – 4	Usage of Numpy for numerical Data, Usage of Pandas for Data Analysis, Matplotlib for Python plotting	12
UNIT – 5	Seaborn for Statical plots, interactive Dynamic visualizations, SciKit for Machine learning.	12
	Tota	60

TEXT BOOKS

1. Python Programming: A Modern Approach, Vamsi Kurama, Pearson
2. Learning Python, Mark Lutz, Orielly

Reference Books:

1. Think Python, Allen Downey, Green Tea Press
2. Core Python Programming, W.Chun, Pearson.
3. Introduction to Python, Kenneth A. Lambert, Cengage
4. Michael Urban and Joel Murach, Python Programming, Shroff/Murach, 2016
5. Haltermanpythonhttps://github.com/halterman/PythonBook-SourceCode
6. Charles Severance et al, Python for Everybody: Exploring Data in Python 3

II YEAR II SEM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
LINEAR IC APPLICATIONS					

Pre-requisite: Network Theory, Electronic Devices and Circuits, Electronic Circuit Analysis
Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Analyse the Differential Amplifier with Discrete components	K4
CO2	Describe the Op-Amp and internal Circuitry: 555 Timer, PLL	K1
CO3	Discuss the Applications of Operational amplifier: 555 Timer, PLL	K2
CO4	Design the Active filters using Operational Amplifier	K5
CO5	Use the Op-Amp in A to D & D to A Converters	K3

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1					**H**										**L**	**L**
CO2			**L**												**H**	**M**
CO3				**M**											**M**	**M**
CO4				**M**											**H**	**M**
CO5					**M**										**M**	**H**

UNIT	CONTENTS	**Hours**
UNIT – 1	**Integrated Circuits:** **Differential Amplifier**- DC and AC analysis of (i) Dual input Balanced output Configuration, (ii) Dual Input Unbalanced Output, (iii)Single Ended Input – Balanced Output (iv) Single Ended Input – un Balanced Output, Cascade Differential Amplifier Stages, Level translator. (Text Book: Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI,1993) **Operational Amplifier:** Introduction, Basic information of Op-Amp, Ideal Operational Amplifier,Op-Amp internal Circuit, Examples of IC Op-Amps, FET Operational Amplifier (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003).Block Diagram Representation of Typical Op-Amp, Analysis of Typical Op-Amp Equivalent Circuit(only MC1435)(Text Book: Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI,1993).OP-Amps Characteristics: Introduction, DC and AC characteristics,741 op-amp & its features.	12
UNIT – 2	**OP-AMPS Applications:** Introduction, Basic Op-Amp Applications, Instrumentation Amplifier, AC Amplifier, V to I and I to V Converter, Sample and Hold Circuit, Log and Antilog Amplifier, Multiplier and Divider, Differentiator, integrator. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003) **Comparators and Waveform Generators:** Introduction, Comparator, Square Wave Generator, Monostable Multivibrator, Triangular Wave Generator, Sine Wave Generators. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	12
UNIT – 3	**Active Filters:** Design & Analysis of Butterworth active filters – 1st order, 2nd order LPF, HPF filters. Band pass, Band reject and all pass filters. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	12
UNIT – 4	**Timer**s: Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger. **Phase Locked Loops:** Introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK demodulators. Applications of VCO (566) (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	12
UNIT – 5	**Digital To Analog And Analog To Digital Converters:** Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, A-D Converters – parallel Comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC.DAC and ADC Specifications. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	12
	Tota	60

Text Books:

1. Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition 2003.
2. Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI, 1993.

References:

1. Operational Amplifiers & Linear Integrated Circuits –Sanjay Sharma ;SK Kataria&Sons;2nd Edition,2010
2. Design with Operational Amplifiers & Analog Integrated Circuits – Sergio Franco, McGraw Hill, 1988.
3. OP AMPS and Linear Integrated Circuits concepts and Applications, James M Fiore, Cenage Learning India
4. Operational Amplifiers & Linear Integrated Circuits–R.F.Coughlin& Fredrick Driscoll, PHI,6th Edition.
5. Operational Amplifiers & Linear ICs – David A Bell, Oxford Uni. Press, 3rd Edition.
6. Operational Amplifiers–C.G. Clayton, Butterworth & Company Publ. Ltd./Elsevier, 1971.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year-II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
ELECTRONIC CIRCUIT ANALYSIS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Design and analysis of small signal high frequency transistor amplifier using BJT and FET.	K4
CO2	Design and analysis of multi stage amplifiers using BJT and FET and Differential amplifier using BJT.	K3&K4
CO3	Derive the expressions for frequency of oscillation and condition for oscillation of RC and LC oscillators and their amplitude and frequency stability concept.	K3
CO4	Know the characteristics of feedback amplifiers and design feedback amplifier based on the given specifications	K4
CO5	Know the importance of power amplifiers and tuned amplifiers	K3

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	M		H	M									H		H	M
CO2	M		H	M											M	H
CO3	M		M	H									H			M
CO4	L		H	M											H	H
CO5	L		M	H									M			M

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**Small Signal High Frequency Transistor Amplifier models: BJT:** Transistor at high frequencies Hybrid- π common emitter transistor model, Hybrid π conductance, Hybrid π capacitances, validity of hybrid π model, determination of high-frequency parameters in terms of low-frequency parameters , CE short circuit current gain, current gain with resistive load, cut-off frequencies, frequency response and gain bandwidth product.**FET:** Analysis of common Source and common drain Amplifier circuits at high frequencies.	12
UNIT – 2	**Multistage Amplifiers:** Classification of amplifiers, methods of coupling**, c**ascaded transistor amplifier and its analysis, analysis of two stage RC coupled amplifier, high input resistance transistor amplifier circuits and their analysis-Darlington pair amplifier, Cascode amplifier, Boot-strap emitter follower Differential amplifier using BJT.	12
UNIT – 3	**Feedback Amplifiers :** Feedback principle and concept, types of feedback, classification of amplifiers feedback topologies, Characteristics of negative feedback amplifiers, Generalized analysis of feedback amplifiers, Performance comparison of feedback amplifiers, Method of analysis of feedback amplifiers.	12
UNIT – 4	**Oscillators:** Oscillator principle, condition for oscillations, types of oscillators, RC-phase shift and Wien bridge oscillators with BJT and FET and their analysis, Generalized analysis of LC Oscillators, Hartley and Colpitt's oscillators using BJT, Frequency and amplitude stability of oscillators.	12
UNIT – 5	**Power Amplifiers:** Classification of amplifiers(A to H), Class A power Amplifiers, Class B Push-pull amplifiers, Complementary symmetry push pull amplifier, Class AB power amplifier, Class-C power amplifier, Thermal stability and Heat sinks. **Tuned Amplifiers**: Introduction, Q-Factor, small signal tuned amplifier, capacitance single tuned amplifier, double tuned amplifiers, , staggered tuned amplifiers	12
	Tota	60

Text Books:

1. Integrated Electronics- J. Millman and C.C. Halkias, Tata McGraw-Hill, 1972.
2. Electronic Devices and Circuits Theory – Robert L. Boylestad and Louis Nashelsky, Pearson/Prentice Hall, Tenth Edition, 2009.
3. Electronic Devices and Integrated Circuits – B.P. Singh,Rekha , Pearson publications, 2006

References:

1. Electronic Circuit Analysis and Design – Donald A. Neaman, McGrawHill, 2010.
2. Microelectronic Circuits-Sedra A.S. and K.C. Smith, Oxford University Press, Sixth Edition, 2011.
3. Electronic Circuit Analysis-B.V.Rao, K.R.Rajeswari, P.C.R.Pantulu, K.B.R.Murthy, Pearson Publications.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year-II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
ANALOG COMMUNICATIONS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Describe various Analog modulation and demodulation schemes and their spectral characteristics	K2
CO2	Analyze noise characteristics of various analog modulation methods	K4
CO3	Discuss various functional blocks of radio transmitters and receivers	K1
CO4	Design simple analog systems for various modulation techniques.	K5
CO5	Apply basic methods of probability and random variables to signal-to-noise ratios	K3

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1				**M**									H		H	M
CO2	**L**		**M**												M	H
CO3						**M**							H			M
CO4							**H**								H	H
CO5				M												

UNIT	CONTENTS	**Contact Hours**
UNIT - 1	**AMPLITUDE MODULATION :** Introduction to communication system, Need for modulation, Frequency Division Multiplexing , Amplitude Modulation, Definition, Time domain and frequency domain description, single tone modulation, power relations in AM waves, Generation of AM waves, square law Modulator, Switching modulator, Detection of AM Waves; Square law detector, Envelope detector.	12
UNIT - 2	**DSB & SSB MODULATION:** Double side band suppressed carrier modulators, time domain and frequency domain description, Generation of DSBSC Waves, Balanced Modulators, Ring Modulator, Coherent detection of DSB-SC Modulated waves, COSTAS Loop. Frequency domain description, Frequency discrimination method for generation of AM SSB Modulated Wave, Time domain description, Phase discrimination method for generating AM SSB Modulated waves. Demodulation of SSB Waves, Vestigial side band modulation: Frequency description, Generation of VSB Modulated wave, Time domain description, Envelope detection of a VSB Wave pulse Carrier, Comparison of AM Techniques, Applications of different AMSystems, FDM.	12
UNIT - 3	**ANGLE MODULATION**: Basic concepts, Frequency Modulation: Single tone frequency modulation, Spectrum Analysis of Sinusoidal FM Wave, Narrow band FM, Wide band FM, Constant Average Power, Transmission bandwidth of FM Wave - Generation of FM Waves, Detection of FM Waves: Balanced Frequency discriminator, Zero crossing detector, Phase locked loop. Comparison of FM & AM.	12
UNIT - 4	**TRANSMITTERS & RECEIVERS: Radio Transmitter** - Classification of Transmitter, AM Transmitter, Effect of feedback on performance of AM Transmitter, FM Transmitter – Variable reactance type and phase modulated FM Transmitter, frequency stability in FM Transmitter. **Radio Receiver** - Receiver Types - Tuned radio frequency receiver, Super hetro dyne receiver, RF section and Characteristics - Frequency changing and tracking, Intermediate frequency, AGC, FM Receiver, Comparison with AM Receiver, Amplitude limiting. Communication Receivers, extensions of super heterodyne principle and additionalcircuits.	12
UNIT - 5	**NOISE:** Review of noise and noise sources, noise figure**,** Noise in Analog communication Systems, Noise in DSB& SSB System, Noise in AM System, Noise in Angle Modulation Systems, Threshold effect in Angle Modulation System, Pre-emphasis & de-emphasis **PULSE MODULATION:** Types of Pulse modulation, PAM (Single polarity, double polarity) PWM: Generation & demodulation of PWM, PPM, Generation and demodulation of PPM, Time Division Multiplexing, TDM Vs FDM	12
	Total	60

TEXT BOOKS:

1. Principles of Communication Systems – H Taub& D. Schilling, GautamSahe, TMH, 3rdEdition, 2007.
2. Principles of Communication Systems - Simon Haykin, John Wiley, 2ndEdition, 2007.

REFERENCES:

1. Electronics & Communication System – George Kennedy and Bernard Davis, TMH 2004
2. Communication Systems– R.P. Singh, SP Sapre, Second Edition TMH,2007.
3. Electronic Communication systems – Tomasi,Pearson, fourth Edition, 2007.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

		L	T	P	C
II Year - II Semester		**0**	**0**	**3**	**1.5**
LINEAR IC APPLICATIONS LAB					

Minimum Twelve Experiments to be conducted :

1. Study of ICs – IC 741, IC 555, IC 565, IC 566, IC 1496 functioning, parameters andSpecifications.
2. OP AMP Applications – Adder, Subtractor, Comparator Circuits.
3. Integrator and Differentiator Circuits using IC 741.
4. Active Filter Applications – LPF, HPF (first order)
5. Active Filter Applications – BPF, Band Reject (Wideband) and Notch Filters.
6. IC 741 Oscillator Circuits – Phase Shift and Wien Bridge Oscillators.
7. Function Generator using OP AMPs.
8. IC 555 Timer – Monostable Operation Circuit.
9. IC 555 Timer – Astable Operation Circuit.
10. Schmitt Trigger Circuits – using IC 741 and IC 555.
11. IC 565 – PLL Applications.
12. IC 566 – VCO Applications.
13. 4 bit DAC using OP AMP.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year-II Semester		**L**	**T**	**P**	**C**
		0	**0**	**3**	**1.5**

ANALOG COMMUNICATIONS LAB

List of Experiments:

(Twelve experiments to be done- **The students have to calculate the relevant parameters**)–
(a. Hardware, b. MATLAB Simulink c. MATLAB Communication toolbox)

A. Amplitude Modulation - Modulation &Demodulation
B. AM - DSB SC - Modulation &Demodulation
C. Diode Detector
D. Pre-emphasis &De-emphasis
E. Frequency Modulation - Modulation &Demodulation
F. AGC Circuits
G. Verification of Sampling Theorem
H. Pulse Amplitude Modulation &Demodulation
I. PWM, PPM – Modulation &Demodulation
J. PLL IC-565 as FM demodulator
K. Radio receiver characteristics
L. Radio Receiver/TV Receiver Demo kits or Trainees.

Note: All the above experiments are to be executed/completed using hardware boards and also to be simulated on Mat lab.

Equipment& Software required:

Software :

i) Computer Systems with latest specifications
ii) Connected in LAN(Optional)
iii) Operating system (Windows/Linux software)
iv) Simulations software (Simulink &MATLAB)

Equipment:

1. RPS - 0 – 30 V
2. CRO - 0 – 20 M Hz.
3. Function Generators - 0 – 1 M Hz
4. Components and Breadboards
5. Multimeters and other meters

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year-II Semester		**L**	**T**	**P**	**C**
		0	**0**	**3**	**1.5**
ELECTRONIC CIRCUIT ANALYSIS LAB					

Note: The students are required to design the circuit and perform the simulation using Multisim/ Equivalent Industrial Standard Licensed simulation software tool**.** Further they are required to verify the result using necessary hardware equipment.

List of Experiments :(Minimum of Ten Experiments has to be performed)

1. Determination of f_T of a given transistor.
2. Voltage-Series Feedback Amplifier
3. Current-Shunt Feedback Amplifier
4. RC Phase Shift/Wien Bridge Oscillator
5. Hartley/ Colpitt's Oscillator
6. Two Stage RC Coupled Amplifier
7. Darlington Pair Amplifier
8. Bootstrapped Emitter Follower
9. Class A Series-fed Power Amplifier
10. Transformer-coupled Class A Power Amplifier
11. Complementary Symmetry Class B Push-Pull Power Amplifier
12. Single Tuned Voltage Amplifier
13. Double Tuned Voltage Amplifier

Equipment required:

Software:

i. Multisim/ Equivalent Industrial Standard Licensed simulation software tool.
ii. Computer Systems with required specifications

Hardware Required:

2. Regulated Power supplies
3. Analog/Digital Storage Oscilloscopes
4. Analog/Digital Function Generators
5. Digital Multimeters
6. Decade Résistance Boxes/Rheostats
7. Decade Capacitance Boxes
8. Ammeters (Analog or Digital)
9. Voltmeters (Analog or Digital)
10. Active & Passive Electronic Components

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

II Year - II Semester		**L**	**T**	**P**	**C**
		1	**0**	**2**	**2**
SCIENTIFIC COMPUTING(SKILL ORIENTED COURSE)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Translate mathematical methods to MATLAB code	K4
CO2	Generalize results and represent data visually	K3
CO3	Apply computer methods for solving a wide range of engineering problems.	K3
CO4	Utilize computer skills to enhance learning and performance in other engineering and science courses	K4
CO5	Acquire knowledge of Advanced MATLAB programming methods and Simulink	K3

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1			**M**										H		H	M
CO2		**M**													M	H
CO3	**L**												H			M
CO4						**H**									H	H
CO5			H										H		L	

UNIT	**CONTENTS**	**Contac Hours**
UNIT – 1	Introduction to MATLAB The Advantages of MATLAB, Disadvantages of MATLAB, MATLAB Environment, Using MATLAB as a Scratch Pad Variables and Arrays, Initializing Variables in MATLAB, Multidimensional Arrays, Sub arrays, Special Values, Displaying Output Data, Data Files, Scalar and Array Operations, Hierarchy of Operations, Introduction to Plotting, Examples, Debugging MATLAB Programs	12
UNIT – 2	Branching Statements and Program Design Use of Pseudo code, The Logical Data Type, Branches, Additional Plotting Features, More on Debugging MATLAB Programs The while Loop, The for Loop, Logical Arrays and Vectorization, The MATLAB Profiler, Additional Examples	12
UNIT – 3	User-Defined Functions Introduction to MATLAB Functions, Variable Passing in MATLAB: The Pass-by-Value Scheme, Optional Arguments, Sharing Data Using Global Memory, Preserving Data Between Calls to a Function, Function Functions, Sub functions, Private Functions, and Nested Functions.	12
UNIT – 4	Graphical User Interfaces How a Graphical User Interface Works, Creating and Displaying a Graphical User Interface, Object Properties, Graphical User Interface Components, Additional Containers: Panels and Button Groups, Dialog Boxes, Menus, Tips for Creating Efficient GUIs	12
UNIT – 5	SIMULINK Introduction, Importance, Model Based Design, Tools, Mathematical Modeling, Converting Mathematical Model into Simulink Model, Running Simulink Models, Importing Exporting Data, Solver Configuration, Masking Block/Model.	12
	Tota	60

TEXT BOOKS:

1. MATLAB® Programming For Engineers, Fourth edition by Stephen J. Chapman
2. MATLAB Programming by Y. Kirani Singh, B.B. Chaudhuri, PHI Publication.

REFERENCE BOOKS:

1. Getting Started WithMatlab: A Quick Introduction For Scientists And Engineers (English) by Rudra Pratap, OXFORD University Press.
2. Applied Numerical Methods Using MATLAB 1st Edition by Won Y. Yang ,Wenwu Cao, Tae-Sang Chung, John Morris

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III YEAR I SEM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year-I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
Digital IC Applications					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Extend the digital operations to any width by connecting the ICs and can also design, simulate their results using hardware description language.	K4
CO2	Analyze the Synthesis process and develop experiments using tools	K4
CO3	Illustrate the process of memory design and understand the concept of memory	K3
CO4	Understand the concepts of different logics and implementations using Integrated Circuits	K4
CO5	Design and analyze any Digital design in real time applications.	K5

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1					**H**			**L**					**H**		**H**	**H**
CO2				**M**						**M**					**M**	**M**
CO3		**L**											**H**			**H**
CO4			**M**				**H**	**M**							**H**	**M**
CO5				**M**												

UNIT	**CONTENTS**	**Contact Hours**
UNIT – 1	Digital Design Using HDL: Design flow, program structure, History of VHDL, VHDL requirements, Levels of Abstraction, Elements of VHDL, Concurrent and Sequential Statements, Packages, Libraries and Bindings, Objects and Classes, Subprograms, Comparison of VHDL and Verilog HDL.	12
UNIT – 2	VHDL Modeling : Simulation, Logic Synthesis, Inside a logic Synthesizer, Constraints, Technology Libraries, VHDL and Logic Synthesis, Functional Gate-Level verification, Place and Route, Post Layout Timing Simulation, Static Timing, Major Netlist formats for design representation, VHDL Synthesis-Programming Approach.	12
UNIT – 3	Combinational Logic Design: Adders & Subtractors, Ripple Adder, Look Ahead Carry Generator, Binary Parallel Adder, Binary Adder-Subtractor, ALU, Decoders, encoders, three state devices, multiplexers and de-multiplexers, Code Converters, parity circuits, comparators, multipliers, Barrel Shifter, Simple Floating-Point Encoder, Cascading Comparators, Dual Priority Encoder, Design considerations with relevant Digital ICs, modeling of Circuits by using VHDL.	12
UNIT – 4	Sequential Logic Design: SSI Latches and Flip-Flops, Counters, Design of Counters using Digital ICs, Ring Counter, Johnson Counter, Asynchronous counters, Modulus N Synchronous Counters, MSI Registers, Shift Registers, Modes of Operation of Shift Registers, Universal Shift Registers, MSI Shift Registers, Design considerations with relevant Digital ICs, modeling of circuits by using VHDL, UART and PIC Controller modeling.	12
UNIT – 5	Digital Logic Families and Interfacing: Introduction to logic families, CMOS logic, CMOS logic families. Bipolar logic, transistor-transistor logic, Emitter coupled logic. Programmable Logic Devices (PLDs) & Memories: Programmable Read Only Memory, Programmable Logic Array, Programmable Array Logic Devices, ROM: Internal structure, Static RAM: Internal structure, SRAM timing, standard, synchronous SRAMS, Dynamic RAM: Internal structure, timing, synchronous DRAMs. Design considerations of PLDs with relevant Digital ICs, Internal architecture of NOR flash and NAND flash. Introduction to FPGA, CPLD Architecture.	12
	Total	60

Text Books:

1. Digital Design Principles & Practices – John F.Wakerly, PHI/ Pearson Education Asia, 3rd Edition, 2005.
2. Designing with TTL Integrated Circuits: Robert L. / John R. Morris & Miller.
3. VHDL Programming by Example-Douglas L.Perry**,** McGraw-Hill,4th Edition

References:

1. "Fundamentals of Digital logic design with VHDL". Stephen Brown &ZvonkoVranesic, Tata McGraw Hill, 2nd edition.
2. VHDL Primer – J. Bhasker, Pearson Education/ PHI, 3rd Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
MICRO PROCESSORS AND MICRO CONTROLLERS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Develop programs for different addressing modes.	K4
CO2	8086 interfacing with different peripherals and implement programs	K3
CO3	Describe the key features of serial and parallel communication	K6
CO4	Design a microcontroller for simple applications	K1
CO5	Illustrate how the different peripherals are interfaced with microprocessor	K4

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1																
CO2							**M**	**L**	**L**		**M**					
CO3			**M**			**H**								**L**		
CO4				**M**	**H**		**M**			**M**	**M**	**H**	**H**		**L**	**L**
CO5		**M**														

UNIT	CONTENTS	**Hours**
UNIT – 1	8086/8088 MICROPROCESSORS: Register organization of 8086, Architecture, signal description of 8086 physical memory organization, general bus operation, I/O addressing capability, special purpose activities Minimum mode, maximum mode of 8086 system and timings, machine language instruction formats addressing mode of 8086, instruction set of 8086,assembler directives and operators.	12
UNIT – 2	PROGRAMMING WITH 8086 MICROPROCESSOR: Machine level programs, programming with an assembler, Assembly language programs, introduction to stack, stack structure of 8086/8088, interrupts and interrupt service routines, interrupt cycle of 8086, non-mask able interrupt and mask able interrupts, interrupt programming.	12
UNIT – 3	BASIC AND SPECIAL PURPOSE PROGRAMMABLE PERIPHERALS AND THEIR INTERFACING WITH 8086. Semiconductor memory interfacing, dynamic RAM interfacing, interfacing i/o ports, PIO 8255 modes of operation of 8255,interfacing to D/A and A/D converters, stepper motor interfacing. Block diagram and functional aspects of 8254 PIT, 8259A, PIC, 8279 keyboard/display controller, 8251 USART, 8257 DMA Controller	12
UNIT – 4	ADVANCED MICRO PROCESSORS: Salient features of 0386DX, architecture and signal description of 80386, register organization of 80386 and addressing modes, data types of 80386, real address mode of 80386, protected mode of 80386, segmentation and Paging, virtual 8086 mode and enhanced mode Instruction set of 80386.The coprocessor 80387.	12
UNIT – 5	8051 MICROCONTROLLER: Introduction to microcontrollers, 8051Microcontrollers, 8051pin description connections, I/O ports and memory organization, MCS51addressing modes and instructions, assembly language programming tools.Introduction to RISC, processor design tradeoffs, Introduction to 16/32 bit processors, ARM architecture and organization, ARM family, Thumb instructions, programming models of ARM 7, Register set, CPSR, SPSR	12
	Total	60

TEXT BOOKS:

1. Douglas V Hall, “Microprocessors and Interfacing Programming and Hardware” , New Delhi Tata McGrawHill Publishing Company Limited
2. A.K.Ray, K.M.Bhurchandi ,”Advanced Microprocessors and Peripherals”, Tata McGraw Hill Publications,2000.
3. Steve Furber, “ARM System on Chip Architecture”, second edition, Pearson publications, 2009.
4. Muhammad Ali Mazidi, Janice GillispieMazidi, RolinD.McKinlay, “The 8051 microcontroller and embedded systems” second edition, Pearson publications.

REFERENCES:

1. Ajay V Deshmukh, ”Microcontrollers”, TATA McGraw Hill publications,2012.
2. Krishna Kant, “Microprocessors and Microcontrollers”, PHI Publications, 2010.
3. N.Sentil Kumar, M.Saravanan, S.Jeevananthan, “Microprocessors and Microcontrollers”, Oxford University Press, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year-I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
ELECTROMAGNETIC WAVES and TRANSMISSION LINES					

Pre-requisite: Understanding of Cartesian co-ordinates, spherical & cylindrical systems
Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Obtain knowledge in different types of transmission lines and calculate characteristics impedance and propagation constant	K1
CO2	Calculate input impedance of a transmission lines, apply Smith chart for analysis of transmission lines	K2
CO3	Determine electric field and capacitance using various Laws	K3
CO4	Calculate magnetic field inductance using various laws and apply the Maxwell equations to analyze the time varying behavior of EM Waves	K4
CO5	Analyze the wave propagation in different media, calculate reflection coefficient, average power	K3

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	L									**H**						
CO2		L											H		H	M
CO3				**M**											M	
CO4				**M**									H			M
CO5								**H**							H	

Mapping of course outcomes with program outcomes

UNIT	CONTENTS	**Hours**
UNIT – 1	**Transmission Lines - I**: Types, Parameters, T&π Equivalent Circuits, Transmission Line Equations, Primary & Secondary Constants, Expressions for Characteristic Impedance, Propagation Constant, Phase and Group Velocities, Infinite Line, Lossless lines, distortion less lines, Illustrative Problems	12
UNIT – 2	**Transmission Lines – II:** Input Impedance Relations, SC and OC Lines, Reflection Coefficient, VSWR. Low loss radio frequency lines and UHF Transmission lines, UHF Lines as Circuit Elements, Impedance Transformations, λ/8,λ/4 and λ /2 Lines –. Smith Chart – Construction and Applications, Quarter wave transformer, Single Stub Matching, Illustrative Problems.	12
UNIT – 3	Review of Co-ordinate Systems, **Electrostatics:**, Coulomb's Law, Electric Field Intensity, Electric Flux Density, Gauss Law and Applications, Electric Potential, Maxwell's Two Equations for Electrostatic Fields, Energy Density, Illustrative Problems. Convection and Conduction Currents, Dielectric Constant, Poisson's and Laplace's Equations; Capacitance – Parallel Plate, Coaxial Capacitors, Illustrative Problems	12
UNIT – 4	**MagnetoStatics:** Biot-Savart Law, Ampere's Circuital Law and Applications, Magnetic Flux Density, Maxwell's Two Equations for Magnetostatic Fields, Magnetic Scalar and Vector Potentials, Forces due to Magnetic Fields, Ampere's Force Law, Inductances and Magnetic Energy. Illustrative Problems **Maxwell's Equations (Time Varying Fields):** Faraday's Law and Transformer EMF, Inconsistency of Ampere's Law and Displacement Current Density, Maxwell's Equations in Different Final Forms and Word Statements. Conditions at a Boundary Surface. Illustrative Problems	12
UNIT – 5	**EM Wave Characteristics :**Wave Equations for Conducting and Perfect Dielectric Media, Uniform Plane Waves – Definition, All Relations Between E & H, Sinusoidal Variations, Wave Propagation in Lossy dielectrics, lossless dielectrics, free space, wave propagation in good conductors, skin depth, Polarization & Types, Illustrative Problems. Reflection and Refraction of Plane Waves – Normal and Oblique Incidences, for both Perfect Conductor and Perfect Dielectrics, Brewster Angle, Critical Angle and Total Internal Reflection, Surface Impedance. Poynting Vector and Poynting Theorem. Illustrative Problems.	12
	Total	60

TEXT BOOKS:

1. Elements of Electromagnetic – Matthew N.O. Sadiku, Oxford Univ. Press, 3rd ed., 2001.
2. Electromagnetic Waves and Radiating Systems – E.C. Jordan and K.G. Balmain, PHI, 2nd Edition, 2000

REFERENCE BOOKS:

1. Electromagnetic Field Theory and Transmission Lines –GSN Raju, Pearson Education 2006
2. Engineering Electromagnetic – William H. Hayt Jr. and John A. Buck, TMH, 7th ed., 2006.
3. Electromagnetic Field Theory and Transmission Lines: G SasiBhushanaRao,Wiley India 2013.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year-I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
CONTROL SYSTEMS (PE1)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	This course introduces the concepts of feedback and its advantages to various control systems	K4
CO2	The performance metrics to design the control system in time-domain and frequency domain are introduced.	K3
CO3	Control systems for various applications can be designed using time-domain and frequency domain analysis	K3
CO4	In addition to the conventional approach, the state space approach for the analysis of control systems is also introduced	K4
CO5	Categorize different types of system and identify a set of algebraic equation to represent and model a complicated system into a more simplified form	K3

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**	**H**	**M**												**L**	
CO2	**M**	**H**	**L**	**M**	**L**										**M**	**H**
CO3	**M**	**M**	**M**	**H**			**M**	**M**	**L**	**L**			**H**		**L**	**L**
CO4	**M**	**M**	**L**	**M**	**L**		**M**		**L**	**M**		**M**	**L**		**M**	**M**
CO5				**M**												

UNIT	CONTENTS	**Hours**
UNIT – 1	**INTRODUCTION** Concepts of System, Control Systems- Open Loop and closed loop control systems and their differences- Different examples of control systems- Feed-Back Characteristics, Effects of feedback. Mathematical models – Differential equations, Impulse Response and transfer functions - Translational and Rotational mechanical systems	12
UNIT – 2	**TRANSFER FUNCTION REPRESENTATION** Transfer Function of DC Servo motor - AC Servo motor- Synchro transmitter and Receiver, Block diagram representation of systems considering electrical systems as examples -Block diagram algebra- Representation by Signal flow graph - Reduction using mason's gain formula. **TIME RESPONSE ANALYSIS** Standard test signals - Time response of first order systems – Characteristic Equation of Feedback control systems, Transient response of second order systems - Time domain specifications – Steady state response - Steady state errors and error constants.	12
UNIT – 3	**STABILITY ANALYSIS IN S-DOMAIN** The concept of stability – Routh-Hurwitz stability criterion – qualitative stability and conditional stability – limitations of Routh's stability **Root Locus Technique:** The root locus concept - construction of root loci-effects of adding poles and zeros to G(s)H(s) on the root loci.	12
UNIT – 4	**Frequency response analysis:** Introduction, Correlation between time and frequency response, Polar Plots Bode Plots, Nyquist Stability Criterion	12
UNIT – 5	**CLASSICAL CONTROL DESIGN TECHNIQUES** Compensation techniques – Lag, Lead, Lead-Lag Controllers design infrequency Domain, PID Controllers State Space Analysis of Continuous Systems Concepts of state, state variables and state model, derivation of state models from block diagrams, Diagonalization- Solving the Time invariant state Equations- State Transition Matrix and it's Properties –Concepts of Controllability and Observability.	12
	Total	60

TEXT BOOKS:

1. Automatic Control Systems 8th edition– by B. C. Kuo–John wiley and son's,2003.
2. Control Systems Engineering – by I. J. Nagrath and M. Gopal,New Age International (P) Limited, Publishers, 2nd edition, 2007
3. Modern Control Engineering–by Katsuhiko Ogata – Pearson Publications, 5th edition, 2015.

REFERENCE BOOKS:

1. Control Systems by A.Nagoorkani, RBA publications,3 edition, 2017.
2. Control Systems by A.Anandkumar, PHI, 2nd Edition, 2014.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
ELECTRONIC MEASUREMENTS AND INSTRUMENTATION(PE1)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Select the instrument to be used based on the requirements.	K1
CO2	Understand and analyze different signal generators and analyzers.	K2
CO3	Understand the design of oscilloscopes for different applications	K6
CO4	Design different transducers for measurement of different parameters.	K6
CO5	Analyse the concept of AC Bridges design for different application	K4

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**					**L**										
CO2								**M**								
CO3													**H**		**M**	**M**
CO4					**L**			**M**							**L**	**H**
CO5				**M**												

UNIT	CONTENTS	**Hours**
UNIT – 1	Performance characteristics of instruments, Static characteristics, Accuracy, Resolution, Precision, Expected value, Error, Sensitivity, Errors in Measurement, Dynamic Characteristics-speed of response, Fidelity, Lag and Dynamic error, DC Voltmeters- Multi-range, Range extension/Solid state and differential voltmeters, AC voltmeters- multi range, range extension, shunt. Thermocouple type RF ammeter, Ohmmeters series type, shunt type.	12
UNIT – 2	Signal Generator- fixed and variable, AF oscillators, Standard and AF sine and square wave signa generators, Function Generators, Square pulse, sweep, Arbitrary waveform. Wave Analyzers, Harmonic Distortion Analyzers, Spectrum Analyzers, Digital Fourier Analyzers.	12
UNIT – 3	Oscilloscopes CRT features, vertical amplifiers, horizontal deflection system, sweep, trigger pulse, delay line, sync selector circuits, simple CRO, triggered sweep CRO, Dual beam CRO, . Dual trace oscilloscope, sampling oscilloscope, storage oscilloscope, digital readout oscilloscope, digital storage oscilloscope, Lissajous method of frequency measurement, standard specifications of CRO, probes for CRO- Active & Passive, attenuator type.	12
UNIT – 4	AC Bridges Measurement of inductance- Maxwell's bridge, Anderson bridge, Measurement o capacitance -Schearing Bridge. Wheat stone bridge, Wien Bridge, Errors and precautions in using bridges Q-meter.	12
UNIT – 5	Transducers- active & passive transducers : Resistance, Capacitance, inductance; Strain gauges, LVDT, Piezo Electric transducers, Resistance Thermometers, Thermocouples, Thermistors. Measurement of physical parameters force, pressure, velocity and calculations.	12
	Tota	60

TEXTBOOKS:

1. Electronic instrumentation, second edition - H.S.Kalsi, Tata McGraw Hill, 2004.

2. Modern Electronic Instrumentation and Measurement Techniques – A.D.Helfrick & W.D.Cooper,PHI,5th Edition, 2002.

REFERENCES:

1. Electronic Instrumentation & Measurements - David A. Bell, PHI, 2nd Edition, 2003.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
INTERNET OF THINGS (PE1)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand internet of Things and its hardware and software components	K2
CO2	Interface I/O devices, sensors &communication modules	K3
CO3	Remotely monitor data and control devices	K4
CO4	Design real time IoT based applications	K1
CO5	Able to realize revolution of Internet in mobile devices, cloud & sensor Networks	K2

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	M						M	H				M	L			L
CO2	L	M		M	L	M			H						H	M
CO3	L	M	H		L										H	H
CO4	M		H	M		M	M			H	L	M	L	M		L
CO5			M													

UNIT	CONTENTS	**Hours**
UNIT – 1	**Introduction to IoT:** Introduction to IoT, Architectural Overview, Design principles and needed capabilities, Basics of Networking, M2M and IoT Technology Fundamentals- Devices and gateways, Data management, Business processes in IoT, Everything as a Service (XaaS), Role of Cloud in IoT, Security aspects in IoT.	12
UNIT – 2	**Elements of IoT:** Hardware Components- Computing- Arduino, Raspberry Pi, ARM Cortex-A class processor, Embedded Devices – ARM Cortex-M class processor, Arm Cortex-M0 Processor Architecture Block Diagram, Cortex-M0 Processor Instruction Set, ARM and Thumb Instruction Set.	12
UNIT – 3	**IoT Application Development:** Communication, IoT Applications, Sensing, Actuation, I/O interfaces Software Components- Programming API's (using Python/Node.js/Arduino) for Communication Protocols-MQTT, ZigBee, CoAP, UDP, TCP, Bluetooth. **Bluetooth Smart Connectivity:** Bluetooth overview, Bluetooth Key Versions, Bluetooth Low Energy (BLE) Protocol, Bluetooth, Low Energy Architecture, PSoC4 BLE architecture and Component Overview.	12
UNIT – 4	**Solution framework for IoT applications:** Implementation of Device integration, Data acquisition and integration, Device data storage- Unstructured data storage on cloud/local server, Authentication authorization of devices	12
UNIT – 5	**IoT Case Studies:** IoT case studies and mini projects based on Industrial automation, Transportation, Agriculture, Healthcare, Home Automation.	12
	Total	60

Text Books:

1. Raj Kamal, "Internet of Things: Architecture and Design Principles", 1st Edition, McGraw Hill Education,2017.
2. The Definitive Guide to the ARM Cortex-M0 by JosephYiu,2011
3. Vijay Madisetti, ArshdeepBahga, Internet of Things, "A Hands on Approach", University Press,2015.

References:

1. Cypress Semiconductor/PSoC4 BLE (Bluetooth Low Energy) Product TrainingModules.
2. Pethuru Raj and Anupama C. Raman, "The Internet of Things: EnablingTechnologies, Platforms, and Use Cases", CRC Press,2017.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		2	**0**	**2**	**3**
PRINCIPLES OF ELECTRONICS (OE-1)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Acquire basic knowledge on the working of various semi-conductor devices	K1
CO2	Develop anaysis capability in BJT and FET Amplifier Circuits	K4
CO3	Develop competence in frequency response analysis of discrete amplifiers	K3
CO4	Develop design competence in signal and power amplifiers using BJT and FET	K6
CO5	Develop knowledge on design trade-offs in various digital electronic families with a view towards reduced power consumption	K1

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**H**														
CO2	**L**												**L**		**H**	**H**
CO3			**M**												**M**	**L**
CO4		**M**											**H**			**M**
CO5						**L**			**M**						**M**	**H**

UNIT	CONTENTS	**Hours**
UNIT – 1	Junction Diode Characteristics : Open circuited p-n junction, Biased p-n junction, p-n junction diode current components in PN junction Diode, diode equation, V-I Characteristics, temperature dependence or V-I characteristics, Diode resistance, Diode capacitance.	12
UNIT – 2	**Special Semiconductor Devices**: Zener Diode, Breakdown mechanisms, Zener diode applications, LED, Varactor Diode, Photodiode, Tunnel Diode, UJT, PNPN Diode, SCR. Construction, operation and V-I characteristics	12
UNIT – 3	**Rectifiers and Filters:** Basic Rectifier setup**,** half wave rectifier, full wave rectifier, bridge rectifier, derivations of characteristics of rectifiers, rectifier circuits-operation, input and output waveforms, Filters, Inductor filter(Series inductor), Capacitor filter(Stunt inductor), π-Filter, comparison of various filter circuits in terms of ripple factors.	12
UNIT – 4	**Transistor Characteristics: BJT:** Junction transistor, transistor current components, transistor equation, transistor configurations, transistor as an amplifier, characteristics of transistor in Common Base, Common Emitter and Common Collector configurations, Ebers-Moll model of a transistor, punch through/ reach through, Photo transistor, typical transistor junction voltage values	12
UNIT – 5	**FET:** FET types, construction, operation, characteristicsμ, g_m, $r_d$parameters, MOSFET-types, construction operation, characteristics, comparison between JFET and MOSFET, CMOS.	12
	Tota	60

Text Books:

1. Integrated Electronics-J. Millman, C. Halkias,TataMc-Graw Hill, Second Edition,2009
2. Electronic Devices and Circuits by David A. Bell, Oxford University Press
3. Electronics devices & circuit theory- Robert L.Boylestad and LouiNashelsky, Pearson/Prentice hall, tenth edition,2009

References:

1. Electronic Devices and Circuits- J. Millman, C. Halkias, Tata Mc-Graw Hill, SecondEdition,2007
2. Electronic Devices and Circuits-K. Lal Kishore, BS Publications, Fourth Edition,2016.
3. Electronic Devices and Circuits-Salivahanan, Kumar, Vallavaraj, Tata Mc-Graw Hill, 4th Edition,2008.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		2	**0**	**2**	**3**
ELECTROMAGNETIC INTERFERENCE & COMPATIBILITY (OE-1)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Discuss effects of EMI and counter measures by EMC-techniques.	K4
CO2	Apply the knowledge gained in selecting proper gadget/device/appliance/system, as per EMC- norms specified by regulating authorities.	K3
CO3	Students shall choose career in the fields of EMI/EMC as an Engineer/Researcher/Entrepreneur in India/abroad.	K6
CO4	Understand the various aspects of shielding & PCB Tracing ,termination & Implementation	K2
CO5	Identifying of EMI Hotspot and various techniques like grounding filtering soldering etc	K5

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**M**														
CO2	**L**												**L**		**M**	**H**
CO3			**L**												**M**	**L**
CO4		**M**											**H**			**M**
CO5						**L**			**M**						**M**	**H**

UNIT	CONTENTS	**Hours**
UNIT – 1	**Natural and Nuclear sources of EMI / EMC**: Introduction, Electromagnetic environment, History, Concepts, Practical experiences and concerns, frequency spectrum conservations. An overview of EMI/ EMC, Natural and Nuclear sources of EMI	12
UNIT – 2	**EMI from apparatus, circuits and open area test sites**: Electromagnetic emissions, noise from relays and switches, non-linearity in circuits, passive inter-modulation, cross talk in transmission lines, transients in power supply lines, electromagnetic interference (EMI). Open area test sites and measurements.	12
UNIT – 3	**Radiated and conducted interference measurements**: Anechoic chamber, TEM cell, GH TEM Cell, characterization of conduction currents / voltages, conducted EM noise on power lines, conducted EMI from equipment, Immunity to conducted EMI detectors and measurements.	12
UNIT – 4	**ESD, Grounding, shielding, bonding and EMI filters**: Principles and types of grounding, shielding and bonding, characterization of filters, power lines filter design. ESD, Electrical fast transients / bursts, electrical surges.	12
UNIT – 5	**Cables, connectors, components**: Introduction, EMI suppression cables, EMC connectors, EMC gaskets, Isolation transformers, optoisolators, Transient and Surge Suppression Devices. **EMC standards- National / International**: Introduction, Standards for EMI and EMC, MIL-Standards IEEE/ANSI standards, CISPR/IEC standards, FCC regulations, EMI/EMC standards in JAPAN, Conclusions.	12
	Tota	60

Text Books:

1. Engineering Electromagnetic Compatibility by **Dr. V.P. Kodali, IEEE Publication**, Printed in India by ***S. Chand & Co. Ltd., New Delhi, 2000***.

References:

1. Introduction to Electromagnetic Compatibility, NY, **John Wiley, 1992, by C.R. Pal.**

2. Electromagnetic Interference and Compatibility **IMPACT series, IIT – Delhi.**

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		2	**0**	**2**	**3**
PRINCIPLES OF COMMUNICATIONS (OE-1)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Analyze and design amplitude modulation systems at the sub-system level. .	K4
CO2	Design angle modulation systems at the sub-system level.	K6
CO3	Classifye and design pulse modulation systems at the sub-system level.	K4
CO4	Apply basic methods of probability and random variables to signal-to-noise ratios	K3
CO5	Design simple analog systems for various modulation techniques.	K6

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**											**M**				
CO2				**M**									**M**			
CO3														**H**	**H**	
CO4		**M**									**L**					**L**
CO5					**H**											

UNIT	CONTENTS	**Hours**
UNIT – 1	AMPLITUDE MODULATION : Introduction and overview of basic communication system, Need for Modulation, Amplitude Modulation, Modulation Index, Spectrum of AM Signal, Power Calculations in AM Systems, Modulators and Demodulators (Diode detector),DSB-SC Signal, SSB Signal, Comparison of AM Techniques.	12
UNIT – 2	ANGLE MODULATION: Angle Modulation, Narrow Band and Wideband FM, Spectrum of an FM Signal. Indirect method of Frequency Modulation (Armstrong Method), FM Demodulation: Balanced Slope Detector, Ratio Detector, Pre – emphasis and De – emphasis, Comparison of FM and AM.	12
UNIT – 3	PULSE ANALOG MODULATION: Time Division Multiplexing, Types of Pulse modulation, PAM (Single polarity, double polarity), Generation & demodulation of PWM, Generation and demodulation of PPM, Comparison of PAM, PWM and PPM systems.	12
UNIT – 4	PULSE DIGITAL MODULATION: Elements of Digital Communication System, Comparison of Digital and Analog Communication Systems. Pulse Code Modulation (PCM): Quantization and Encoding, Differential Pulse Code Modulation, Delta Modulation.	12
UNIT – 5	DIGITAL MODULATION TECHNIQUES: Introduction, Amplitude Shift Keying, Binary Frequency Shift Keying, Binary Phase Shift Keying, Differential PSK (DPSK), Quadrature Phase Shift keying (QPSK), Comparison of Digital Modulation Techniques.	12
	Tota	60

TEXT BOOKS:

1. Simon Haykins, "Communication Systems", 2nd Edition, Reprint, John Wiley and Sons, 2008.
2. H. Taub and D. L. Schilling, "Principles of Communication Systems", Tata McGraw-Hill, 3rd print, 2008.

REFERENCES:

1. R.P. Singh and S. Sapre, "Communication Systems: Analog and Digital", 3rd edition, Tata McGraw-Hill, 2017.
2. Digital Communication, Bernard Sklar, 2nd Edn. Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		0	**0**	**3**	**1.5**
MICROPROCESSORS and MICROCONTROLLERS LAB					

The students are required to develop the necessary Algorithm, Flowchart and Assembly Language Program Source Code for executing the following functions using MASM/TASM software and to verify the results with necessary Hardware Kits.

PART-I: MICROPROCESSOR 8086

1. Introduction to MASM/TASM.
2. Arithmetic operation- Multi byte Addition and Subtraction, Multiplication and Division- Signed and unsigned Arithmetic operation, ASCII- Arithmetic operation.
3. Logic operations-Shift and rotate- Converting packed BCD to unpacked BCD, BCD to ASCII conversion.
4. By using string operation and Instruction prefix: Move Block, Reverse string, Sorting, Inserting, Deleting, Lengthof the string, String comparison.
5. DOS/BIOS programming: Reading keyboard (Buffered with and without echo)- Display characters, Strings.

PART-II: INTERFACING WITH MICROPROCESSOR

1. 8259 – Interrupt Controller-Generate an interrupt using 8259 timer.
2. 8279 – Keyboard Display- Write a program to display a string of characters.
3. 8255 – PPI-Write ALP to generate sinusoidal wave using PPI.
4. 8251 – USART-Write a program in ALP to establish Communication between two processors.

PART-III: MICROCONTROLLER 8051

1. Reading and Writing on a parallel port.
2. Timer in different modes.
3. Serial communication implementation.

PART-IV: INTERFACING WITH MICROCONTROLLER

Write C programs to interface 8051 chip to Interfacing modules to Develop single chip solutions.

1. Simple Calculator using 6 digit seven segment display and Hex Keyboard interface to 8051.
2. Alphanumeric LCD panel and Hex keypad input interface to 8051.
3. External ADC and Temperature control interface to 8051.
4. Generate different waveforms Sine, Square, Triangular, and Rampetc. using DAC interface to 8051; changethe frequency and Amplitude.

EQUIPMENT REQUIRED FOR LABORATORY

1. MASM/TASM software
2. 8086 Microprocessor Kits
3. 8051 Micro Controller kits
4. Interfaces/peripheral subsystems
   i) 8259 PIC
   ii) 8279-KB/Display
   iii) 8255 PPI
   iv) 8251 USART
5. A/D and D/AC Interface

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		**L**	**T**	**P**	**C**
		0	**0**	**3**	**1.5**
DIGITAL IC APPLICATIONS LAB					

The students are required to design and draw the internal structure of the following Digital Integrated Circuits and to develop VHDL source code, perform simulation using relevant simulator and analyze the obtained simulation results using necessary synthesizer. Further, it is required to verify the logic with necessary hardware.

List of Experiments:

1. Realization of Logic Gates
2. 3 to 8 Decoder- 74138
3. 8*1 Multiplexer-74151 and 2*1 De-multiplexer-74155
4. 4-Bit Comparator-7485.
5. D Flip-Flop- 7474
6. Decade Counter- 7490
7. 4 Bit Counter-7493
8. Shift Register-7495
9. Universal shift register-74194/195
10. Ram (16*4)-74189 (read and write operations)
11. ALU

Equipment Required:

1.Xilinix ISE software-latest version
2. Personal computer with necessary pheripherals
3.Hardware kits- Various FPGA families.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - I Semester		L	T	P	C
		1	0	2	2

SCILAB (SKILL ADVANCED COURSES/SOFT SKILL COURSES)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand the need for simulation/implementation for the verification of mathematical functions	K2
CO2	Understand the main features of the SCILAB program development environment to enable their usage in the higher learning.	K2
CO3	Implement simple mathematical functions/equations in numerical computing environment such as SCILAB	K5
CO4	Interpret and visualize simple mathematical functions and operations thereon using plots/display	K3
CO5	Analyze the program for correctness and determine/estimate/predict the output and verify it under simulation environment using SCILAB tools	K4

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**							**M**					**L**		**M**	**M**
CO2															**H**	**H**
CO3			**M**				**H**						**M**	**H**		**M**
CO4					**H**										**H**	**H**
CO5		**M**												**M**		

UNIT	CONTENTS	Hours
UNIT – 1	**MATRICES AND ARRAYS IN SCILAB** About SCILAB, SCILAB System, How to start SCILAB, Entering Matrices sum and transpose, subscripts, Colon Operator, magic Function, Variables and constants: Definition, naming (identifiers or labels for different entities, initialization and accessing of variables. Constants and their representation.	12
UNIT – 2	**WORKING WITH MATRICES** Generating Matrices, The load Function, Concatenation, Deleting Rows and Columns, Linear Algebra, Arrays Multivariate Data, Scalar Expansion, Logical Subscripting, find Function. Variables Numbers, Operators Functions, Expressions.	12
UNIT – 3	**GRAPHICS & COMMAND WINDOW** The format Function, Suppressing Output, Entering Long Statements, Command Line Editing. Plotting Process, Editing Process, Preparing Graphs, Basic Plotting Functions, Mesh & Surface Plot, and Image Reading &Writing, Printing graphics, Simple programs.	12
UNIT – 4	**DATA STRUCTURE & FLOW CONTROL** If, else and elseif , switch and case, for, while, continue, break, try-catch, return. Multidimensional Arrays, Cell Arrays, Characters and Text, Structures, Simple programs.	12
UNIT – 5	**SCRIPTS & FUNCTIONS** Scripts, Functions, Global Variables, Passing String, Arguments to Functions, eval Function, Function Handles, Vectorization, Preallocation, Simple programs.	12
	Total	60

TEXT BOOKS:

1. Introduction to SCILAB by Rachna Verma and Arvind Verma

REFERENCE BOOKS:

1. SCILAB - A Beginner‟s Approach by Anil Kumar Verma

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III YEAR II SEM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
VLSI DESIGN					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Apply the Concept of design rules during the layout of a circuit.	K3
CO2	Model and simulate digital VLSI systems using hardware design language.	K1
CO3	Synthesize digital VLSI systems from register-transfer or higher level descriptions	K5
CO4	Understand current trends in semiconductor technology, and how it impacts scaling and performance.	K2
C05	Differentiate various FPGA CPLD Architectures	K4

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**M**			**M**								**M**	**H**		**M**	**M**
CO2										**M**				**L**	**H**	**H**
CO3		**L**				**L**							**M**			**M**
CO4									H						**L**	**H**
CO5						M										

UNIT	CONTENTS	**Hours**
UNIT – 1	**Introduction :**Introduction to IC Technology, MOS and related VLSI Technology, Basic MOS Transistors Enhancement and Depletion modes of transistor action, IC production process, MOS and CMOS Fabrication processes, Bi-CMOS Technology, Comparison between CMOS and Bipolar technologies. **Basic Electrical Properties Of MOS and Bi-CMOS Circuits:** I_{ds} versus V_{ds} Relationships, Aspects of MOS transistor Threshold Voltage, MOS transistor Trans, Output Conductance and Figure of Merit. The Pass transistor, NMOS Inverter, Pull-up to Pull-down Ratio for NMOS inverter driven by another NMOS inverter. Alternative forms of pull-up, The CMOS Inverter, MOS transistor circuit model, Bi-CMOS Inverter, Latch-up in CMOS circuits and BiCMOS Latch-up Susceptibility.	12
UNIT – 2	**MOS and Bi-CMOS Circuit Design Processes:** MOS Layers, Stick Diagrams, Design Rules and Layout, General observations on the Design rules, 2µm Double Metal, Double Poly, CMOS/BiCMOS rules, 1.2µm Double Metal, Double Poly CMOS rules, Layout Diagrams of NAND and NOR gates and CMOS inverter, Symbolic Diagrams-Translation to Mask Form.	12
UNIT – 3	**Basic Circuit Concepts:** Sheet Resistance, Sheet Resistance concept applied to MOS transistors and Inverters, Area Capacitance of Layers, Standard unit of capacitance, The Delay Unit, Inverter Delays, Propagation Delays, Wiring Capacitances, Fan-in and fan-out characteristics, Transistor switches, Realization of gates using NMOS, PMOS and CMOS technologies. **Scaling Of MOS Circuits:** Scaling models, Scaling factors for device parameters, Limits due to sub threshold currents, current density limits on logic levels and supply voltage due to noise.	12
UNIT – 4	**Subsystem Design:** Architectural issues, switch logic, Gate logic, examples of structured design, clocked sequential circuits.	12
UNIT – 5	**VLSI Design Issues:** VLSI Design issues and design trends, design process, design for testability, technology options, power calculations, package selection, clock mechanisms, Introduction to mixed signal design, ASIC design flow, FPGA design flow, introduction to SoC design. Basic CPLD architecture, typical CPLD design flow **FPGA Design:** Basic FPGA architecture, , FPGA configuration, configuration modes, FPGA design process-FPGA design flow, FPGA families.	12
	Total	60

Text Books:

1. Essentials of VLSI Circuits and Systems By Kamran Eshraghian, Douglas and A. Pucknell and Sholeh Eshraghian, Prentice-Hall of India Private Limited,2005 Edition.

References:

1. VLSI Design By A.Albert Raj &T.Latha,PHI Learning Private Limited,2010.
2.VLSI Design-A.Shanthi and A.Kavita, New Age International Private Limited, 2006 First Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
DIGITAL SIGNAL PROCESSING					

Pre-requisite: Signals & Systems
Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Discuss Signals and Systems in Discrete Domain; z-Transforms and its applications to the analysis of LTI systems	K2
CO2	Explain the analysis of signals in frequency domain and calculation of DFT using FFT Algorithms	K2
CO3	Identify the FIR and IIR structures for the required digital filter and study of various filter structures	K1, K2
CO4	Analyze and Design a Digital filter (FIR&IIR) from the given specifications.	K4,K5
CO5	Describe the Architecture of DSP Processor	K1

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**H**												**H**			**M**
CO2	**H**												**M**			**H**
CO3	**M**		**M**	**H**												**H**
CO4	**M**	**M**	**H**												**M**	**H**
CO5	**M**			**H**									**M**			**H**

UNIT	CONTENTS	**Hours**
Unit - 1	**Introduction:** Signals, Systems, and Signal Processing, Classification of Signals, The Concept of Frequency in Continuous Time and Discrete Time Signals **Discrete Time Signals and Systems**: Discrete Time Signals, Discrete Time Systems, Analysis of Discrete Time Linear Time Invariant Systems, Discrete Time Systems Described by Difference Equations, Implementation of Discrete Time Systems, Correlation of Discrete Time Signals **The z-Transform and Its Applications to the Analysis of LTI Systems**: The z-Transform, Properties, Rational z Transforms, Inversion of the z-Transform, Analysis of Linear Time Invariant Systems in the z-Domain, The One sided z-Transform.	9
Unit-2	**Frequency Analysis of Signals**: Frequency Analysis of Continuous Time Signals, Frequency Analysis of Discrete Time Signals, Frequency Domain and Time Domain Signal Properties, Properties of the Fourier Transform for Discrete Time Signals. **The Discrete Fourier Transform: Its Properties and Applications**: Frequency Domain Sampling: The Discrete Fourier Transform, Properties of the DFT, Linear Filtering Methods Based on the DFT, Frequency Analysis of Signals Using DFT, The Discrete Cosine Transform. **Efficient Computation of the DFT: Fast Fourier Transform Algorithms**: Direct Computation of the DFT, Radix-2 FFT Algorithms.	9
Unit-3	**Implementation of Discrete Time Systems**: Structures for the Realization of Discrete Time Systems, **Structures for FIR Systems**: Direct Form Structure, Cascade Form Structures, Frequency Sampling Structures **Structures for IIR Systems**: Discrete Form Structures Signal Flow Graphs and Transposed Structures, Cascade Form Structures, Parallel Form Structures.	9
Unit-4	**Design of Digital Filters**: General Considerations: Causality and Its Implications, Characteristics of Practical Frequency Selective Filters. **Design of FIR Filters**: Symmetric and Anti symmetric FIR Filters, Design of Linear Phase FIR Filters Using Windows, Design of Linear Phase FIR Filters by the Frequency Sampling Method. **Design of IIR Filters From Analog Filters**: IIR Filter Design by Approximation of Derivatives, IIR Filter Design by Impulse Invariance, IIR Filter Design by the Bilinear Transformation, Characteristics of Commonly Used Analog Filters. **Frequency Transformations**: Frequency Transformations in the Analog Domain, Frequency Transformations in the Digital Domain.	9
Unit-5	**Introduction to programmable DSPs**: Multiplier and Multiplier Accumulator, Modified bus structures and memory access schemes in P-DSPs ,Multiple Access Memory, Multiported memory, VLIW architecture, Pipelining, Special addressing modes, On-Chip Peripherals. **Architecture of TMS320C5X:** Introduction, Bus Structure, Central Arithmetic Logic Unit, Auxiliary Register ALU, Index Register, Auxiliary Register Compare Register, Block Move Address Register, Block Repeat Registers, Parallel Logic Unit, Memory mapped registers, program controller, some flags in the status registers, On- chip memory, On-chip peripherals.TMS320C5X Assembly Language Instructions.	9
	Total	45

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G.Manolakis, 4th Edition, Pearson Education, 2007.
2. Digital Signal Processors – Architecture, Programming and Applications,,B.Venkataramani, M.Bhaskar, TATA McGraw Hill, 2002.

Reference Books:

1. Discrete Time Signal Processing – A.V.Oppenheim and R.W. Schaffer, 3rd Edition, Pearson, 2014.
2. Digital Signal Processing-A. Nagoor Kani, 2nd Edition, McGrawHill Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
DIGITAL COMMUNICATIONS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Determine the performance of different waveform coding techniques for the generation and digital representation of the signals	K3
CO2	Determine the probability of error for various digital modulation schemes	K4
CO3	Analyse different source coding techniques	K3
CO4	Compute and analyse different error control coding schemes for the reliable transmission of digital information over the channel	K3
CO5	Analyze the performance of a Base Band ,Pass Band digital communication Systems in terms of error rate and spectral efficiency	K2

Mapping of course outcomes with program outcomes

	PO1	**PO3**	**PO4**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**M**							**H**		**L**				
CO2		**M**									L		M	L
CO3				**L**									H	M
CO4							**H**				H			M
CO5				M										

UNIT	CONTENTS	**Hours**
UNIT – 1	**PULSE DIGITAL MODULATION:** Elements of digital communication systems, advantages of digital communication systems, Elements of PCM: Sampling, Quantization & Coding, Quantization error, Companding in PCM systems. Differential PCM systems(DPCM). Delta modulation, its draw backs, adaptive delta modulation, comparison of PCM and DM systems, noise in PCM and DM systems, Time division multiplexing, Frequency division multiplexing	12
UNIT – 2	**DIGITAL MODULATION TECHNIQUES:** Introduction, ASK, FSK, PSK, DPSK, DEPSK, QPSK, M-ary PSK ASK, FSK, similarity of BFSK and BPSK. **DATA TRANSMISSION :**Base band signal receiver, probability of error, the optimum filter, matched filter, probability of error using matched filter, coherent reception, non-coherent detection of FSK, calculation of error probability of ASK, BPSK, BFSK,QPSK	12
UNIT – 3	**INFORMATION THEORY:** Discrete messages, concept of amount of information and its properties. Average information, Entropy and its properties. Information rate, Mutual information and its properties	12
UNIT – 4	**SOURCE CODING:** Introductions, Advantages, Shannon's theorem, Shanon-Fano coding, Huffman coding, efficiency calculations, channel capacity of discrete and analog Channels, capacity of a Gaussian channel, bandwidth –S/N trade off.	12
UNIT – 5	**LINEAR BLOCK CODES:** Introduction, Matrix description of Linear Block codes, Error detection and error correction capabilities of Linear block codes, Hamming codes, Binary cyclic codes, Algebraic structure, encoding, syndrome calculation, BCH Codes. **CONVOLUTIONAL CODES:** Introduction, encoding of convolution codes, time domain approach, transform domain approach. Graphical approach: state, tree and trellis diagram decoding using Viterbi algorithm.	12
	Tota	60

TEXT BOOKS:

1. Digital communications - Simon Haykin, John Wiley, 2005
2. Principles of Communication Systems – H. Taub and D. Schilling, TMH, 2003
3. Digital Communications- J.Das, S.K.Mullick, P.K.Chatterjee, John willy& sons, 1986.

REFERENCES:

1. Digital and Analog Communication Systems - Sam Shanmugam, John Wiley, 2005.
2. Digital Communications – John Proakis, TMH, 1983. Communication Systems Analog & Digital – Singh &Sapre, TMH, 2004.
3. Modern Analog and Digital Communication – B.P.Lathi, Oxford reprint, 3rd edition, 2004.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
ANTENNA and WAVE PROPAGATION (PE2)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)
CO1	Identify basic antenna parameters	K2
CO2	Design and analyze wire antennas, loop antennas, reflector antennas, lens antennas, horn antennas and micro-strip antennas	K6
CO3	Design and analyze antenna arrays, analyze antennas with parasitic elements	K3
CO4	To analyze different types of non-resonant radiators and patch antennas	K3
CO5	To gain knowledge in VHF,UHF and microwave antennas, know the various antenna parameters measurements and understand the wave propagation	K1

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**M**				**L**								**L**		**H**	**M**
CO2			**H**												**H**	**L**
CO3	**M**												**M**			**H**
CO4			**H**												**L**	**M**
CO5					**M**											

UNIT	CONTENTS	**Hours**
UNIT – 1	**ANTENNA FUNDAMENTALS:** Introduction, Radiation Mechanism – Single Wire, 2-Wire, dipoles, Current Distribution on a thin wire antenna. Antenna Parameters - Radiation Patterns, Patterns in Principal Planes, Field Regions, Main Lobe and Side Lobes, Beam-width, Radiation Intensity, Directivity, Antenna Efficiency, Gain, Beam Efficiency, Bandwidth, Polarization, Input Impedance, Beam Area and Resolution, Antenna Apertures, Aperture Efficiency, Effective Height, illustrated Problems	12
UNIT – 2	**THIN LINEAR WIRE ANTENNAS:** Retarded Potentials, Radiation from Small Electric Dipole, Quarter wave Monopole and Half wave Dipole – Current Distributions, Evaluation of Field Components, Power Radiated, Radiation Resistance, Beam-widths, Directivity, Effective Area and Effective Height. Natural current distributions, fields and patterns of Thin Linear Center-fed Antennas of different lengths, Radiation Resistance at a point which is not current maximum. Antenna Theorems – Applicability and Proofs for equivalence of directional characteristics, Loop Antennas: Small Loops - Field Components, Comparison of far fields of small loop and short dipole, Concept of short magnetic dipole, D and R_r relations for small loops.	12
UNIT – 3	**ANTENNA ARRAYS :** 2 element arrays – different cases, Principle of Pattern Multiplication, N element Uniform Linear Arrays – Broadside, End-fire Arrays, EFA with Increased Directivity, Derivation of their characteristics and comparison; Concept of Scanning Arrays. Directivity Relations (no derivations). Related Problems. Binomial Arrays, Effects of Uniform and Non-uniform Amplitude Distributions, Design Relations.Arrays with Parasitic Elements, Yagi-Uda Arrays, Folded Dipoles and their characteristics.	12
UNIT – 4	**NON-RESONANT RADIATORS :** Introduction, Traveling wave radiators – basic concepts, Long wire antennas – field strength calculations and patterns, Microstrip Antennas-Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas – Geometry and Parameters, Impact of different parameters on characteristics. Broadband Antennas: Log periodic antenna, Basic principle, Helical Antennas – Significance, Geometry, basic properties; Design considerations for monofilar helical antennas in Axial Mode and Normal Modes (Qualitative Treatment).	12
UNIT – 5	**VHF, UHF AND MICROWAVE ANTENNAS:** Reflector Antennas: Flat Sheet and Corner Reflectors. Paraboloidal Reflectors - Geometry, characteristics, types of feeds, F/D Ratio, Spill Over, Back Lobes, Aperture Blocking, Off-set Feeds, Case grain Feeds Horn Antennas – Types, Optimum Horns, Design Characteristics of Pyramidal Horns; FRIIS Transmission Equation, Antenna Measurements , Patterns Required, Set Up, Distance Criterion, Directivity and Gain Measurements (Comparison, Absolute and 3-Antenna Methods). **WAVE PROPAGATION:** types of propagations.Sky Wave Propagation – Formation of Ionospheric Layers and their Characteristics, Mechanism of Reflection and Refraction, Critical Frequency, MUF and Skip Distance Space Wave Propagation – Mechanism, LOS and Radio Horizon.	12
	Tota	60

TEXT BOOKS : 1. Antennas for All Applications – John D. Kraus and Ronald J. Marhefka, 3rd Edition, TMH, 2003.
2.Electromagnetic Waves and Radiating Systems – E.C. Jordan and K.G. Balmain, PHI, 2nd Edition, 2000.
REFERENCES : 1. Antenna Theory - C.A. Balanis, John Wiley and Sons, 2nd Edition, 2001.
2.. Transmission and Propagation – E.V.D. Glazier and H.R.L. Lamont, The Services Text Book of Radio, vol. 5, Standard Publishers Distributors, Delhi.3.Antennas – John D. Kraus, McGraw-Hill, 2nd Edition, 1988.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year-II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**

COMPUTER ARCHITECTURE and ORGANIZATION (PE2)

Pre-requisite: Digital Logic Design

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Explain the representation of data, the register transfer language and Micro operations	K1, K2
CO2	Describe Basic computer organization and design ; programming the basic computer and design the micro programmer control unit	K1, K2,K4
CO3	Devise the design central processing unit and explain various algorithms for computer arithmetic operations	K4, K2
CO4	Discuss various Peripheral devices and various data transfer skills	K1, K2
CO5	Discuss memory Hierarchy and different types of memories	K1, K2

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**	**H**											**L**		**M**	
CO2	**M**	**H**											**M**	**L**	**M**	
CO3		**M**	**M**										**M**	**M**	**M**	**L**
CO4	**L**	**M**	**H**	**M**									**H**		**H**	**M**
CO5	**M**	**L**	**L**	**H**									**M**		**H**	**H**

Unit	Contents	Hours
Unit I	**Chapter-1 :** **Introduction :** Digital Computers, Why study computer organization and Architecture?, A few basic issues, Von Neumann computers, Basic organization of a computer **Data Representation:** Data types, Complements, Fixed-point representation, Conversion of fractions, Floating-point representation **Register Transfer and Microoperations :** Register transfer language, Register transfer, Bus and Memory transfers, Arithmetic Microoperations, Logic Microoperations, Shift Microoperations, Arithmetic Logic Shift Unit	9 hours
Unit II	**Chapter-2** **Basic Computer Organization and Design:** Instruction Codes, Computer Registers, Computer Instructions, Timing and Control, Instruction Cycle, Memory-Reference instructions, Input-Output and Interrupt, Complete Computer Description, Design of Basic computer **Programming the Basic Computer:** Introduction, Machine Language, Assembly language, The Assembler, Program Loops, Programming Arithmetic and Logic Operations **Microprogrammed Control:** Control Memory, Address Sequencing, Microprogram Example, Design of Control Unit	9 hours
Unit III	**Chapter-3** **Central Processing Unit:** Introduction, General Register Organization, Stack organization, Instruction Formats, Addressing Modes, Data transfer and Manipulation, Program Control, Reduced Instruction Set Computer **Computer Arithmetic**: Introduction, Addition and Subtraction, Multiplication Algorithms, Division Algorithms , Floating-Point Arithmetic Operations, Decimal Arithmetic Unit, Decimal Arithmetic Operations.	9 hours
Unit IV	**Chapter – 4** **Input-Output organization :** Peripheral Devices, Input-Output Interface, Asynchronous Data Transfer, Modes of Transfer, Priority Interrupt, Direct Memory Access (DMA), Input-Output Processor (IOP), Serial Communication.	9 hours
Unit V	**Chapter – 5** **Memory Organization:** Memory Hierarchy, Main Memory, Auxiliary Memory, Associative Memory, Cache Memory, Virtual Memory, Memory Management Hardware.	9 hours
	Total	45 hrs

Text Book

1. M. Morris Mano," **Computer System Architecture**," Pearson Publishers, Revised Third Edition

Reference Books

1. John P Hayes, "**Computer Architecture and Organization,"**Mc-Graw Hill Publishers, Third Edition
2. Carl Hamacher, "**Computer Organization**," Tata Mc-Graw Hill Publishers, Fifth Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year-II Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
SOFT COMPUTING TECHNIQUES (PE2)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Students can understand the architecture of modern computer.	K3
CO2	They can analyze the Performance of a computer using performance equation	K6
CO3	Understanding of different instruction types	K4
CO4	Students can calculate the effective address of an operand by addressing modes	K5
CO5	They can understand how computer stores positive and negative numbers	K2

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**M**											**H**		**H**	**H**
CO2														**L**	**M**	**L**
CO3	**L**												**H**			**M**
CO4			**H**		**H**									**M**	**H**	**L**
CO5				**M**										**M**		

UNIT	CONTENTS	**Hours**
UNIT – 1	**Introduction to soft computing:** Introduction, Artificial Intelligence, Artificial Neural Networks, Fuzzy systems, Genetic Algorithm and Evolutionary programming, Swarm Intelligent systems, Expert systems, Comparison among Intelligent systems.	12
UNIT – 2	**Artificial Neural Networks:** Introduction to Artificial Neural Networks, Classification of ANNS, First generation neural networks, Perceptron network, Adaline, Madaline, Second generation neural networks, Back propagation neural networks, Hopfield Neural Network, Kohonen neural network, Hamming neural network, Radial basis function neural networks, spike neuron models.	12
UNIT – 3	**Fuzzy Logic System:** Introduction to fuzzy logic, classical sets and fuzzy sets, fuzzy set operations, fuzzy relations, fuzzy composition, natural language and fuzzy interpretations, fuzzy inference system, fuzzy controllers	12
UNIT – 4	**Genetic Algorithm:** Introduction to Genetic algorithms, Genetic algorithms, procedures of Gas, working of Gas, Travelling sales man problem, Evolutionary programming, working principle of GA Machine learning classifier system	12
UNIT – 5	**Swarm Intelligent system** Introduction to swarm intelligence, back ground, Ant colony system, working of ant colony optimization, Particle swarm intelligent systems, Artificial bee colony system, cuckoo search algorithm..	12
	Tota	60

TEXT BOOKS:

1.Soft computing with MATLAB programming—N.P.Padhy, S.P.Simon, Oxford university press,2015
2. Neural Networks and Fuzzy Systems - Kosko, B., Prentice-Hall of India Pvt. Ltd., 1994.
3. Introduction to Artificial Neural Systems-Jacek.M.Zurada, Jaico Publishing House,1999

REFERENCE BOOKS:

1. Fuzzy Sets, Uncertainty and Information - Klir G.J. &Folger T.A., Prentice-Hall of India Pvt. Ltd., 1993.
2. Fuzzy Set Theory and Its Applications - Zimmerman H.J. Kluwer Academic Publishers,1994.
3. Introduction to Fuzzy Control - Driankov, Hellendroon, Narosa Publishers.
4. Artificial Neural Networks - Dr. B. Yagananarayana, 1999, PHI, New Delhi.
5. Elements of Artificial Neural Networks - KishanMehrotra, Chelkuri K. Mohan,Sanjay Ranka, Penram International.
6. Artificial Neural Network –Simon Haykin, 2nd Ed., Pearson Education.
7. Introduction Neural Networks Using MATLAB 6.0 - S.N. Shivanandam, S. Sumati, S. N.Deepa,1/e, TMH, New Delhi.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year-II Semester		**L**	**T**	**P**	**C**
		2	**0**	**2**	**3**
Biomedical Instrumentation (OE2)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Students will have a clear knowledge about human physiology system	K2
CO2	They will have knowledge of the principle of operation and the background knowledge of biomedical instruments and specific applications of biomedical engineering	K5
CO3	Provide students with an understanding of the basic physiology associated with the generation of various bioelectric signals like ECG,EEG etc.	K2
CO4	Provide students With the ideas of application of the principles of engineering , mathematics and physics to medicine and biology by which man kind is benefited	K4
CO5	Discuss the application of Electronics in Diagnosis Diagnostics and therapeutic area	K1

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**									**L**			**M**		**H**	**M**
CO2									**M**				**L**	**M**	**M**	**H**
CO3	**M**						**H**						**M**			**M**
CO4					**H**								**H**	**L**	**H**	**H**
CO5				**M**												

UNIT	CONTENTS	**Hours**
UNIT – 1	Sources of Bioelectric potentials and Electrodes: Resisting and Action Potentials, Propagation of Action Potentials, The Bioelectric Potentials. Electrodes: Electrode theory, Bio Potential Electrodes, Biochemical Transducers, introduction to bio-medical signals	12
UNIT – 2	The Cardiovascular System: The Heart and Cardiovascular System, The Heart, Blood Pressure, Characteristics of Blood Flow, Heart Sounds, Cardio Vascular Measurements, Electrocardiography, Measurement of Blood Pressure, Measurement of Blood Flow and Cardiac output, Plethysmography, Measurement of Heart Sounds, Event detection, PQRS & T-Waves in ECG, the first & second Heart beats, ECG rhythm analysis, the di-crotic notch in the carotid pulse detection of events and waves, analysis of exercise ECG, analysis of event related potentials, correlation analysis of EEG channels, correlation of muscular contraction	12
UNIT – 3	Patient Care & Monitory and Measurements in Respiratory System: The elements of Intensive Care Monitory, Diagnosis, Calibration and reparability of Patient Monitoring equipment, other instrumentation for monitoring patients, pace makers, defibrillators, the physiology of respiratory system, tests and instrumentation for mechanics of breathing ,respiratory theory equipment, analysis of respiration	12
UNIT – 4	Bio telemetry and Instrumentation for the clinical laboratory Introduction to bio telemetry, Physiological parameters adaptable to bio telemetry, the components of bio telemetry system, implantable units, applications of telemetry in patient care – The blood, tests on blood cells, chemical test, automation of chemical tests.	12
UNIT – 5	X-ray and radioisotope instrumentation and electrical safety of medical equipment: Generation of Ionizing radiation, instrumentation for diagnostic X-rays, special techniques, instrumentation for the medical use of radioisotopes, radiation therapy - Physiological effects of electrical current, shock Hazards from electrical Equipment, Methods of accident prevention, Modern Imaging Systems: Tomography, Magnetic resonance ,Imaging System, Ultrasonic Imaging System, Medical Thermograph	12
	Tota	60

TEXT BOOK:

1. Biomedical Instrumentation and Measurements – C. Cromwell, F.J. Weibell, E.A.Pfeiffer – PHI.
2. Biomedical Instruments Theory and Design-Welkowitz, Elseiver

Reference:

1. Biomedical instrumentation systems- Shakthi Chattarjee, Aubert Miller Cenage Learning
2. Hand Book of Bio-Medical Instrumentation – R.S. Khandpur, (TMH)

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		**L**	**T**	**P**	**C**
		2	**0**	**2**	**3**
ELECTRONIC MEASUREMENTS AND INSTRUMENTATION(OE2)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Select the instrument to be used based on the requirements.	K3
CO2	Understand and analyze different signal generators and analyzers.	K2
CO3	Understand the design of oscilloscopes for different applications	K2
CO4	Design different transducers for measurement of different parameters.	K6
CO5	Analyse the concept of AC Bridges design for different application	K4

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**L**				**M**							**H**		**H**	**M**
CO2					**L**								**M**		**M**	
CO3	M												**M**			**M**
CO4			**M**		**M**								**M**	**L**		**H**
CO5				M												

UNIT	CONTENTS	**Hours**
UNIT – 1	Performance characteristics of instruments, Static characteristics, Accuracy, Resolution, Precision, Expected value, Error, Sensitivity, Errors in Measurement, Dynamic Characteristics-speed of response, Fidelity, Lag and Dynamic error, DC Voltmeters- Multi-range, Range extension/Solid state and differential voltmeters, AC voltmeters- multi range, range extension, shunt. Thermocouple type RF ammeter, Ohmmeters series type, shunt type.	12
UNIT – 2	Signal Generator- fixed and variable, AF oscillators, Standard and AF sine and square wave signa generators, Function Generators, Square pulse, sweep, Arbitrary waveform. Wave Analyzers, Harmonic Distortion Analyzers, Spectrum Analyzers, Digital Fourier Analyzers.	12
UNIT – 3	Oscilloscopes CRT features, vertical amplifiers, horizontal deflection system, sweep, trigger pulse, delay line, sync selector circuits, simple CRO, triggered sweep CRO, Dual beam CRO, . Dual trace oscilloscope, sampling oscilloscope, storage oscilloscope, digital readout oscilloscope, digital storage oscilloscope, Lissajous method of frequency measurement, standard specifications of CRO, probes for CRO- Active & Passive, attenuator type.	12
UNIT – 4	AC Bridges Measurement of inductance- Maxwell's bridge, Anderson bridge, Measurement o capacitance -Schearing Bridge. Wheat stone bridge, Wien Bridge, Errors and precautions in using bridges, Q-meter.	12
UNIT – 5	Transducers- active & passive transducers : Resistance, Capacitance, inductance; Strain gauges, LVDT Piezo Electric transducers, Resistance Thermometers, Thermocouples, Thermistors. Measurement of physical parameters force, pressure, velocity and acceleration.	12
	Tota	60

TEXTBOOKS:

1. Electronic instrumentation, second edition - H.S.Kalsi, Tata McGraw Hill, 2004.
2. Modern Electronic Instrumentation and Measurement Techniques – A.D. Helfrick and W.D. Cooper, PHI, 5th Edition, 2002.

REFERENCES:

1. Electronic Instrumentation & Measurements - David A. Bell, PHI, 2nd Edition, 2003.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		L	T	P	C
		2	0	2	3

DISPLAY DEVICES (OE-2)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Able to understand the transmission of video signal and importance of television standards to effectively work with broadcasting application	K2
CO2	Able to acquire sound knowledge of latest topics in digital video transmission	K2
CO3	Able to analyze various colour television system with a greater emphasis on television standards	K1
CO4	Able to understand advanced topics in digital television and high definition television	K4
CO5	Analyze & Evaluate the NTSC &PAL colour systems	K4

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	L		H		L	L	H	M	L	L			H			
CO2		M					H	M	M	M	H			H		H
CO3			M	M		L	M	H	H	H	M	M			M	H
CO4			L				L			M	L	L				
CO5				M												

UNIT	CONTENTS	Hours
UNIT – 1	**INTRODUCTION:** TV transmitter and receivers, synchronization. Television Pictures: Geometric form and aspect ratio, image continuity, interlaced scanning, picture resolution, Composite video signal: Horizontal and vertical sync, scanning sequence, Colour signal generation and Encoding: Perception of brightness and colours, additive colour mixing, video signals for colours, luminance signal, colour difference signals, encoding of colour difference signals, formation of chrominance signals, PAL encoder.	12
UNIT – 2	**TV SIGNAL TRANSMISSION AND PROPAGATION:** Picture signal transmission, positive and negative modulation, VSB transmission, sound signal transmission, standard channel BW, TV transmitter, TV signal propagation, interference, TV broadcast channels. **MONOCHROME TV RECEIVER:** RF tuner, IF subsystem, video amplifier, sound section, sync separation and processing, deflection circuits, scanning circuits. PAL–D colour receiver: Electron tuners, IF subsystem, Y-signal channel, chroma decoder, separation of U & V Colour phasors, synchronous demodulators, subcarrier generation, raster circuits.	12
UNIT – 3	**VISION IF SUBSYSTEM:** AGC, noise cancellation, video and intercarrier sound signal detection, Colour receiver IF subsystem, Receiver sound system: FM detection, FM Sound detectors, typical applications.TV Receiver Tuners: Tuner operation, VHF and UHF tuners. **COLOUR SIGNAL DECODING:**PAL-D decoder, chroma signal amplifiers, separation of U and V signals, Color burst separation, Burst phase discriminator, Reference oscillator, Indent and color killer circuits, RO phase shift and 180 degrees PAL-SWITCH circuitry, U & V demodulators, Colour signal mixing	12
UNIT – 4	**HISTORY OF HDTV:** Analog and Digital TV Compared, Going HD, Broadcast Engineering and Information Technology, The Road to HDTV, The Grand Alliance, A DTV Standard at Last, Producing HDTV, HD Goes Coast-to-Coast, DTV Conversion. **COMPRESSION TECHNIQUES:** Compression, MPEG-2 Video Compression, MPEG-4, H.264, Motion – JPEG (M-JPEG) compression.	12
UNIT – 5	**DTV TRANSMITTER AND RECIEVER:** Engineering Basics, Presentation, Transmission, Reception and Demodulation, Transport Stream Demultiplexing, Decoding and Decompression, Program Assembly and Presentation Receiver Issues, Presentation Concerns, standard bodies of HDTV and DTV. **EMERGING TECHNOLOGIES AND STANDARDS:** Technology and Standards Development, Presentation Delivery and Distribution, MPEG and Metadata, Enhanced, Interactive and Personalized, Virtual Product Placement Multiplatform Emergency Alert System.	12
	Total	60

TEXT BOOKS

1. Modern Television Practice – Principles, Technology and Service – R.R.Gulati, New Age International Publication, 2005

2. Television and Video Engineering – A.M.Dhake, 2nd Edition,

3. "HDTV and the Transition to Digital Broadcasting: Understanding New Television Technologies" by Philip J. Cianci, Focal Press, 2007.

REFERENCES: 1. Basic Television and Video Systems – B.Grob and C.E.Herndon, McGrawHill,1999**.**

2.“Newnes Guide to Television and Video Technology" by Ibrahim.K.F, Newnes Publishers, 4th edition, 2007**.**

3.“H.264 and MPEG-4 and Video compression video coding for Next-generation Multimedia" by Iain E.G.Richardson, John Wiley & Sons Ltd., 2003.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		L	T	P	C
		0	0	3	1.5
VLSI Design LAB					

The students are required to design the schematic diagrams using CMOS logic and to draw the layout diagrams to perform the following experiments using CMOS 130nm Technology with necessary EDA tools (Mentor Graphics/Tanner)

List of Experiments:

1. Design and implementation of an inverter
2. Design and implementation of universal gates
3. Design and implementation of full adder
4. Design and implementation of full subtractor
5. Design and implementation of RS-latch
6. Design and implementation of D-latch
7. Design and implementation asynchronous counter
8. Design and Implementation of static RAM cell
9. Design and Implementation of differential amplifier
10. Design and Implementation of ring oscillator

Equipment Required:

1. Mentor Graphics/Tanner software-latest version
2. Personal computer with necessary peripherals.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		L	T	P	C
		0	0	3	1.5

DIGITAL SIGNAL PROCESSING LAB

List of the Experiments / programs

Student has to perform at least FOUR Experiments in each part:

PART-1(SIGNALS)

1) Generation of discrete time signals for discrete signals
2) To verify the Linear Convolution
a) Using MATLAB
b) Using Code Composer Studio(CCS)
3) To verify the Circular Convolution for discrete signals
a) Using MATLAB
b) Using Code Composer Studio(CCS)
4) To Find the addition of Sinusoidal Signals
5) To verify Discrete Fourier Transform(DFT) and Inverse Discrete Fourier Transform(IDFT)
a) Using MATLAB
b) Using Code Composer Studio(CCS)
6) Transfer Function Stability Analysis: using pole-zero plot, bode plot, Nyquist plot, z-plane plot.

PART-2 (FILTERS)

7) Frequency Response of IIR low pass Butterworth Filter
8) Frequency Response of IIR high pass Butterworth Filter
9) Frequency Response of IIR low pass Chebyshev Filter
10) Frequency Response of IIR high pass Chebyshev Filter
11) Frequency Response of FIR low pass Filter using Rectangle Window
12) Frequency Response of FIR low pass Filter using Triangle Window

PART – 3(IMAGE PROCESSING)

13) An image processing in a false contouring system
14) To generate the histogram equalization to the image
15) To verify the Normalized Cross Correlation to the addition of noise and removal of noise using filters toan image.
16) Compute the edge of an image using spatial filters.
17) Perform the image motion blur and calculate PSNR to the noise image and also noise free image.
To verify the PSNR to the Second order Decomposition of Discrete Wavelet transforms and to the reconstructed image using inverse Discrete Wavelet transform

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		L	T	P	C
		0	0	3	1.5
DIGITAL COMMUNICATIONS LAB					

List of Experiments:

1. Time division multiplexing.
2. Frequency Division Multiplexing
3. Pulse code modulation.
4. Differential pulse code modulation.
5. Delta modulation.
6. Frequency shift keying.
7. Phase shift keying .
8. Differential phase shift keying.
9. Companding
10. Source Encoder and Decoder
11. Linear Block Code-Encoder and Decoder
12. Binary Cyclic Code – Encoder and Decoder
13. Convolution Code – Encoder and Decoder

Equipment required for Laboratories:

1. RPS – 0 – 30 V
2. CRO – 0 – 20 M Hz.
3. Function Generators – 0 – 1 M Hz
4. RF Generators – 0 – 1000 M Hz./0 – 100 M Hz.
5. Multimeters
6. Lab Experimental kits for Digital Communication
7. Components

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

III Year - II Semester		L	T	P	C
		1	0	2	2

MACHINE LEARNING WITH SCIKIT(SKILL ADVANCED COURSES/SOFT SKILL COURSES)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand the need for simulation/implementation for the verification of mathematical functions	K2
CO2	Understand the main features of the SCILAB program development environment to enable their usage in the higher learning.	K2
CO3	Implement simple mathematical functions/equations in numerical computing environment such as SCILAB	K3
CO4	Interpret and visualize simple mathematical functions and operations thereon using plots/display	K4
CO5	Analyze the program for correctness and determine/estimate/predict the output and verify it under simulation environment using SCILAB tools & Develop graphs by running Scilab programs	K4

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**			**L**								**M**	**M**		**H**	**M**
CO2					**M**						**L**		**M**	**M**	**H**	**H**
CO3						**H**				**L**			**M**			**M**
CO4							**H**		**M**				**M**	**H**	**H**	**H**
CO5								**M**								

UNIT	CONTENTS	Hours
UNIT – 1	The Fundamentals of Machine Learning, Learning from experience, Machine learning tasks, Training data and test data, Performance measures, bias, and variance, An introduction to sci kit-learn ,Installing scikit-learn **,**Installing scikit-learn on Windows, Installing scikit-learn on Linux ,Installing scikit-learn on OS X, Verifying the installation**,** Installing pandas and matplotlib Linear Regression: Simple linear regression, Evaluating the fitness of a model with a cost function ,Solving ordinary least squares for simple linear regression**,** Evaluating the model, Multiple linear regression, Polynomial regression, Regularization, Applying linear regression, Exploring the data, Fitting and evaluating the model**,** Fitting models with gradient descent	12
UNIT – 2	Extracting features from categorical variables, Extracting features from text, The bag-of-words representation, Stop-word filtering, Stemming and lemmatization, Extending bag-of-words with TF-IDF weights, Space-efficient feature vectorizing with the hashing trick, Extracting features from images, Extracting features from pixel intensities, Extracting points of interest as features ,SIFT and SURF, Data standardization Binary classification with logistic regression, Spam filtering, Binary classification performance metrics, Accuracy, Precision and recall ,Calculating the F1 measure, ROC AUC, Tuning models with grid search, Multi-class classification, Multi-class classification performance metrics, Multi-label classification and problem transformation**,** Multi-label classification performance metrics	12
UNIT – 3	Decision trees ,Training decision trees, Selecting the questions, Information gain, Gini impurity, Decision trees with scikit-learn, Tree ensembles, The advantages and disadvantages of decision trees Clustering with the K-Means algorithm, Local optima, The elbow method, Evaluating clusters, Image quantization, Clustering to learn features	12
UNIT – 4	An overview of PCA ,Performing Principal Component Analysis, Variance, Covariance, and Covariance Matrices, Eigenvectors and eigenvalues, Dimensionality reduction with Principal Component Analysis ,Using PCA to visualize high-dimensional data, Face recognition with PCA	12
UNIT – 5	Kernels and the kernel trick, Maximum margin classification and support vectors, Classifying characters in scikit-learn, Classifying handwritten digits, Classifying characters in natural images Nonlinear decision boundaries, Feed forward and feedback artificial neural networks, Multilayer perceptron, Minimizing the cost function, Forward propagation, Back propagation, Approximating XOR with Multilayer perceptron, Classifying handwritten digits	12
	Total	60

TEXT BOOKS

1 Mastering Machine Learning with scikit-learn, Gavin Hackeling,Packt Publishing

REFERENCE BOOKS

1. Hands-On Machine Learning with Scikit-Learn and TensorFlow, Aurélien Géron

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV YEAR I SEM

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3

ANALOG IC DESIGN (PE-3)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand the concepts of MOS Devices and Modeling	K2
CO2	Design and analyze any Analog Circuits in real time applications.	K6
CO3	Extend the Analog Circuit Design to Different Applications in Real Time.	K2
CO4	Understand of Open-Loop Comparators and Different Types of Oscillators.	K2
CO5	Analyze the frequency response of amplifier and operational amplifier circuits	K4

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**M**	**L**	**H**										**H**		**H**	**M**
CO2				**L**				**M**					**H**	**H**	**M**	**M**
CO3				**L**				**M**					**M**			**H**
CO4				**M**									**H**	**M**	**H**	**H**
CO5				M												

UNIT	CONTENTS	Contact Hours
UNIT – 1	**MOS Devices and Modeling:** The MOS Transistor, Passive Components- Capacitor & Resistor, Integrated circuit Layout, CMOS Device Modeling - Simple MOS Large-Signal Model, Other Model Parameters, Small-Signal Model for the MOS Transistor, Computer Simulation Models, Sub-threshold MOS Model.	12
UNIT – 2	**Analog CMOS Sub-Circuits:** MOS Switch, MOS Diode, MOS Active Resistor, Current Sinks and Sources, Current Mirrors-Current mirror with Beta Helper, Degeneration, Cascode current Mirror and Wilson Current Mirror, Current and Voltage References, Band gap Reference.	12
UNIT – 3	**CMOS Amplifiers:** Inverters, Differential Amplifiers, Cascode Amplifiers, Current Amplifiers, Output Amplifiers, High Gain Amplifiers Architectures.	12
UNIT – 4	**CMOS Operational Amplifiers:** Design of CMOS Op Amps, Compensation of Op Amps, Design of Two-Stage Op Amps, Power- Supply Rejection Ratio of Two-Stage Op Amps, Cascode Op Amps, Measurement Techniques of OP Amp.	12
UNIT – 5	**Comparators:** Characterization of Comparator, Two-Stage, Open-Loop Comparators, Other Open-Loop Comparators, Improving the Performance of Open-Loop Comparators, Discrete-Time Comparators. **Oscillators & Phase-Locked Loops:** General Considerations, Ring Oscillators, LC Oscillators, Voltage Controlled Oscillators. Simple PLL, Charge Pump PLLs, Non-Ideal Effects in PLLs, Delay Locked Loops, Applications.	12
	Total	60

Text Books:

1. Design of Analog CMOS Integrated Circuits- BehzadRazavi, TMH Edition.
2. CMOS Analog Circuit Design - Philip E. Allen and Douglas R. Holberg, Oxford University Press, International Second Edition/Indian Edition, 2010.

References:

1. Analysis and Design of Analog Integrated Circuits- Paul R. Gray, Paul J. Hurst, S. Lewis and R. G. Meyer, Wiley India, Fifth Edition, 2010.
2. Analog Integrated Circuit Design- David A.Johns, Ken Martin, Wiley Student Edn, 2013.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3
MICROWAVE ENGINEERING (PE-3)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Design and analysis of microwave transmission lines	K5
CO2	Working and analysis of microwave amplifiers and oscillators of low power and high power tubes	K3
CO3	Understand the designing & working of a microwave oscillator with solid state materials	K2
CO4	Understand the working of microwave components and s- parameters calculation for reciproca for reciprocal components.	K2
CO5	Understand the S-parameter calculation for non-reciprocal components and measure o microwave parameters	K2

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**	**M**											**L**		**M**	**MM**
CO2				**L**				**M**								
CO3				**M**			**M**							**H**	**M**	
CO4						**L**								**H**		**M**
CO5				**M**									**L**			

UNIT	CONTENTS	Contac Hours
UNIT – 1	**MICROWAVE TRANSMISSION LINES:** Introduction, Microwave Spectrum and Bands, Applications o Microwaves. Rectangular Waveguides – TE/TM mode analysis, Expressions for Fields, Characteristic Equation and Cut-off Frequencies, Sketches of TE and TM mode fields in the cross-section, Mode Characteristics – Phase and Group Velocities, Wavelengths and Impedance Relations, Impossibility of TEM , Related Problems Excitation techniques-wave guides. **MICROSTRIP LINES**: Introduction, Zo Relations, Effective Dielectric Constant, Losses, Q factor.	12
UNIT – 2	**MICROWAVE TUBES:** Limitations and Losses of conventional tubes at microwave frequencies, Re-entran Cavities, Microwave tubes – O type and M type classifications, O-type tubes : Two Cavity Klystrons - Structure, Velocity Modulation Process and Applegate Diagram, Bunching Process and Applications, Reflex Klystrons – Structure, Applegate Diagram and Principle of working, Oscillating Modes and o/p Characteristics Electronic and Mechanical Tuning, Applications, Related Problems.**HELIX TWTS:** Significance, Types and Characteristics of Slow Wave Structures; Structure of TWT and Suppression of Oscillations. **M-type Tubes**Introduction, Cross-field effects, Magnetrons – Different Types, 8-Cavity Cylindrical Travelling Wave Magnetron – Hull Cut-off Condition, Modes of Resonance and PI-Mode Operation, Separation of PI-Mode	12
UNIT – 3	**MICROWAVE SOLID STATE DEVICES:** Introduction, Classification, Applications. TEDs – Introduction Gunn Diode – Principle, RWH Theory, Characteristics, LSA Mode of Operation. Avalanche .Transit Time Devices – Introduction, IMPATT and TRAPATT Diodes – Principle of Operation and Characteristics	12
UNIT – 4	**WAVEGUIDE COMPONENTS AND APPLICATIONS - I :** Coupling Mechanisms – Probe, Loop, Aperture types. Waveguide Discontinuities – Waveguide irises, Tuning Screws and Posts, Matched Loads. Waveguide Attenuators – Resistive Card, Rotary Vane types; Waveguide Phase Shifters – Dielectric, Rotary Vane types Scattering Matrix– Significance, Formulation and Properties, S-Matrix Calculations for – 2 port Junction, E-plane and H-plane Tees, Magic Tee, Hybrid Ring; Directional Couplers – 2Hole, Bethe Hole types.	12
UNIT – 5	**Ferrite Components:** Faraday Rotation, relation between S-parameters in terms of Z and Y parameters, S-parameters of Gyrator, Isolator, Circulator. **MICROWAVE MEASUREMENTS:** Description of Microwave Bench – Different Blocks and their Features, Precautions; Microwave Power Measurement – Bolometer Method Measurement of Attenuation, Frequency, VSWR, Impedance Measurement	12
	Tota	60

TEXT BOOKS:

1. Foundations for Microwave Engineering – R.E. Collin, IEEE Press, John Wiley, 2nd Edition, 2002.
2. Microwave Devices and Circuits – Samuel Y. Liao, PHI, 3rd Edition,1994.

REFERENCES:

1. Microwave Principles – Herbert J. Reich, J.G. Skalnik, P.F. Ordung and H.L. Krauss, CBS Publishers and Distributors, New Delhi, 2004
2. Microwave Engineering- Annapurna Das and SisirK.Das, McGraw Hill Education, 3rd Edition.
3. Microwave Engineering – G S N Raju , I K International

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3
INFORMATION THEORY & CODING(PE-3)					

Pre-requisite:

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Design an Application with Error-Control coding	K1
CO2	Use Compression and Decompression Techniques	K2
CO3	Perform source coding and channel coding	K4
CO4	Design the channel performance using information theory	K6
CO5	Design BCH &RS Codes for channel performance improvement against burst errors	K

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**						**H**						**M**		**H**	**M**
CO2			**M**		**H**								**L**	**M**	**M**	**H**
CO3	**L**												**L**			**M**
CO4				**M**												
CO5				**M**												

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**INFORMATION THEORY AND SOURCE CODING:** Uncertainty, information, entropy and its properties, entropy of binary memory less source and its extension to discrete memory less source, source coding theorem, data compression, prefix coding, Huffman coding, Lempel-Ziv coding, Source with memory and its entropy.	12
UNIT – 2	**DISCRETE CHANNELS:** Binary Symmetric Channel, mutual information & its properties, Channel capacity, channel coding theorem and its application to BSC, Shannon's theorem on channel capacity, capacity of a channel of infinite bandwidth, bandwidth - S/N trade off, practical communication systems in light of Shannon's theorem, Fading channel, channels with memory	12
UNIT – 3	**GROUPS, FIELDS AND LINEAR BLOCK CODES:**Galois field and its construction in GF(2^m) and its basic properties, vector spaces and matrices in GF(2), Linear block codes, systematic codes and its encoding circuit, syndrome and error detection, minimum distance, error detecting and correcting capabilities of block code, decoding circuit, probability of undetected error for linear block code in BSC, Hamming code and their applications.	12
UNIT – 4	**CYCLIC CODES AND BCH CODES:**Basic properties of Cyclic codes, Generator and parity check matrix of cyclic codes, encoding and decoding circuits, syndrome computation and error detection, cyclic Hamming codes, encoding and decoding of BCH codes, error location and correction.	12
UNIT – 5	**CONVOLUTIONALCODES** Introduction to convolution code, its construction and Viterbi algorithm for maximum likelihood decoding, Automatic repeat request strategies and their throughput efficiency considerations	12
	Tota	60

Text Books:

1. Sklar, Digital Communication, Pearson Education Asia, 2nd Edition,2001.
2. Shu Lin and Costello, Error Control Coding: Fundamentals and Applications, 2nd Edition, Pearson,2004.

Reference Books:

1. Simon Haykin, Digital Communication, Wiley Publications,2013.
2. Information theory and coding, Muralidhar Kulkarni, KS AShivaprakash,2015.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3

DATA COMMUNICATIONS & COMPUTER NETWORKS (PE4)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Know the Categories and functions of various Data Communication Networks	K3
CO2	Design and analyze various error detection techniques.	K5
CO3	Demonstrate the mechanism of routing the data in network layer	K4
CO4	Know the Functioning of various Application layer Protocols.	K2
CO5	Identify the basic security threats of a network	K2

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
							H			M						
CO2		L					M		M				M	L		
CO3			M		H		M	L				M	M		L	
CO4											L					
CO5				M			M		H			H				L

UNIT	CONTENTS	Contact Hours
UNIT – 1	**Introduction to Data Communications:** Components, Data Representation, Data Flow, Networks- Distributed Processing, Network Criteria, Physical Structures, Network Models, Categories of Networks Interconnection of Networks, The Interne – A Brief History, The Internet Today, Protocol and Standards – Protocols, Standards, Standards Organizations, Interne Standards. Network Models, Layered Tasks, OSI model, Layers in OSI model, TCP/IP Protocol Suite, Addressing Introduction, Wireless Links and Network Characteristics, WiFi: 802.11 Wireless LANs –The 802.11 Architecture,	12
UNIT – 2	**Data Link Layer:** Links, Access Networks, and LANs- Introduction to the Link Layer, The Services Provided by the Link Layer, Types of errors, Redundancy, Detection vs Correction, Forward error correction Versus Retransmission Error-Detection and Correction Techniques, Parity Checks, Check summing Methods, Cyclic Redundancy Check (CRC) , Framing, Flow Control and Error Control protocols , Noisy less Channels and Noisy Channels, HDLC, Multiple Access Protocols, Random Access ,ALOHA, Controlled access, Channelization Protocols. 802.11 MAC Protocol, IEEE 802.11 Frame.	12
UNIT – 3	**The Network Layer:** Introduction, Forwarding and Routing, Network Service Models, Virtual Circuit and Datagram Networks-Virtual-Circuit Networks, Datagram Networks, Origins of VC and Datagram Networks, Inside a Router-Inpu Processing, Switching, Output Processing, Queuing, The Routing Control Plane, The Internet Protocol(IP):Forwarding and Addressing in the Internet- Datagram format, Ipv4 Addressing, Internet Control Message Protocol(ICMP), Ipv6	12
UNIT – 4	**Transport Layer:** Introduction and Transport Layer Services : Relationship Between Transport and Network Layers, Overview of the Transport Layer in the Internet, Multiplexing and De-multiplexing, Connectionless Transport: UDP –UDP Segment Structure, UDP Checksum, Principles of Reliable Data Transfer-Building a Reliable Data Transfer Protocol, Pipelined Reliable Data Transfer Protocols, Go-Back-N(GBN), Selective Repeat(SR), Connection Oriented Transport: TCP – The TCP Connection, TCP Segment Structure, Round-Trip Time Estimation and Timeout, Reliable Data Transfer, Flow Control, TCP Connection Management, Principles of Congestion Control – The Cause and the Costs of Congestion, Approaches to Congestion Control	12
UNIT – 5	**Application Layer:** Principles of Networking Applications – Network Application Architectures, Processes Communicating, Transport Services Available to Applications, Transport Services Provided by the File Transfer: FTP,-FTP Commands and Replies, Electronic Mail in the Internet- STMP, Comparison with HTTP, DNS-The Internet's Directory Service – Service Provided by DNS, Overview of How DNS Works, DNS Records and messages.	12
	Tota	60

TEXT BOOKS:

1. Computer Networking A Top-Down Approach – Kurose James F, Keith W, 6thEdition , Pearson,2017.
2. Data Communications and Networking Behrouz A.Forouzan4th Edition McGraw Hill Education,2017.

REFERENCES:

1. Data communication and Networks – BhusanTrivedi, Oxford university press,2016
2. Computer Networks – Andrew S Tanenbaum, 4th Edition, PearsonEducation,2003.
3. Understanding Communications and Networks,3rdEdition,W.A.Shay,CengageLearning,2003.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3

LOW POWER VLSI DESIGN
(PE4)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand the need of Low power circuit design.	K2
CO2	Attain the knowledge of architectural approaches.	K4
CO3	Analyze and design Low-Voltage Low-Power combinational circuits.	K4
CO4	Known the design of Low-Voltage Low-Power Memories	K6
CO5	Summarize the power optimization and trade- off technique in digital circuits	K2

#Based on suggested Revised BTL

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**				**M**								**M**		**H**	**M**
CO2			**M**										**L**	**H**	**M**	**H**
CO3													**M**			**L**
CO4			**M**										**H**	**L**	**M**	**H**
CO5				M												

Mapping of course outcomes with program outcomes
(Please fill the above with Levels of Correlation, viz., L, M, H)

UNIT	CONTENTS	Contact Hours
UNIT – 1	**Fundamentals:** Need for Low Power Circuit Design, Sources of Power Dissipation – Switching Power Dissipation, Short Circuit Power Dissipation, Leakage Power Dissipation, Glitching Power Dissipation Short Channel Effects –Drain Induced Barrier Lowering and Punch Through, Surface Scattering, Velocity Saturation, Impact Ionization, Hot Electron Effect.	12
UNIT – 2	**Supply Voltage Scaling for Low Power:** Device Feature Size Scaling, Constant-Field Scaling, Constant-Voltage Scaling, Architectural-Level Approaches: Parallelism for Low Power, Pipelining for Low Power Combining Parallelism with Pipelining, Voltage Scaling Using High-Level Transformations: Multilevel Voltage Scaling Challenges in MVS Voltage Scaling Interfaces, Static Timing Analysis Dynamic Voltage and Frequency Scaling	12
UNIT – 3	**Low-Power Design Approaches:** Low-Power Design through Voltage Scaling – VTCMOS circuits MTCMOS circuits, Architectural Level Approach –Pipelining and Parallel Processing Approaches. Power Gating, Clock Gating Versus Power Gating, Power- Gating Issues, Isolation Strategy, State Retention Strategy, Power-Gating Controller, Power Management, Combining DVFS and Power Management.	12
UNIT – 4	**Low-Voltage Low-Power Adders:** Introduction, Standard Adder Cells, CMOS Adder's Architectures - Ripple Carry Adders, Carry Look- Ahead Adders, Carry Select Adders, Carry Save Adders, Low-Voltage Low-Power Design Techniques –Trends of Technology and Power Supply Voltage.	12
UNIT – 5	**Low-Voltage Low-Power Memories:** Basics of ROM, Low-Power ROM Technology, Basics of SRAM Memory Cell, Pre-charge and Equalization Circuit, Low-Power SRAM Technologies, Basics of DRAM Self-Refresh Circuit.	12
	Tota	60

TEXT BOOKS:

1. CMOS Digital Integrated Circuits – Analysis and Design – Sung-Mo Kang, Yusuf Leblebici,TMH,2011.
2. Low-Voltage, Low-Power VLSI Subsystems – Kiat-Seng Yeo, Kaushik Roy, TMH ProfessionalEngineering,1st edition,2004

REFERENCE BOOKS:

1. Introduction to VLSI Systems: A Logic, Circuit and System Perspective – Ming-BO Lin, CRCPress,2011
2. Low Power CMOS VLSI Circuit Design – Kaushik Roy, Sharat C. Prasad, John Wiley & Sons,2000.
3. Practical Low Power Digital VLSI Design – Gary K. Yeap, Kluwer Academic Press,2002.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3

DIGITAL IMAGE PROCESSING(PE4)

Pre-requisite: Signals & Systems, Digital Signal Processing

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Describe the Image Processing system, Use of Digital Image Processing in different spectra and its general applications, scope of digital image processing and compare various image transforms.	K1 & K2
CO2	Apply basic operations like intensity transformations in spatial and frequency domain.	K3
CO3	Describe Image degradation model and Explain the restoration techniques on images	K2 & K4
CO4	Analyze the digital Images using wavelets and multi resolution processing and use various coding techniques for various image compression methods.	K4 & K3
CO5	State morphological operators; Explain various segmentation techniques on digital images; Explain various Colour models and Colour Image Processing	K1,K2

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**												**M**	**L**		
CO2		**M**													**M**	**H**
CO3							**H**								**M**	**H**
CO4					**M**										**H**	**M**
CO5		**H**													**H**	**M**

Unit	Contents	Contact Hours
Unit I	**Introduction:** Introduction to Image Processing, Examples of fields that use Digital Image Processing, Fundamental steps in digital image processing, components of an image processing system, Elements of Visual Perception, Light and the Electromagnetic Spectrum, Examples of the fields that use Digital Image Processing, Image sensing and acquisition, image sampling and quantization, Some basic relationships between pixels, An introduction to the mathematical tools used in digital image processing. (Text Book: R. C. Gonzalez and R. E. Woods, "Digital Image Processing, 3rd edition, Pearson, 2008.) **Image Transforms:** Need for image transforms, Image transforms, Fourier Transform, 2D Discrete Fourier Transform and its properties, Walsh Transform, Hadamard transform, Haar Transform, Slant transform, Discrete Cosine transform, KL Transform, Singular Value Decomposition.(Text Book: Jayaraman, S. Esakkirajan, and T. Veerakumar," Digital Image Processing", Tata McGraw-Hill Education, 2009)	9 Hours
Unit II	**Intensity Transformations and Spatial Filtering:** Background, Some basic intensity transformation functions, histogram processing, fundamentals of spatial filtering, smoothing spatial filters, and sharpening spatial filters. **Filtering in the Frequency Domain:** The Basics of filtering in the frequency domain, image smoothing using frequency domain filters, Image Sharpening using frequency domain filters, Selective filtering (Text Book: R. C. Gonzalez and R. E. Woods, "Digital Image Processing, 3rd edition, Pearson, 2008.)	9 Hours
Unit III	**Image Restoration and Reconstruction:** A model of the image degradation / Restoration process, Noise models, restoration in the presence of noise only-Spatial Filtering, Periodic Noise Reduction by frequency domain filtering, Linear, Position – Invariant Degradations, Estimating the degradation function, Inverse filtering, Minimum mean square error (Wiener) filtering, image reconstruction from projections. (Text Book: R. C. Gonzalez and R. E. Woods, "Digital Image Processing, 3rd edition, Pearson, 2008.)	9 Hours
Unit IV	**Wavelets and Multi resolution Processing:** Image pyramids, sub band coding, Multi resolution expansions, wavelet transforms in one dimensions& two dimensions, Wavelet packets. **Image compression:** Fundamentals, Basic compression methods: Huffman coding, Golomb coding, Arithmetic coding, LZW coding, Run-Length coding, Block Transform coding, Predictive coding (Text Book: R. C. Gonzalez and R. E. Woods, "Digital Image Processing, 3rd edition, Pearson, 2008.)	9 Hours
Unit V	**Image segmentation:** Fundamentals, point, line, edge detection, thresholding, and Region –based segmentation **Morphological Image Processing:** Preliminaries, Erosion and dilation, opening and closing, basic morphological algorithms, gray-scale morphology **Color image processing:** Color fundamentals, color models, pseudo color image processing, basics of full color image processing, color transformations, smoothing and sharpening. Image segmentation based on color, noise in color images, color image compression. (Text Book: R. C. Gonzalez and R. E. Woods, "Digital Image Processing, 3rd edition, Pearson, 2008.)	9 hours
	Total	**45 hour**

Text Books

1. R. C. Gonzalez and R. E. Woods, "Digital Image Processing, 3rd edition, Pearson,2008.
2. Jayaraman, S. Esakkirajan, and T. Veerakumar," Digital Image Processing", Tata McGraw-Hill Education, 2009.

Reference Books

1. Anil K.Jain, "Fundamentals of Digital Image Processing", Prentice Hall of India, 9th Edition, Indian Reprint, 2002.
2. B.Chanda, D.DuttaMajumder, "Digital Image Processing and Analysis", PHI, 2009.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3

DSP PROCESSORS AND ARCHITECTURES (PE5)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand the basic concepts of Digital Signal Processing.	K2
CO2	To differentiate the architectural features of General purpose processors and DSP processors.	K2
CO3	Understand the architectures of TMS320C54xx devices and ADSP 2100 DSP devices.	K3
CO4	Write the simple assembly language programs by using instruction set of TMS320C54xx.	K4
CO5	To interface the various devices to DSP Processors.	K3&K4

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	H	L	M										M			H
CO2		H	M	M											L	H
CO3			H	M												H
CO4			H	M												H
CO5			H												M	H

UNIT	CONTENTS	Contact Hours
UNIT – 1	**Introduction to Digital Signal Processing:**Introduction, a Digital signal-processing system, the sampling process, discrete time sequences, Discrete Fourier Transform (DFT) and Fast Fourier Transform (FFT), Linear time-invariant systems, Digital filters, Decimation and interpolation.**Computational Accuracy in DSP Implementations:**Number formats for signals and coefficients in DSP systems, Dynamic Range and Precision, Sources of error in DSP implementations, A/D Conversion errors, DSP Computational errors, D/A Conversion Errors, Compensating filter	12
UNIT – 2	**Architectures for Programmable DSP Devices**Basic Architectural features, DSP Computational Building Blocks, Bus Architecture and Memory, Data Addressing Capabilities, Address Generation UNIT, Programmability and Program Execution, Speed Issues, Features for External interfacing	12
UNIT – 3	**Programmable Digital Signal Processors:**Commercial Digital signal-processing Devices, Data Addressing modes of TMS320C54XX Processors,Memory space of TMS320C54XX Processors, Program Control, TMS320C54XX Instructions and Programming, On-Chip Peripherals, Interrupts of TMS320C54XX Processors, Pipeline Operation of TMS320C54XX Processors.	12
UNIT – 4	**Analog Devices Family of DSP Devices :**Analog Devices Family of DSP Devices – ALU and MAC block diagram, Shifter Instruction, Base Architecture of ADSP 2100, ADSP-2181 high performance Processor. Introduction to Black fin Processor – The Black fin Processor, Introduction to Micro Signal Architecture, Overview of Hardware Processing Units and Register files, Address Arithmetic Unit, Control Unit, Bus Architecture and Memory.	12
UNIT – 5	**Interfacing Memory and I/O Peripherals to Programmable DSP Devices:**Memory -space organization, interface External bus interfacing signals, Parallel I/O interface, Programmed I/O, Interrupts and I/O, Direct memory access	12
	Total	60

TEXT BOOKS:

1. Digital Signal Processing – Avtar Singh and S. Srinivasan, Thomson Publications, 2004.
2. A Practical Approach To Digital Signal Processing - K Padmanabhan, R. Vijayarajeswaran, Ananthi. S, New Age International, 2006/2009
3. Embedded Signal Processing with the Micro Signal Architecture: Woon-SengGan, SenM.Kuo, Wiley-IEEE Press, 2007

REFERENCE BOOKS:

1. Digital Signal Processors, Architecture, Prog and Applications-B. Venkataramani and M. Bhaskar, 2002, TMH.
2. DSP Processor Fundamentals, Architectures & Features – Lapsley et al. 2000, S. Chand & Co.
3. Digital Signal Processing App Using the ADSP-2100 Family by The Applications Engineering Staff of Analog Devices, DSP Division, Edited by Amy Mar, PHI

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3
RADAR ENGINEERING (PE5)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	State the radar range equation and solve some analytical problems.	K1
CO2	Discuss the different types of radars and its applications.	K2
CO3	Describe the concept of tracking and different tracking techniques.	K1
CO4	Analyze the various components of radar receiver and its performance	K4
CO5	Able to carry out research and development of the radar system design	K3

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	M			H		L							H		L	M
CO2						M							L	L	M	H
CO3			H										M			H
CO4						L							L	L	H	H
CO5			M													

(Please fill the above with Levels of Correlation, viz., L, M, H)

UNIT	CONTENTS	Contact Hours
UNIT – 1	**Basics of Radar**: Introduction, Maximum Unambiguous Range, simple Radar range Equation, Radar Block Diagram and Operation, Radar Frequencies and Applications. Prediction of Range Performance, Minimum Detectable Signal, Receiver Noise, Illustrative Problems **Radar Equation :** Modified Radar Range Equation, SNR, probability of detection, probability of False Alarm, Integration of Radar Pulses, Radar Cross Section of Targets (simple targets – sphere, cone-sphere), Creeping Wave, Transmitter Power, PRF and Range Ambiguities, System Losses (qualitative treatment), Illustrative Problems.	12
UNIT – 2	**CW and Frequency Modulated Radar :** Doppler Effect, CW Radar – Block Diagram, Isolation between Transmitter and Receiver, Non-zero IF Receiver, Receiver Bandwidth Requirements, Applications of CW radar, Illustrative Problems.**FM-CW Radar:** Range and Doppler Measurement, Block Diagram and Characteristics, FM-CW altimeter, Multiple Frequency CW Radar	12
UNIT – 3	**MTI and Pulse Doppler Radar:** Introduction, Principle, MTI Radar with – Power Amplifier Transmitter and Power Oscillator Transmitter, Delay Line Cancellers – Filter Characteristics, Blind Speeds, Double Cancellation, N^{th} Cancellation Staggered PRFs. Range Gated Doppler Filters. MTI Radar Parameters, Limitations to MTI Performance, MTI versus Pulse Doppler Radar.	12
UNIT – 4	**Tracking Radar:** Tracking with Radar, Sequential Lobing, Conical Scan, Mono pulse Tracking Radar – Amplitude Comparison Mono pulse (one- and two- coordinates), Phase Comparison Mono pulse, Tracking in Range, Acquisition and Scanning Patterns, Comparison of Trackers.	12
UNIT – 5	**Detection of Radar Signals in Noise:** Introduction, Matched Filter Receiver – Response Characteristics and Derivation, Correlation detection, Noise Figure and Noise Temperature.**Radar Transmitters & Receivers** – Displays – types. Duplexers – Branch type and Balanced type, Circulators as Duplexers. Introduction to Phased Array Antennas – Basic Concepts, Radiation Pattern, Beam Steering and Beam Width changes, Series versus parallel feeds, Applications, Advantages and Limitations. Radomes. Modulators, solid-state transmitters	12
	Total	60

TEXT BOOKS:

1. Introduction to Radar Systems – Merrill I. Skolnik, TMH Special Indian Edition, 2nd Ed., 2007.

REFERENCE BOOKS:

1. Radar: Principles, Technology, Applications – Byron Edde, Pearson Education, 2004.
2. Radar Principles – Peebles, Jr., P.Z., Wiley, New York, 1998.
3. Principles of Modern Radar: Basic Principles – Mark A. Richards, James A. Scheer, William A. Holm, Yesdee, 2013

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		**L**	**T**	**P**	**C**
		3	**0**	**0**	**3**
EMBEDDED SYSTEMS (PE-5)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Know basics of embedded system, classification, memories, different communication interface and what embedded firmware is and its role in embedded system, different system components.	K2
CO2	Distinguish all communication devices in embedded system, other peripheral device	K2
CO3	Distinguish concepts of C versus embedded C and compiler versus cross-compiler	K2
CO4	Choose an operating system, and learn how to choose an RTOS	K4
CO5	Acquire knowledge about devices and buses used in embedded networking	K1

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1			**L**	**L**	**L**					**L**	**L**			**M**		
CO2	**L**	**M**	**M**				**M**			**L**					**M**	**M**
CO3	**M**	**M**	**L**			**L**					**L**	**M**	**M**	**M**		
CO4	**H**	**H**	**M**	**M**					**M**	**M**					**H**	**H**
CO5			**M**													

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**Introduction:** Embedded System-Definition, History, Classification, application areas and purpose of embedded systems, The typical embedded system-Core of the embedded system, Memory, Sensors and Actuators, Communication Interface, Embedded firmware, PCB and passive components. Characteristics Quality attributes of an Embedded systems, Application-specific and Domain-Specific examples of an embedded system, Main processing elements of embedded system, hardware and software partitions.	12
UNIT – 2	**Embedded Hardware Design:** Analog and digital electronic components, I/O types and examples, Serial communication devices, Parallel device ports, Wireless devices, Timer and counting devices, Watchdog timer, Real time clock.	12
UNIT – 3	**Embedded Firmware Design:** Embedded Firmware design approaches, Embedded Firmware development languages, ISR concept, Interrupt sources, Interrupt servicing mechanism, Multiple interrupts, DMA, Device driver programming, Concepts of C versus Embedded C and Compiler versus Cross-compiler.	12
UNIT – 4	**Real Time Operating System:** Operating system basics, Types of operating systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Threads, Processes and Scheduling, Task Scheduling, Communication, Synchronization, Device Drivers, How to choose an RTOS. Electronics and Communication Engineering **Hardware Software Co-Design:** Fundamental Issues in Hardware Software Co-Design, Computational models in embedded design, Hardware software Trade-offs, Integration of Hardware and Firmware, ICE.	12
UNIT – 5	**Embedded System Development:** The integrated development environment, Types of files generated on cross-compilation, Deassembler/Decompiler, Simulators, Emulators and Debugging, Target hardware debugging, Boundary Scan, Embedded Software development process and tools. **Embedded System Implementation And Testing:** The main software utility tool, CAD and the hardware, Translation tools-Pre-processors, Interpreters, Compilers and Linkers, Debugging tools, Quality assurance and testing of the design, Testing on host machine, Simulators, Laboratory Tools. Test and evolution of an embedded systems(Build in self testetc).**Case study-** typical embedded system design flow with an example.	12
	Tota	60

Text Books:

1. Embedded Systems Architecture By Tammy Noergaard, Elsevier Publications, 2005
2. Embedded System Design, Frank Vahid, Tony Givargis, John Wiley Publications.

References:

1. Embedding system building blocks By Labrosse, CMP publishers.
2. Embedding system, Second Edition , RajKamal

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		2	0	2	3
VLSI Technology (OE3)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Apply the Concept of design rules during the layout of a circuit.	K3
CO2	Synthesize digital VLSI systems from register-transfer or higher level descriptions	K4
CO3	Understand current trends in semiconductor technology, and how it impacts scaling and performance	K2
CO4	Model and simulate digital VLSI systems using hardware design language.	K1
CO5	Analyse target devices interfacing process	K4

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	M			H					M				H		H	M
CO2			H				M						M	M	M	H
CO3				L									H			M
CO4	M					M							H	M	H	H
CO5				M												

UNIT	CONTENTS	Contact Hours
UNIT – 1	**Introduction :** Introduction to IC Technology, MOS and related VLSI Technology, Basic MOS Transistors Enhancement and Depletion modes of transistor action, IC production process, MOS and CMOS Fabrication processes, BiCMOS Technology, Comparison between CMOS and Bipolar technologies. **Basic Electrical Properties Of MOS and Bi-CMOS Circuits:** I_{ds} versus V_{ds} Relationships, Aspects of MOS transistor Threshold Voltage, MOS transistor Trans, Output Conductance and Figure of Merit. The Pass transistor NMOS Inverter, Pull-up to Pull-down Ratio for NMOS inverter driven by another NMOS inverter, Alternative forms of pull-up, The CMOS Inverter, MOS transistor circuit model, Bi-CMOS Inverter, Latch-up in CMOS circuits and BiCMOS Latch-up Susceptibility	12
UNIT – 2	**MOS and Bi-CMOS Circuit Design Processes:** MOS Layers, Stick Diagrams, Design Rules and Layout General observations on the Design rules, 2µm Double Metal, Double Poly, CMOS/BiCMOS rules, 1.2µm Double Metal, Double Poly CMOS rules, Layout Diagrams of NAND and NOR gates and CMOS inverter Symbolic Diagrams-Translation to Mask Form.	12
UNIT – 3	**Basic Circuit Concepts:** Sheet Resistance, Sheet Resistance concept applied to MOS transistors and Inverters Area Capacitance of Layers, Standard unit of capacitance, The Delay Unit, Inverter Delays, Propagation Delays, Wiring Capacitances, Fan-in and fan-out characteristics, Transistor switches, Realization of gates using NMOS PMOS and CMOS technologies. **Scaling Of MOS Circuits:** Scaling models, Scaling factors for device parameters, Limits due to sub threshold currents, current density limits on logic levels and supply voltage due to noise]	12
UNIT – 4	**Subsystem Design:** Architectural issues, switch logic, Gate logic, examples of structured design, clocked sequential circuits	12
UNIT – 5	**VLSI Design Issues:** VLSI Design issues and design trends, design process, design for testability, technology options, power calculations, package selection, clock mechanisms, Introduction mixed signal design, ASIC design flow, FPGA design flow, introduction to SoC design. Basic CPLD architecture, typical CPLD design flow **FPGA Design:** Basic FPGA architecture, , FPGA configuration, configuration modes, FPGA design process FPGA design flow.	12
	Tota	60

Text Books:

1. Essentials of VLSI Circuits and Systems By Kamran Eshraghian, Douglas and A. Pucknell and SholehEshraghian, Prentice-Hall of India Private Limited,2005 Edition.

References:

1. VLSI Design By A.Albert Raj &T.Latha,PHI Learning Private Limited,2010.
2. VLSI Design-A.Shanthi and A.Kavita, New Age International Private Limited, 2006 First Edition.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		2	0	2	3
SOFTWARE DEFINED RADIO(OE3)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Describe the basics of the software defined radio	K2
CO2	Analyze complex problems critically in the domains of Radio frequency implementation issues, multirate signal processing in SDR, as well as a Smart antenna techniques for better spectrum exploitation for conducting research	K4
CO3	Apply appropriate techniques for the development of scientific and technological knowledge in designing software defined radios and their usage for cognitive radio.	K3
CO4	Demonstrate advanced knowledge in the evolving paradigm of Software defined radio and technologies for its implementation	K3
CO5	To learn the hardware software architectures of software defined radio	K2

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1			**M**	**L**									**H**		**H**	**H**
CO2	**M**		**H**										**M**	**H**	**M**	**M**
CO3			**H**										**M**			**H**
CO4																
CO5				**M**												

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	Introduction: The Need for Software Radios, what is Software Radio, Characteristics and benefits of software radio- Design Principles of Software Radio, RF Implementation issues the Purpose of RF Front – End, Dynamic Range- The Principal Challenge of Receiver Design	12
UNIT – 2	RF Receiver Front- End Topologies- Enhanced Flexibility of the RF Chain with Software Radios- Importance of the Components to Overall Performance- Transmitter Architectures and Their Issues- Noise and Distortion in the RF Chain, ADC and DAC Distortion	12
UNIT – 3	Profile and Radio Resource Management: Communication Profiles- Introduction, Communication Profiles, Terminal Profile, Service Profile, Network Profile, User Profile, Communication Profile Architecture, Profile Data Structure	12
UNIT – 4	XML Structure, Distribution of Profile Data, Access to Profile Data, Management of Communication Profiles, Communication Class marks, Dynamic Class marks for Reconfigurable Terminals, Compression and Coding, Meta Profile Data	12
UNIT – 5	Radio Resource Management in Heterogeneous Networks: Introduction, Definition of Radio Resource Management, Radio Resource Units over RRM Phases, RRM Challenges and Approaches, RRM Modeling and Investigation Approaches, Investigations of JRRM in Heterogeneous Networks	12
	Total	60

Text Books:

2. Software Defined Radio Architecture System and Functions- Markus Dillinger, Kambiz Madani, WILEY 2003
3. Software Defined Radio: Enabling Technologies- Walter Tuttle Bee, 2002, Wiley Publications..

References:

1. Software Radio: A Modern Approach to Radio Engineering - Jeffrey H. Reed, 2002, PEA Publication.
2. Software Defined Radio for 3G - Paul Burns, 2002, Artech House.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		2	0	2	3
BIOMEDICAL SIGNAL PROCESSING(OE-3)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	The student will be able to understand various methods of acquiring bio signals	K2
CO2	The student will be able to understand various sources of bio signal distortions and its remedial techniques	K2
CO3	The students will be able to analyze ECG and EEG signal with characteristic feature points.	K4
CO4	The student will have a basic understanding of diagnosing bio-signals and classifying them.	K2
CO5	Develop a thorough understanding on basics of ECG pattern recognition and classification algorithms	K2

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1								**L**					**H**		**M**	**H**
CO2	**L**					**M**							**M**	**H**	**H**	**H**
CO3													**L**			**M**
CO4				**H**									**M**	**M**	**M**	**M**
CO5			**M**													

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	Random Processes: Stationary random process, Ergodicity, Power spectral density and autocorrelation function of random processes. Noise power spectral density analysis, Noise bandwidth and noise figure of systems.	12
UNIT – 2	Data Compression Techniques: Lossy and Lossless data reduction Algorithms. ECG data compression using Turning point, AZTEC, CORTES, Huffman coding, vector quantization, DICOM Standards	12
UNIT – 3	Cardiological Signal Processing: Pre-processing, QRS Detection Methods, Rhythm analysis, Arrhythmia Detection Algorithms, Automated ECG Analysis, ECG Pattern Recognition. Adaptive Noise Cancelling Principles of Adaptive Noise Cancelling, Adaptive Noise Cancelling with the LMS Adaptation Algorithm Noise Cancelling Method to Enhance ECG Monitoring, Fetal ECG Monitoring	12
UNIT – 4	Signal Averaging, Polishing: Mean and trend removal, Prony's method, Prony's Method based on the Least Squares Estimate, Linear prediction, Yule – Walker (Y –W) equations, Analysis of Evoked Potentials	12
UNIT – 5	Neurological Signal Processing: Modelling of EEG Signals, Detection of spikes and spindles Detection of Alpha, Beta and Gamma Waves. Auto Regressive (A.R.) modelling of seizure EEG. Sleep Stage analysis. Inverse Filtering, Least squares and polynomial modelling.	12
	Tota	60

TEXT BOOKS

1. Probability, Random Variables & Random Signal Principles – Peyton Z. Peebles, 4th Ed., 2009, TMH.
2. Biomedical Signal Processing- Principles and Techniques - D. C. Reddy, 2005, TMH.

REFERENCE BOOKS

1. Digital Bio Dignal Processing - Weitkunat R, 1991, Elsevier.
2. Biomedical Signal Processing - AkayM , IEEE Press.
3. Biomedical Signal Processing -Vol. I Time & Frequency Analysis - Cohen.A, 1986, CRC Press

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		2	0	2	3

PRINCIPLES OF SENSORS
(OE4)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand general concepts of Internet of Things	K2
CO2	Recognize various devices, sensors and applications	K3
CO3	Understand and use various communication protocols for IoT	K5
CO4	Evaluate design issues in IoT applications	K5
CO5	Create IoT solutions using sensors, actuators and Devices & Understand general concepts of Internet of Things	K6

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1			**M**										**M**		**H**	**M**
CO2		**L**					**H**						**L**	**M**	**M**	**H**
CO3	**M**					**M**							**M**			**M**
CO4		**L**			**M**								**H**	**L**	**H**	**H**
CO5	**M**															

UNIT	CONTENTS	Hours
UNIT – 1	**Unit 1: INTRODUCTION** Basics of Measurement – Classification of errors – Error analysis – Static and dynamic characteristics of transducers .	12
UNIT – 2	**Unit 2:** Performance measures of sensors – Classification of sensors – Sensor calibration techniques – Sensor Output Signal Types	12
UNIT – 3	**Unit 3:MOTION, PROXIMITY ANDRANGINGSENSORS** Motion Sensors – Potentiometers, Resolver, Encoders – Optical, Magnetic, Inductive, Capacitive.	12
UNIT – 4	**Unit 4:** LVDT – RVDT – Synchro – Microsyn, Accelerometer.,– GPS, Bluetooth, Range Sensors – RF beacons, Ultrasonic Ranging, Reflective beacons, Laser Range Sensor (LIDAR).	12
UNIT – 5	**Unit 5: FORCE, MAGNETIC AND HEADINGSENSORS** Strain Gage, Load Cell, Magnetic Sensors–types, principle, requirement and advantages: Magneto resistive – Hall Effect – Current sensor Heading Sensors – Compass, Gyroscope,Inclinometers	12
	Total	60

TEXT BOOKS

1. Ernest O Doebelin, "Measurement Systems – Applications and Design", Tata McGraw-Hill,2009.
2. Sawney A K and Puneet Sawney, "A Course in Mechanical Measurements and Instrumentation and Control", 12th edition, Dhanpat Rai & Co, New Delhi,2013.

REFERENCE BOOKS

1. Patranabis D, "Sensors and Transducers", 2nd Edition, PHI, New Delhi,2010.
2. John Turner and Martyn Hill, "Instrumentation for Engineers and Scientists", Oxford SciencePublications,1999.
3. Richard Zurawski, "Industrial Communication Technology Handbook" 2nd edition, CRC Press, 2015

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3
CONSUMER ELECTRONICS (OE4)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	List technical specification of electronics Audio system (microphone and speaker).	K4
CO2	Trouble shoots consumer electronics products like TV, washing machine and AC.	K2
CO3	Identify and explain working of various colour TV transmission blocks.	K3
CO4	Understand various functions of Cam coder and shoot a video and take snapshots and save them in appropriate format	K2&K3
CO5	Understand the basic functions of various consumer electronic goods.	K2

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**M**						**M**						**M**		**M**	**H**
CO2		**M**				**H**		**M**					**H**	**L**	**H**	**M**
CO3			**L**		**H**				**M**				**L**			**H**
CO4									**L**				**M**	**M**		
CO5				**L**												

UNIT	CONTENTS	Hours
UNIT – 1	Audio Fundamentals and Devices: Basic characteristics of sound signal: level and loudness, pitch, frequency response, fidelity and linearity, Reverberation. Audio level metering, decibel level in acoustic measurement. Microphone: working principle, sensitivity, nature of response, directional characteristics.	12
UNIT – 2	Audio systems: CD player, home theatre sound system, surround sound. Digital console: block diagram, working principle, applications. FM tuner: concepts of digital tuning, ICs used in FM tuner TDA 7021T . PA address system: planning, speaker impedance matching, Characteristics, power amplifier, Specification	12
UNIT – 3	Television Receivers and Video Systems: PAL-D colour TV receiver, block diagram, Precision IN Line colour picture tube. Digital TVs:- LCD, LED , PLASMA, HDTV, 3-D TV, projection TV, DTH receiver..	12
UNIT – 4	Home / Office Appliances: FAX and Photocopier. Microwave Oven: types, single chip controllers, wiring and safety instructions, technical specifications. Washing Machine: wiring diagram, electronic controller for washing machine, technical specifications, types of washing machine, fuzzy logic.	12
UNIT – 5	Air conditioner and Refrigerators: Components features, applications, and technical specification. Digital camera and cam coder: - pick up devices - picture processing – picture storage.	12
	Total	60

TEXT BOOKS

1. Consumer Electronics, Bali S.P., Pearson Education India,2010.
2. Audio video systems : principle practices & troubleshooting, Bali R and Bali S.P., Khanna Book Publishing Co. (P) Ltd., 2010Delhi , India.

REFERENCE BOOKS

1. Intellectual Property in Consumer Electronics, Software and Technology Startups, Springer Nature; 2014th edition (24 September 2013),ISBN-10:9781461479116.
2. 2. Consumer Electronics, B.R. Gupta , V. Singhal, S.K. Kataria & Sons; 2013th edition

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		3	0	0	3
Basics of IC Technology (OE4)					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand and analyze the IC 741 operational amplifier and its characteristics	K2
CO2	Design the solution for linear & non-linear applications using IC741	K6
CO3	Elucidate and design the active filters and oscillators.	K2
CO4	Identify the needs of voltage regulators and timers	K3
CO5	Comprehend & differentiate the working principle of various data converters	K6

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1		**H**		**M**									**L**		**M**	**L**
CO2	**L**												**M**	**H**	**L**	**H**
CO3													**L**			**L**
CO4					**M**								**M**	**M**	**M**	**H**
CO5			**H**													

UNIT	CONTENTS	Hours
UNIT – 1	Introduction to Linear Integrated Circuits Ideal and Practical Op-Amp, Op-Amp Characteristics, DC and AC Characteristics, Features of 741 Op-Amp, Modes of Operation - Inverting, Non-Inverting. Non-Linear Applications of OP-AMP Instrumentation Amplifier, AC Amplifier, Differentiators and Integrators, Comparators, Schmitt Trigger, Introduction to Voltage Regulators, Features of 723 Regulator.	12
UNIT – 2	Introduction to Filters Introduction to Active Filters, Characteristics of Band pass, Band reject and All Pass Filters wave form generators Waveform Generators – Triangular, Saw tooth, Square Wave, IC555 Timer –Functional Diagram, Monostable, and Astable Operations	12
UNIT – 3	Digital Integrated Circuits Classification of Integrated Circuits, Comparison of Various Logic Families Combinational Logic ICs – Specifications.	12
UNIT – 4	Applications of Digital ICs Code Converters, Decoders, Demultiplexers, LED & LCD Decoders with Drivers, Encoders, Priority Encoders, Multiplexers, Demultiplexers, Priority Generators/Checkers	12
UNIT – 5	Memories Memories - ROM Architecture, Types of ROMS & Applications, RAM Architecture, Static & Dynamic RAMs.	12
	Total	60

TEXT BOOKS

1. Op-Amps & Linear ICs – Ramakanth A. Gayakwad, PHI, 2003.
2. Digital Fundamentals – Floyd and Jain, Pearson Education, 8th Edition, 2005

REFERENCE BOOKS

1. Linear Integrated Circuits –D. Roy Chowdhury, New Age International (p) Ltd, 2ndEd., 2003.
2. Op Amps and Linear Integrated Circuits-Concepts and Applications James M.Fiore,Cengage Learning/ Jaico, 2009.
3. Operational Amplifiers with Linear Integrated Circuits by K. Lal Kishore –Pearson,2009.
4. Linear Integrated Circuits and Applications – Salivahanan, MC GRAW HILL EDUCATION.
5. Modern Digital Electronics – RP Jain – 4/e – MC GRAW HILL EDUCATION, 2010.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		1	0	2	2

INTRODUCTION TO DATA ANALYTICS(SKILL ADVANCED COURSES)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Explore the fundamental concepts of data analytics	K6
CO2	Understand data analysis techniques for applications handling large data	K2
CO3	Understand various machine learning algorithms used in data analytics process	K2
CO4	Visualize and present the inference using various tools	K4
CO5	Learn to think through the ethics surrounding privacy, data sharing and algorithmic decision-making	K5

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**		**M**										**M**		**H**	**M**
CO2				**H**									**H**	**M**	**M**	**M**
CO3			**M**										**H**			**H**
CO4		**H**											**M**	**L**	**H**	**M**
CO5				**H**												

UNIT	CONTENTS	Hours
UNIT – 1	INTRODUCTION Data Analytics - Types – Phases - Quality and Quantity of data – Measurement - Exploratory data analysis - Business Intelligence	12
UNIT – 2	BIG DATA Big Data and Cloud technologies - Introduction to HADOOP: Big Data, Apache Hadoop, MapReduce - Data Serialization - Data Extraction - Stacking Data - Dealing with data.	12
UNIT – 3	DATA VISUALIZATION Introduction to data visualization – Data visualization options – Filters – Dashboard development tools – Creating an interactive dashboard with dc.js - summary.	12
UNIT – 4	ANALYTICS AND MACHINE LEARNING Machine learning – Modeling Process – Training model – Validating model – Predicting new observations – Supervised learning algorithms – Unsupervised learning algorithms.	12
UNIT – 5	ETHICS AND RECENT TRENDS Data Science Ethics – Doing good data science – Owners of the data - Valuing different aspects of privacy - Getting informed consent - The Five Cs – Diversity – Inclusion – Future Trends.	12
	Total	60

TEXT BOOKS

1. Davy Cielen, Arno D. B. Meysman, Mohamed Ali, Introducing Data Science, Manning Publications Co., 1st edition, 2016.
2. Gareth James, Daniela Witten, Trevor Hastie, Robert Tibshirani, An Introduction to Statistical Learning: with Applications in R, Springer, 1st edition, 2013.
3. Bart Baesens, Analytics in a Big Data World: The Essential Guide to Data Science and its Applications, Wiley.
4. D J Patil, Hilary Mason, Mike Loukides, Ethics and Data Science, O' Reilly, 1st edition, 2018

REFERENCE BOOKS

1. Dr Anil Maheshwari, Data Analytics Made Accessible, Publisher: Amazon.com Services LLC.
2. Joel Grus, Data Science from Scratch: First Principles with Python, O'Reilly, 1st edition, 2015.
3. Cathy O'Neil, Rachel Schutt, Doing Data Science, Straight Talk from the Frontline, O' Reilly, 1st edition, 2013.
4. Jure Leskovec, Anand Rajaraman, Jeffrey David Ullman, Mining of Massive Datasets, Cambridge University Press, 2nd edition, 2014.
5. Eric Siegel, Predictive Analytics The Power to Predict Who Will Click, Buy, Lie, or Die, 2 nd Ed., Wiley

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

IV Year - I Semester		L	T	P	C
		1	0	2	2

INTERFACING WITH ARDUINO(SKILL ADVANCED COURSES)

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand general concepts of Internet of Things	K2
CO2	Recognize various devices, sensors and applications	K4
CO3	Understand and use various communication protocols for IoT	K2
CO4	Evaluate design issues in IoT applications	K5
CO5	Create IoT solutions using sensors, actuators and Devices & Understand general concepts of Internet of Things	K6

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1		**H**			**H**			**H**					**M**		**M**	**H**
CO2				**M**									**M**	**H**	**L**	**M**
CO3	**M**												**H**			**H**
CO4													**H**	**M**	**M**	**M**
CO5	**L**				**L**											

UNIT	CONTENTS	Hours
UNIT – 1	Introduction to IoT: The impact of IoT in industry and daily life, Understanding the IoT ecosystem: devices, platforms, and applications. Overview of IoT Components –Analog sensors, Digital sensors	12
UNIT – 2	Programming an Arduino IoT Device, Preparing the development environment (Arduino IDE), Exploring the Arduino language (C/C++) syntax, Coding, compiling, and uploading to the microcontroller	12
UNIT – 3	Working with Arduino Communication Modules, Bluetooth Modules, WiFi Modules, RFID Modules, I2C and SPI	12
UNIT – 4	Interfacing Arduino and Blynk via USB, LED Blinking, Controlling a Servomotor. ESP8266 WiFi Serial Module – Overview, Setting up the Hardware, Interfacing with Arduino	12
UNIT – 5	Creating an IoT Temperature and Humidity Sensor System – Overview of DHT-22 Sensor, Interfacing the Hardware: Arduino, ESP8266 WiFi Module, and DHT-22 Sensor, Checking Your Data via ThingSpeak, Connecting Your Arduino Set-up to Blynk via WiFi, Running your Arduino IoT Sensor System, Troubleshooting	12
	Total	60

TEXT BOOKS

1. Vijay Madisetti and Arshdeep Bahga, "Internet of Things (A Hands-onApproach)", 1st Edition, VPT, 2014

REFERENCE BOOKS

1. Francis daCosta, "Rethinking the Internet of Things: A Scalable Approach to Connecting Everything", 1st Edition, Apress Publications, 2013
2. Cuno Pfister, Getting Started with the Internet of Things, O"Reilly Media, 2011, ISBN: 978-1-4493- 9357-1

MINOR COURSES

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
ELECTRONIC DEVICES AND CIRCUITS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Apply the basic concepts of semiconductor physics.	K3
CO2	Understand the formation of p-n junction and how it can be used as a p-n junction as diode in different modes of operation.	K2
CO3	Know the construction, working principle of rectifiers with and without filters with relevant expressions and necessary comparisons..	K1
CO4	Understand the construction, principle of operation of transistors, BJT and FET with their V-I characteristics in different configurations.	K2
CO5	Know the need of transistor biasing, various biasing techniques for BJT and FET and stabilization concepts with necessary expressions. & Perform the analysis of small signal low frequency transistor amplifier circuits using BJT and FET in different configurations.	K1

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**M**			**L**									**L**		**M**	**H**
CO2					**M**									**M**		
CO3						**M**										**M**
CO4								**H**					**H**	**L**		
CO5			**H**												**H**	**L**

UNIT	**CONTENTS**	**Contact Hours**
UNIT – 1	**Junction Diode Characteristics :** energy band diagram of PN junction Diode, Open circuited p-n junction, Biased p-n junction, p-n junction diode, current components in PN junction Diode, diode equation, V-I Characteristics, temperature dependence on V-I characteristics, Diode resistance, Diode capacitance.	12
UNIT – 2	**Special Semiconductor Devices**: Zener Diode, Breakdown mechanisms, Zener diode applications, LED, Varactor Diode, Photodiode, Tunnel Diode, UJT, PNPN Diode, SCR. Construction, operation and V-I characteristics.	12
UNIT – 3	Rectifiers and Filters: Basic Rectifier setup, half wave rectifier, full wave rectifier, bridge rectifier, derivations of characteristics of rectifiers, rectifier circuits-operation, input and output waveforms, Filters, Inductor filter(Series inductor), Capacitor filter(Stunt inductor), π-Filter, comparison of various filter circuits in terms of ripple factors.	12
UNIT – 4	**Transistor Characteristics: BJT:** Junction transistor, transistor current components, transistor equation, transistor configurations, transistor as an amplifier, characteristics of transistor in Common Base, Common Emitter and Common Collector configurations, Ebers-Moll model of a transistor, punch through/ reach through, Photo transistor, typical transistor junction voltage values. Transistor Biasing and Thermal Stabilization : Need for biasing, operating point, load line analysis,	12
UNIT – 5	FET: FET types, construction, operation, characteristicsμ, gm, rdparameters, MOSFET-types, construction, operation, characteristics, comparison between JFET and MOSFET. Small Signal Low Frequency Transistor Amplifier Models: BJT**:** Two port network, Transistor hybrid model,.**FET:** Generalized analysis of small signal model,.	12
	Total	60

Text Books:

1. Electronic Devices and Circuits- J. Millman, C. Halkias, Tata Mc-Graw Hill, SecondEdition,2007
2. Electronic Devices and Circuits by David A. Bell, Oxford University Press
3. Electronics devices & circuit theory- Robert L.Boylestad and LouiNashelsky, Pearson/Prentice hall, tenth edition,2009

References:

1. Integrated Electronics-J. Millman, C. Halkias,TataMc-Graw Hill, Second Edition,2009
2. Electronic Devices and Circuits-K. Lal Kishore, BS Publications, Fourth Edition,2016.
3. Electronic Devices and Circuits-Salivahanan, Kumar, Vallavaraj, Tata Mc-Graw Hill, 4th Edition,2008.
4. Electronic Devices and Integrated Circuits – B.P. Singh,Rekha , Pearson publications, 2006.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
SIGNALS and SYSTEMS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Differentiate the various classifications of signals and systems	K4
CO2	Analyze the frequency domain representation of signals using Fourier concepts	K4
CO3	Classify the systems based on their properties and determine the response of LTI Sytems	K6
CO4	Know the sampling process and various types of sampling techniques.	K1
CO5	Apply Laplace and z-transforms to analyze signals and Systems (continuous & discrete).	K3

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1			**M**		**H**							**M**		**M**		**H**
CO2	**L**						**L**					**L**	**M**	**L**	**M**	**M**
CO3						**H**						**M**	**L**	**M**		
CO4												**H**	**M**	**H**	**L**	**H**
CO5						**M**							**H**			

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**INTRODUCTION:** Definition of Signals and Systems, Classification of Signals, Classification of Systems, Operations on signals: time-shifting, time-scaling, amplitude-shifting, amplitude-scaling. Problems on classification and characteristics of Signals and Systems. Complex exponential and sinusoidal signals, Singularity functions and related functions: impulse function, step function signum function and ramp function..	12
UNIT – 2	**FOURIER SERIES AND FOURIERTRANSFORM:** Fourier series representation of continuous time periodic signals, properties of Fourier series, Dirichlet's conditions, Trigonometric Fourier series and Exponential Fourier series, Relation between Trigonometric and Exponential Fourier series, Complex Fourier spectrum. Deriving Fourier transform from Fourier series, Fourier transform of arbitrary signal, Fourier transform of standard signals, Fourier transform of periodic signals, properties of Fourier transforms, Fourier transforms involving impulse function and Signum function.	12
UNIT – 3	**ANALYSIS OF LINEAR SYSTEMS:** Introduction**,** Linear system, impulse response, Response of a linear system, Linear time invariant (LTI) system, Linear time variant (LTV) system, Concept of convolution in time domain and frequency domain, Graphical representation of convolution, Transfer function of a LTI system, Related problems. Filter characteristics of linear systems. Distortion less transmission through a system, Signal bandwidth, system bandwidth, Ideal LPF, HPF and BPF characteristics, Causality and Poly-Wiener criterion for physical realization,	12
UNIT – 4	**CORRELATION:** Auto-correlation and cross-correlation of functions, properties of correlation function, Energy density spectrum, Parseval's theorem **SAMPLING THEOREM :** Graphical and analytical proof for Band Limited Signals, impulse sampling, Natural and Flat top Sampling, Reconstruction of signal from its samples, effect of under sampling – Aliasing, Introduction to Band Pass sampling, Related problems.	12
UNIT – 5	**LAPLACE TRANSFORMS:** Introduction**,** Concept of region of convergence (ROC) for Laplace transforms, constraints on ROC for various classes of signals, Properties of L.T's, Inverse Laplace transform, Relation between L.T's, and F.T. of a signal. Laplace transform of certain signals using waveform synthesis. **Z–TRANSFORMS:** Concept of Z- Transform of a discrete sequence. Region of convergence in Z-Transform, constraints on ROC for various classes of signals, Inverse Z-transform, properties of Z-transforms. Distinction between Laplace, Fourier and Z transforms.	12
	Total	60

TEXT BOOKS:

1. Signals, Systems & Communications - B.P. Lathi, BS Publications, 2003.
2. Signals and Systems - A.V. Oppenheim, A.S. Willsky and S.H. Nawab, PHI, 2ndEdn,1997
3. Signals & Systems - Simon Haykin and Van Veen, Wiley, 2ndEdition,2007

REFERENCE BOOKS:

1. Principles of Linear Systems and Signals – BP Lathi, Oxford University Press,2015
2. Signals and Systems – T K Rawat , Oxford University press,2011

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
SWITCHING THEORY and LOGIC DESIGN					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Classify different number systems and apply to generate various codes.	K4
CO2	Design different types of combinational logic circuits.	K6
CO3	Apply knowledge of flip-flops in designing of Registers and counters	K3
CO4	The operation and design methodology for synchronous sequential circuits and algorithmic state machines.	K5
CO5	Produce innovative designs by modifying the traditional design techniques & Use the concept of Boolean algebra in minimization of switching functions	K4

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**H**							**H**				**M**		**H**	**M**
CO2	**M**											**M**		**H**	**M**	**H**
CO3	**M**					**L**		**M**				**L**	**M**	**M**	**H**	**M**
CO4		**L**										**M**			**M**	**H**
CO5	**M**								**L**			**H**	**L**	**H**	**H**	

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**REVIEW OF NUMBER SYSTEMS &CODES:** Representation of numbers of different radix, conversation from one radix to another radix, r-1's compliments and r's compliments of signed members. Gray code ,4 bit codes; BCD, Excess-3, 2421, 84-2-1 code etc. Error detection & correction codes: parity checking, even parity, odd parity, Hamming code. Realization of three level logic circuits. Study the pin diagram and obtain truth table for the following relevant ICs 7400,7402,7404,7408,7432,7486	12
UNIT – 2	BOOLEAN THEOREMS AND LOGIC OPERATIONS: Boolean theorems, principle of complementation & duality, De-morgan theorems. Logic operations ; Basic logic operations -NOT, OR, AND, Universal Logic operations, EX-OR, EX-NOR operations. Standard SOP and POS Forms, NAND-NAND and NOR-NOR realization	12
UNIT – 3	MINIMIZATION TECHNIQUES: Minimization and realization of switching functions using Boolean theorems, K-Map (up to 3 variables) COMBINATIONAL LOGIC CIRCUITS DESIGN: Design of Half adder, full adder, half subtractor, full subtractor, Excess 3 adder circuit and carry look-a-head adder circuit, Design code converts using Karnaugh method and draw the complete circuit diagrams.	12
UNIT – 4	COMBINATIONAL LOGIC CIRCUITS DESIGN USING MSI &LSI :Design of encoder ,decoder, multiplexer and de-multiplexers, Implementation of higher order circuits using lower order circuits .Realization of Boolean functions using decoders and multiplexers. Design of Priority encoder, 4-bit digital comparator and seven segment decoder.	12
UNIT – 5	SEQUENTIAL CIRCUITS I:Classification of sequential circuits (synchronous and asynchronous) , operation of NAND & NOR Latches and flip-flops; truth tables and excitation tables of RS flip-flop, JK flip-flop, T flip-flop, D flip-flop with reset and clear terminals. Conversion from one flip-flop to another flip-flop. shift register, counters(Elementary treatment)	12
	Total	60

TEXT BOOKS:

1. Switching and finite automata theory Zvi.KOHAVI, Niraj.K.Jha 3rd Edition, Cambridge University Press,2009
2. Digital Design by M.MorrisMano,Michael D Ciletti,4th edition PHI publication,2008
3. Switching theory and logic design by Hill and Peterson**,**Mc-Graw Hill TMH edition, 2012.

REFERENCES:

1. Fundamentals of Logic Design by Charles H. Roth Jr, Jaico Publishers,2006
2. Digital electronics by R S Sedha.S.Chand& company limited,2010
3. Switching Theory and Logic Design by A. AnandKumar,PHI Learning pvt ltd,2016.
4. Digital logic applications and design by John M Yarbough, Cengage learning, 2006.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
ANALOG COMMUNICATIONS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Differentiate various Analog modulation and demodulation schemes and their spectral characteristics	K3
CO2	Analyze noise characteristics of various analog modulation methods	K4
CO3	Analyze various functional blocks of radio transmitters and receivers	K4
CO4	Design simple analog systems for various modulation techniques.	K6
CO5	Understand the importance of noise considerations in communication system	K2

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**												**M**			
CO2			**H**								**M**		**H**	**M**		
CO3							**H**				**L**	**M**	**M**	**H**		
CO4			**M**								**M**			**M**		
CO5		**M**														

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	Introduction to communications systems: communication, communication systems, infonnation,transmitter,channel-noise,receiver,modulation,description,need for modulation, bandwidth requirements, sine wave and fourier series review, frequency spectra of non sinusoidal waves (chapter 1, george kennedy)	12
UNIT – 2	Noise, external noise, internal noise, noise calculations, noise calculations, noise figure, noise temparature(chapter 2, george kennedy)	12
UNIT – 3	Amplitude modulation ,amplitude modulation theory, generation of am (chapter 3, george kennedy)	12
UNIT – 4	Single-sideband techniques , suppression of unwanted sideband, extensions of ssb (chapter 4, george kennedy)	12
UNIT – 5	Frequency modulation , theory of frequency and phase modulation, noise and frequency modulation, generation of frequency modulation (chapter 5 george kennedy)	12
	Total	60

TEXT BOOKS:

1. Principles of Communication Systems – H Taub& D. Schilling, GautamSahe, TMH, 3rdEdition, 2007.
2. Electronics & Communication System – George Kennedy and Bernard Davis, TMH 2004

REFERENCES:

1. Principles of Communication Systems - Simon Haykin, John Wiley, 2ndEdition, 2007
2. Communication Systems– R.P. Singh, SP Sapre, Second Edition TMH,2007.
3. Electronic Communication systems – Tomasi,Pearson, fourth Edition, 2007.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
Linear Integrated Circuits					

Pre-requisite: Network Theory, Electronic Devices and Circuits, Electronic Circuit Analysis
Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Analyse the Differential Amplifier with Discrete components	K4
CO2	Describe the Op-Amp and internal Circuitry: 555 Timer, PLL	K1
CO3	Discuss the Applications of Operational amplifier: 555 Timer, PLL	K2
CO4	Design the Active filters using Operational Amplifier	K5
CO5	Use the Op-Amp in A to D & D to A Converters	K3

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1					**L**										**L**	**L**
CO2			**M**												**H**	**H**
CO3				**M**											**M**	**H**
CO4				**H**											**L**	**M**
CO5					**H**										**H**	**L**

Unit	**Contents**	**Hours**
Unit – 1	**Integrated Circuits:** **Differential Amplifier**- DC and AC analysis of (i) Dual input Balanced output Configuration, (ii) Dual Input Unbalanced Output, (iii)Single Ended Input – Balanced Output (iv) Single Ended Input – un Balanced Output, Cascade Differential Amplifier Stages, Level translator. (Text Book: Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI,1993) **Operational Amplifier:** Introduction, Basic information of Op-Amp, Ideal Operational Amplifier, Op-Amp internal Circuit, Examples of IC Op-Amps, FET Operational Amplifier (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003) Block Diagram Representation of Typical Op-Amp, Analysis of Typical Op-Amp Equivalent Circuit(only MC1435) (Text Book: Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI,1993) OP-Amps Characteristics: Introduction, DC and AC characteristics, 741 op-amp & its features.	9 hrs
Unit – 2	**OP-AMPS Applications:** Introduction, Basic Op-Amp Applications, Instrumentation Amplifier, AC Amplifier, V to I and I to V Converter, Sample and Hold Circuit, Log and Antilog Amplifier, Multiplier and Divider, Differentiator, integrator. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003) **Comparators and Waveform Generators:** Introduction, Comparator, Square Wave Generator, Monostable Multivibrator, Triangular Wave Generator, Sine Wave Generators. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	9 hrs
Unit – 3	**Active Filters:** Design & Analysis of Butterworth active filters – 1st order, 2nd order LPF, HPF filters. Band pass, Band reject and all pass filters. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	9 hrs
Unit – 4	**Timer**s: Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger. **Phase Locked Loops:** Introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK demodulators. Applications of VCO (566) (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	9 hrs
Unit – 5	**Digital To Analog And Analog To Digital Converters:** Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, A-D Converters – parallel Comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC.DAC and ADC Specifications. (Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition,2003)	9 hrs
	Total	45 hrs

Text Books:

1. Linear Integrated Circuits – D. Roy Choudhury, New Age International (p) Ltd, 2nd Edition 2003.
2. Op-Amps & Linear ICs - Ramakanth A. Gayakwad, PHI, 1993.

References:

1. Operational Amplifiers & Linear Integrated Circuits –Sanjay Sharma ;SK Kataria&Sons;2nd Edition,2010
2. Design with Operational Amplifiers & Analog Integrated Circuits – Sergio Franco, McGraw Hill, 1988.
3. OP AMPS and Linear Integrated Circuits concepts and Applications, James M Fiore, Cenage Learning India Ltd.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
ELECTRONIC CIRCUITS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Design and analysis of small signal high frequency transistor amplifier using BJT and FET.	K6
CO2	Design and analysis of multi stage amplifiers using BJT and FET and Differential amplifier using BJT.	K4
CO3	Derive the expressions for frequency of oscillation and condition for oscillation of RC and LC oscillators and their amplitude and frequency stability concept.	K3
CO4	Develop, Design and create simple analogue and digital electronic circuits	K6
CO5	Measure the characteristics electronic circuits and present experimental results	K2

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**		**H**									**M**		**H**		
CO2					**M**										**H**	
CO3		**M**											**H**	**M**		**H**
CO4				**L**												
CO5			**M**													

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	**Small Signal High Frequency Transistor Amplifier models:BJT:** Transistor at high frequencies, Hybrid- π common emitter transistor model, Hybrid π conductance, Hybrid π capacitances, validity of hybrid π model, determination of high-frequency parameters in terms of low-frequency parameters , CE short circuit current gain, current gain with resistive load, cut-off frequencies, frequency response and gain bandwidth product. **FET:** Analysis of common Source and common drain Amplifier circuits at high frequencies.	12
UNIT – 2	**Multistage Amplifiers:** Classification of amplifiers, methods of coupling**, c**ascaded transistor amplifier and its analysis, analysis of two stage RC coupled amplifier, high input resistance transistor amplifier circuits and their analysis-Darlington pair amplifier, Cascode amplifier, Boot-strap emitter follower, Differential amplifier using BJT.	12
UNIT – 3	**Feedback Amplifiers :** Feedback principle and concept, types of feedback, classification of amplifiers, feedback topologies, Characteristics of negative feedback amplifiers, Generalized analysis of feedback amplifiers, Performance comparison of feedback amplifiers, Method of analysis of feedback amplifiers.	12
UNIT – 4	**Oscillators:** Oscillator principle, condition for oscillations, types of oscillators, RC-phase shift and Wien bridge oscillators with BJT and FET and their analysis, Generalized analysis of LC Oscillators, Hartley and Colpitt's oscillators using BJT, Frequency and amplitude stability of oscillators.	12
UNIT – 5	**Power Amplifiers:** Classification of amplifiers(A to H), Class A power Amplifiers, Class B Push-pull amplifiers, Complementary symmetry push pull amplifier, Class AB power amplifier, Class-C power amplifier, Thermal stability and Heat sinks.**Tuned Amplifiers**: Introduction, Q-Factor, small signal tuned amplifier, capacitance single tuned amplifier, double tuned amplifiers, , staggered tuned amplifiers	12
	Total	60

Text Books:

1. Integrated Electronics- J. Millman and C.C. Halkias, Tata McGraw-Hill, 1972.
2. Electronic Devices and Circuits Theory – Robert L. Boylestad and Louis Nashelsky, Pearson/Prentice Hall, Tenth Edition, 2009.
3. Electronic Devices and Integrated Circuits – B.P. Singh,Rekha , Pearson publications, 2006

References:

1. Electronic Circuit Analysis and Design – Donald A. Neaman, McGrawHill, 2010.
2. Microelectronic Circuits-Sedra A.S. and K.C. Smith, Oxford University Press, Sixth Edition, 2011.
3. Electronic Circuit Analysis-B.V.Rao, K.R.Rajeswari, P.C.R.Pantulu, K.B.R.Murthy, Pearson Edn.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
DIGITAL SIGNAL PROCESSING					

Pre-requisite: Signals & Systems
Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Discuss Signals and Systems in Discrete Domain; z-Transforms and its applications to the analysis of LTI systems	K2
CO2	Explain the analysis of signals in frequency domain and calculation of DFT using FFT Algorithms	K2
CO3	Identify the FIR and IIR structures for the required digital filter and study of various filter structures	K1, K2
CO4	Analyze and Design a Digital filter (FIR&IIR) from the given specifications.	K4,K5
CO5	Describe the Architecture of DSP Processor	K1

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**H**												**H**			**M**
CO2	**H**												**M**			**H**
CO3	**M**		**M**	**H**												**H**
CO4	**M**	**M**	**H**												**M**	**H**
CO5	**M**			**H**									**M**			**H**

UNIT	CONTENTS	**Hours**
Unit -1	**Introduction:** Signals, Systems, and Signal Processing, Classification of Signals, The Concept of Frequency in Continuous Time and Discrete Time Signals **Discrete Time Signals and Systems**: Discrete Time Signals, Discrete Time Systems, Analysis of Discrete Time Linear Time Invariant Systems, Discrete Time Systems Described by Difference Equations, Implementation of Discrete Time Systems, Correlation of Discrete Time Signals **The z-Transform and Its Applications to the Analysis of LTI Systems**: The z-Transform, Properties, Rational z Transforms, Inversion of the z-Transform, Analysis of Linear Time Invariant Systems in the z-Domain, The One sided z-Transform.	9
Unit-2	**Frequency Analysis of Signals**: Frequency Analysis of Continuous Time Signals, Frequency Analysis of Discrete Time Signals, Frequency Domain and Time Domain Signal Properties, Properties of the Fourier Transform for Discrete Time Signals. **The Discrete Fourier Transform: Its Properties and Applications**: Frequency Domain Sampling: The Discrete Fourier Transform, Properties of the DFT, Linear Filtering Methods Based on the DFT, Frequency Analysis of Signals Using DFT, The Discrete Cosine Transform. **Efficient Computation of the DFT: Fast Fourier Transform Algorithms**: Direct Computation of the DFT, Radix-2 FFT Algorithms.	9
Unit-3	**Implementation of Discrete Time Systems**: Structures for the Realization of Discrete Time Systems, **Structures for FIR Systems**: Direct Form Structure, Cascade Form Structures, Frequency Sampling Structures **Structures for IIR Systems**: Discrete Form Structures Signal Flow Graphs and Transposed Structures, Cascade Form Structures, Parallel Form Structures.	9
Unit-4	**Design of Digital Filters**: General Considerations: Causality and Its Implications, Characteristics of Practical Frequency Selective Filters. **Design of FIR Filters**: Symmetric and Antisymmetric FIR Filters, Design of Linear Phase FIR Filters Using Windows, Design of Linear Phase FIR Filters by the Frequency Sampling Method. **Design of IIR Filters From Analog Filters**: IIR Filter Design by Approximation of Derivatives, IIR Filter Design by Impulse Invariance, IIR Filter Design by the Bilinear Transformation, Characteristics of Commonly Used Analog Filters. **Frequency Transformations**: Frequency Transformations in the Analog Domain, Frequency Transformations in the Digital Domain.	9
Unit-5	**Introduction to programmable DSPs**: Multiplier and Multiplier Accumulator, Modified bus structures and memory access schemes in P-DSPs ,Multiple Access Memory, Multiported memory, VLIW architecture, Pipelining, Special addressing modes, On-Chip Peripherals. **Architecture of TMS320C5X:** Introduction, Bus Structure, Central Arithmetic Logic Unit, Auxiliary Register ALU, Index Register, Auxiliary Register Compare Register, Block Move Address Register, Block Repeat Registers, Parallel Logic Unit, Memory mapped registers, program controller, some flags in the status registers, On- chip memory, On-chip peripherals. TMS320C5X Assembly Language Instructions.	9
	Total	45

TEXT BOOKS:

1. Digital Signal Processing, Principles, Algorithms, and Applications: John G. Proakis, Dimitris G.Manolakis, 4th Edition, Pearson Education, 2007.
2. Digital Signal Processors – Architecture, Programming and Applications,,B.Venkataramani, M.Bhaskar, TATA McGraw Hill, 2002.

Reference Books:

1. Discrete Time Signal Processing – A.V.Oppenheim and R.W. Schaffer, 3rd Edition, Pearson, 2014.
2. Digital Signal Processing-A. Nagoor Kani, 2nd Edition, McGrawHill Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Minor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
DIGITAL COMMUNICATIONS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Determine the performance of different waveform coding techniques for the generation and digital representation of the signals	K3
CO2	Determine the probability of error for various digital modulation schemes	K3
CO3	Analyse different source coding techniques	K4
CO4	Compute and analyse different error control coding schemes for the reliable transmission of digital information over the channel	K4
CO5	Understand the generation and detection of advanced modulation techniques	K2

Mapping of course outcomes with program outcomes

	PO1	**PO3**	**PO4**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**H**										H	M		
CO2		**L**						**L**						M
CO3			**M**				**M**		**M**				H	
CO4				**L**		**H**					M	M		
CO5														

UNIT	CONTENTS	**Hours**
UNIT – 1	**PULSE DIGITAL MODULATION:** Elements of digital communication systems, advantages of digital communication systems, Elements of PCM: Sampling, Quantization & Coding, Quantization error, Differential PCM systems(DPCM). Delta modulation, its draw backs, adaptive delta modulation, comparison of PCM and DM systems, Time division multiplexing, Frequency division multiplexing	12
UNIT – 2	**DIGITAL MODULATION TECHNIQUES:** Introduction, ASK, FSK, PSK, DPSK, DEPSK, QPSK, M-ary PSK, ASK, FSK, similarity of BFSK and BPSK.	12
UNIT – 3	**INFORMATION THEORY:** Discrete messages, concept of amount of information and its properties. Average information, Entropy and its properties. Information rate, Mutual information and its properties	12
UNIT – 4	**SOURCE CODING:** Introductions, Advantages, Shannon's theorem, LINEAR BLOCK CODES: Introduction, Matrix description of Linear Block codes, Error detection and error correction capabilities of Linear block codes, Hamming codes, Binary cyclic codes, Algebraic structure, encoding, syndrome calculation, BCH Codes.	12
UNIT – 5	**CONVOLUTIONAL CODES:** Introduction, encoding of convolution codes, time domain approach, transform domain approach. Graphical approach: state, tree and trellis diagram decoding using Viterbi algorithm.	12
	Total	60

TEXT BOOKS:

1. Digital communications - Simon Haykin, John Wiley, 2005
2. Principles of Communication Systems – H. Taub and D. Schilling, TMH, 2003
3. Digital Communications- J.Das, S.K.Mullick, P.K.Chatterjee, John willy& sons, 1986.

REFERENCES:

1. Digital and Analog Communication Systems - Sam Shanmugam, John Wiley, 2005.
2. Digital Communications – John Proakis, TMH, 1983. Communication Systems Analog & Digital – Singh &Sapre, TMH, 2004.

3.Modern Analog and Digital Communication – B.P.Lathi, Oxford reprint, 3rd edition, 2004.

HONOR COURSES

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
ARTIFICIAL NEURAL NETWORKS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Develop the basic concepts of Nanotechnology and Nano machines	K3
CO2	Apply fundamentals of logic devices and the need of Quantum computing.	K4
CO3	Illustrate the operation of Silicon MOSFETS	K3
CO4	Describe the mathematical treatment for the modeling and design of the carbon nanotubes	K2
CO5	Understand the applications such as MEMS, RAM, Mass Storage devices and gain knowledge on Electrodes and Contacts	K2

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**L**									**M**		**L**		**H**	
CO2														**M**		**H**
CO3			**M**					**H**		**M**						
CO4						**H**							**M**	**H**		**M**
CO5					**M**											

UNIT	CONTENTS	**Hours**
UNIT – 1	Introduction: What is neural Network, Human Brain, Models of a Neuron, Neural network viewed as a directed graph, feedback, Network Architectures, Knowledge representation, Artifical Intelligence and Neural Networks, Historical Notes	12
UNIT – 2	Learning Processes: Introduction, Error Correction Learning, Memory based learning, Hebbian learning, Competitive Learning, Boltzmann Learning, Credit assignment problem, learning with a teacher, learning without a teacher, learning tasks, memory, adaptation, statistical nature of learning process, statistical learning rheory, Probability approximately correct model of learning	12
UNIT – 3	Single Layer Perceptrons:Introduction, Adaptive filtering problem, unconstrained optimization techniques, linear least square filters, least mean square algorithm, learning curves,learning rate annealing techniques, perceptron,relation between the perceptron and bayes classifier for a Gaussian environment	12
UNIT – 4	Multilayer Perceptrons:Introduction,some prelimenaries, back-propagation algorithm, summary of back propagation algorithm,XOR problrm, Heuristics for making the backpropagation algorithm perform better, output representation and decision rule, convolutional networks	12
UNIT – 5	Radial Basis function networks:Introduction, covers theorem, interpolation problem, supervised learning as an III-posed hypersurface reconstruction, regularization theory, regularization networks, generalized radial basis function networks, XOR problem, estimation of the regularization parameter, approximation properties of RBF netwroks, comparison of RBF networks and multilayer perceptrons	12
	Tota	60

Text Books:

1. Neural Networks & Learning Machines, Simon Haykin, Pearson Education
2. Neural Networks - A Classroom Approach, Satish Kumar, MC Grawhill.

References Books:

1. J.A.freeman, D.M.Skapura,Neural Networks: Algorithms, Applications, and Programming Techniques, Pearson
2. M.H.GHassoun, Fundamentals of Artificial Neural Networks, PHI,

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
NANO ELECTRONICS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	**Develop** the basic concepts of Nanotechnology and Nano machines	K3
CO2	**Apply** fundamentals of logic devices and the need of Quantum computing.	K3
CO3	**Illustrate** the operation of Silicon MOSFETS	K2
CO4	**Describe** the mathematical treatment for the modeling and design of the carbon nanotubes	K2
CO5	**Understand** the applications such as MEMS, RAM, Mass Storage devices and gain knowledge on Electrodes and Contacts	K4

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**L**										**L**		M		M	H
CO2								**L**								
CO3		**M**											H	M		M
CO4						**H**									H	
CO5				**M**										H		

UNIT	CONTENTS	**Hours**
UNIT – 1	Background to nanotechnology: Types of nanotechnology and nanomachines – periodic table – atomic structure – molecules and phases – energy – molecular and atomic size –surface and dimensional space – top down and bottom up; Molecular Nanotechnology: Electron microscope, scanning electron microscope – atomic force microscope –scanning tunnelling microscope –nanomanipulator – nanotweezers – atom manipulation– nanodots – self assembly – dip pen nanolithography. Nanomaterials: preparation –plasma arcing – chemical vapor deposition –sol-gels – electrodeposition – ball milling –applications of nanomaterials;	12
UNIT – 2	Fundamentals of logic devices:- Requirements – dynamic properties – threshold gates; physical limits to computations; concepts of logic devices:- classifications – two terminal devices – field effect devices – coulomb blockade devices – spintronics – quantum cellular automata – quantum computing – DNA computer; performance of information processing systems;- basic binary operations, measure of performance processing capability of biological neurons – performance estimation for the human brain. Ultimate computation:- power dissipation limit – dissipation in reversible computation – the ultimate computer.	12
UNIT – 3	Silicon MOSFETS - Novel materials and alternate concepts:- fundamentals of MOSFET Devices- scaling rules – silicon-dioxide based gate dielectrics – metal gates – junctions & contacts – advanced MOSFET concepts. Quantum transport devices based on resonant tunneling:- Electron tunneling – resonant tunneling diodes – resonant tunneling devices; Single electron devices for logic applications:- Single electron devices – applications of single electron devices to logic circuits.	12
UNIT – 4	Carbon Nanotube: Fullerenes - types of nanotubes – formation of nanotubes – assemblies – purification of carbon nanotubes – electronic propertics – synthesis of carbon nanotubes – carbon nanotube interconnects – carbon nanotube FETs – Nanotube for memory applications – prospects of an all carbon nanotube nanoelectronics	12
UNIT – 5	Electrodes & contacts – functions – molecular electronic devices – first test systems – simulation and circuit design – fabrication; Future applications: MEMS – robots – random access memory – mass storage devicesfor washing machine, technical specifications, types of washing machine, fuzzy logic.	12
	Total	60

Text Books:

1. 'Introduction to Nanoelectronics' by V. V. Mitin, V. Kochelap, Michel A Stroscio.Cambridge, 2007.
2. 'Fundamental of Nanoelectronics' by George W Hanson, Prentice Hall, 2008.

References Books:

1. Michael Wilson, KamaliKannangara, Geoff Smith, Michelle Simmons and Burkhard
2. Raguse, Nanotechnology: Basic Science and Emerging Technologies, Chapman & Hall /CRC, 2002

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
COMPUTER NETWORKS					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand and explore the basics of Computer Networks and Various Protocols.	K2
CO2	Understand the World Wide Web concepts	K2
CO3	Administrate a network and flow of information	K4
CO4	Understand easily the concepts of network security, mobile and ad hoc networks	K2
CO5	Have the Knowledge on Internet transport protocols & Understand the different layers of TCP/IP Protocol Suite	K1

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1				**H**			**L**						H	M		H
CO2		**M**													H	
CO3				H			**L**							H		
CO4	**M**												MM		M	M
CO5				**H**												

UNIT	CONTENTS	**Hours**
UNIT – 1	OVERVIEW OF THE INTERNET Protocol, Layering Scenario, TCP/IP Protocol Suite: The OSI Model, Internet history standards and administration; Comparison of the OSI and TCP/IP reference model. Physical Layer: Guided transmission media, wireless transmission media. Data Link Layer – design issues, CRC Codes, Elementary Data link Layer protocols, sliding window protocol	12
UNIT – 2	MULTIPLE ACCESS PROTOCOLS ALOHA, CSMA, Collision free protocols, Ethernet- Physical Layer, Ethernet Mac Sub layer, data link layer switching & use of bridges, learning bridges, spanning tree bridges, repeaters , hubs, bridges , switches, routers and gateways.	12
UNIT – 3	NETWORK LAYER Network Layer Design issues, store and forward packet switching connection less and connection oriented networks-routing algorithms-optimality principle, shortest path, flooding, Distance Vector Routing, Count to Infinity Problem, Hierarchical Routing, Congestion control algorithms, admission control.	12
UNIT – 4	INTERNETWORKING Tunneling, Internetwork Routing, Packet fragmentation, IPv4, Ipv6 Protocol, IP addresses, CIDR, IMCP, ARP, RARP, DHCP. Transport Layer: Services provided to the upper layers elements of transport protocol-addressing connection establishment, connection release,Connection Release, Crash Recovery	12
UNIT – 5	THE INTERNET TRANSPORT PROTOCOLS UDP-RPC, Real Time Transport Protocols, The Internet Transport ProtocolsIntroduction to TCP, The TCP Service Model, The TCP Segment Header, The Connection Establishment, The TCP Connection Release, The TCP Connection Management Modeling, The TCPSliding Window, The TCP Congestion Control, The future of TCP. Application Layer-Introduction ,providing services, Applications layer paradigms, Client server model, Standard client-server application-HTTP, FTP, electronic mail, TELNET, DNS, SSH	12
	Total	60

TEXT BOOKS

1. Data Communications and Networking – Behrouz A. Forouzan, Fifth Edition TMH, 2013.
2. Computer Networks -- Andrew S Tanenbaum, 4th Edition, Pearson Education.

REFERENCE BOOKS

1. An Engineering Approach to Computer Networks-S.Keshav, 2nd Edition, Pearson Education.
2. Understanding communications and Networks, 3rd Edition, W.A.Shay, Cengage Learning.
3. Introduction to Computer Networks and Cyber Security, Chwan-Hwa (John) Wu, J. David Irwin, CRC Press.
4. Computer Networks, L.L.Peterson and B.S.Davie,4th edition, ELSEVIER.
5. Computer Networking: A Top-Down Approach Featuring the Internet, James F.Kurose,K.W.Ross,3rd Edition, Pearson Education.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
ARTIFICIAL INTELLIGENCE					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understanding the basic concept of AI	K1
CO2	Understanding reasoning and fuzzy logic for artificial intelligence	K2
CO3	Understanding game playing and natural language processing.	K2
CO4	Apply AI techniques to real world problems to develop intelligent systems	K4
CO5	Understand the concept of Artificial Intelligence, search techniques and knowledge representation issues	K3

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1		**M**											**M**			
CO2	**L**														**M**	
CO3					**H**								**H**			**M**
CO4		**M**														
CO5			**M**													

(Please fill the above with Levels of Correlation, viz., L, M, H)

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	What is AI (Artificial Intelligence)? : The AI Problems, The Underlying Assumption, What are AI Techniques, The Level Of The Model, Criteria For Success, Some General References, One Final Word Problems, State Space Search & Heuristic Search Techniques: Defining The Problems As A State Space Search, Production Systems, Production Characteristics, Production System, Characteristics And Issues In The Design Of Search Programs, Additional Problems. Generate-And-Test, Hill Climbing, Best-First Search, Problem Reduction, Constraint Satisfaction, Means-Ends Analysis.	12
UNIT – 2	Knowledge Representation Issues: Representations And Mappings, Approaches To Knowledge Representation. Using Predicate Logic: Representation Simple Facts In Logic, Representing Instance And Isa Relationships, Computable Functions And Predicates, Resolution. Representing Knowledge Using Rules: Procedural Versus Declarative Knowledge, Logic Programming, Forward Versus Backward Reasoning.	12
UNIT – 3	Symbolic Reasoning Under Uncertainty: Introduction To No monotonic Reasoning, Logics For Non-monotonic Reasoning. Statistical Reasoning: Probability And Bays' Theorem, Factors And Rule-Base Systems, Bayesian Networks, Dempster Shafer Theory	12
UNIT – 4	Fuzzy Logic. Weak Slot-and-Filler Structures: Semantic Nets, Frames. Strong Slot-and-Filler Structures: Conceptual Dependency, Scripts, CYC	12
UNIT – 5	Game Playing: Overview, And Example Domain: Overview, Mini Max, Alpha-Beta Cut-off, Refinements, Iterative deepening, The Blocks World, Components Of A Planning System, Goal Stack Planning, Nonlinear Planning Using Constraint Posting, Hierarchical Planning, Reactive Systems, Other Planning Techniques. Understanding: What is understanding? What makes it hard? As constraint satisfaction Natural Language Processing: Introduction, Syntactic Processing, Semantic Analysis, Semantic Analysis, Discourse And Pragmatic Processing, Spell Checking Connectionist Models: Introduction: Hopfield Network, Learning In Neural Network, Application Of Neural Networks, Recurrent Networks, Distributed Representations, Connectionist AI And Symbolic AI	12
	Total	60

References:

1. Elaine Rich and Kevin Knight "Artificial Intelligence", 2nd Edition, Tata Mcgraw-Hill, 2005.
2. Stuart Russel and Peter Norvig, "Artificial Intelligence: A Modern Approach", 3rd Edition, Prentice Hall, 2009.

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**
MACHINE LEARNING					

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand the concepts of computational intelligence like machine learning	K2
CO2	Ability to get the skill to apply machine learning techniques to address the real time Problems in different areas	K3
CO3	Understand the Neural Networks and its usage in machine learning application.	K2
CO4	Apply principles and algorithms evaluate models generated from data	K4
CO5	Apply the algorithms to a real world problems	K3

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1			M											M		
CO2						M		H		H			H			H
CO3		L												H	H	
CO4			M													
CO5			L													

(Please fill the above with Levels of Correlation, viz., L, M, H)

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	Introduction Well-posed learning problems, designing a learning system Perspectives and issues in machine Learning Concept learning and the general to specific ordering Introduction, A concept learning task, concept learning as search, Find-S: Finding a Maximally Specific Hypothesis, Version Spaces and the Candidate Elimination algorithm, Remarks on Version Spaces and Candidate Elimination, Inductive Bias. Decision Tree Learning Introduction, Decision Tree Representation, Appropriate Problems for Decision Tree Learning, The Basic Decision Tree Learning Algorithm Hypothesis Space Search in Decision Tree Learning, Inductive Bias in Decision Tree Learning, Issues in Decision Tree Learning.	12
UNIT – 2	Artificial Neural Networks Introduction, Neural Network Representation, Appropriate Problems for Neural Network Learning, Perceptions, Multilayer Networks and the Back propagation Algorithm, Discussion on the Back Propagation Algorithm, An illustrative Example: Face Recognition Evaluation Hypotheses Motivation, Estimation Hypothesis Accuracy, Basics of Sampling Theory, A General Approach for Deriving Confidence Intervals, Difference in Error of Two Hypotheses, Comparing Learning Algorithms.	12
UNIT – 3	Bayesian learning Introduction, Bayes Theorem, Bayes Theorem and Concept Learning Maximum Likelihood and Least Squared Error Hypotheses, Maximum Likelihood Hypotheses for Predicting Probabilities, Minimum Description Length Principle , Bayes Optimal Classifier, Gibs Algorithm, Naïve Bayes Classifier, An Example: Learning to Classify Text, Bayesian Belief Networks, EM Algorithm. Computational Learning Theory Introduction, Probably Learning an Approximately Correct Hypothesis, Sample Complexity for Finite Hypothesis Space, Sample Complexity for Infinite Hypothesis Spaces, The Mistake Bound Model of Learning. Instance-Based Learning Introduction, k-Nearest Neighbor Learning, Locally Weighted Regression, Radial Basis Functions, Case-Based Reasoning, Remarks on Lazy and Eager Learning.	12
UNIT – 4	Pattern Comparison Techniques Temporal patterns, Dynamic Time Warping Methods, Clustering, Codebook Generation, Vector Quantization Pattern Classification Introduction to HMMS, Training and Testing of Discrete Hidden Markov Models and Continuous Hidden Markov Models, Viterbi Algorithm, Different Case Studies in Speech recognition and Image Processing	12
UNIT – 5	Analytical Learning Introduction, Learning with Perfect Domain Theories : PROLOG-EBG Remarks on Explanation-Based Learning, Explanation-Based Learning of Search Control Knowledge, Using Prior Knowledge to Alter the Search Objective, Using Prior Knowledge to Augment Search Operations. Combining Inductive and Analytical Learning Motivation, Inductive-Analytical Approaches to Learning, Using Prior Knowledge to Initialize the Hypothesis.	12
	Total	60

Text Books

1. Machine Learning – Tom M.Mitchell,-MGH
2. Fundamentals of Speech Recognition By Lawrence Rabiner and Biing – Hwang Juang.

References

1. Machine Learning : An Algorithmic Perspective, Stephen Marsland, Taylor & Francis

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		**L**	**T**	**P**	**C**
		4	**0**	**0**	**4**

DIGITAL CONTROL SYSTEMS

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Understand the concepts of Digital control systems	K2
CO2	Analyze and design discrete systems in state variable analysis	K4
CO3	Relate the concepts of stability analysis and design discrete time systems.	K4
CO4	Steady state error analysis of digital control systems	K5
CO5	Digital control design with digital controller & Design of full and reduced order observer	K2

Mapping of course outcomes with program outcomes

	PO1	**PO2**	**PO3**	**PO4**	**PO5**	**PO6**	**PO7**	**PO8**	**PO9**	**PO10**	**PO11**	**PO12**	**PSO1**	**PSO2**	**PSO3**	**PSO4**
CO1	**H**			**H**				**M**					**H**			
CO2						**L**							**M**	**M**	**H**	
CO3			**M**													**H**
CO4		**M**						**H**					**H**	**M**		
CO5						**L**										**M**

UNIT	CONTENTS	**Hours**
UNIT – 1	INTRODUCTION Block Diagram of typical control system- advantages of sampling in control systems – examples of discrete data and digital systems – data conversion and quantization – sample and hold devices – D/A and A/D conversion – sampling theorem – reconstruction of sampled signals –ZOH. Z-transform: Definition and evaluation of Z-transforms – mapping between s-plane and z-plane – inverse z-plane transform – theorems of the Z-transforms – limitations of z-transforms –pulse transfer function – pulse transfer function of ZOH –relation between G(s) and G(z) – signal flow graph method applied to digital systems	12
UNIT – 2	STATE SPACE ANALYSIS State space modelling of digital systems with sample and hold – state transition equation of digital time in variant systems – solution of time in variant discrete state equations by the Z-Transformation – transfer function from the state model – Eigen values – Eigen vector and diagonalisation of the A-matrix – Jordan canonical form. Computation of state transition matrix-Transformation to phase to variable canonical form-The state diagram – decomposition of digital system – Response of sample data system between sampling instants using state approach. Stability: Definition of stability – stability tests – The second method of Liapunov.	12
UNIT – 3	TIME DOMAIN ANALYSIS Comparison of time response of continuous data and digital control systemscorrelation between time response and root locus j the s-plane and z-plane – effect of polezero configuration in the z-plane upon the maximum overshoot and peak time of transient response – Root loci for digital control systems – steady state error analysis of digital control systems – Nyquist plot – Bode plot-G.M and P.M	12
UNIT – 4	DESIGN The digital control design with digital controller with bilinear transformation – Digital PID controller-Design with deadbeat response-Pole placement through state feedback-Design of full order state observer-Discrete Euler Lagrance Equation – Discrete maximum principle	12
UNIT – 5	DIGITAL STATE OBSERVER Design of – Full order and reduced order observers. Design by max. Principle: Discrete Euler language equation-discrete maximum principle.	12
	Total	60

TEXT BOOKS

1. Discrete-Time Control systems – K. Ogata, Pearson Education/PHI, 2nd Edition.
2. Digital Control and State Variable Methods by M. Gopal, TMH.

REFERENCE BOOKS

1. Digital Control Systems, Kuo, Oxford University Press, 2nd Edition, 2003.
2. Digital Control Engineering, M. Gopal
3. Digital Control Engineering Analysis and Design, M. Sami Fadali, AntonioVisioli, Second Edition, Academic Press

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		L	T	P	C
		4	0	0	4

PATTERN RECOGNITION

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Explain & compare a variety of pattern classifications ,structural pattern recognition	K2
CO2	Analyze the pattern classifier combination technique	K4
CO3	Illustrate the artificial neural network based pattern recognition	K2
CO4	Discuss the application pattern recognition	K2
CO5	Summarize the various techniques in pattern recognition	K2

#Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	**L**												**M**		**M**	
CO2		**L**	**M**										**H**			**H**
CO3				**M**										**M**	**H**	**M**
CO4						**H**										
CO5					**H**								**H**		**H**	

(Please fill the above with Levels of Correlation, viz., L, M, H)

UNIT	CONTENTS	**Contact Hours**
UNIT – 1	Introduction: Fundamental problems in pattern Recognition system design, Design concepts and methodologies, Simple pattern recognition model. Decisions and Distance Functions: Linear and generalized decision functions, Pattern space and weight space, Geometrical properties, implementations of decision functions, Minimum-distance pattern classifications. Probability - Probability of events: Random variables, Joint distributions and densities, Movements of random variables, Estimation of parameter from samples.	12
UNIT – 2	Decision making - Baye's theorem, Multiple features, Conditionally independent features, Decision boundaries, Unequal cost of error, estimation of error rates, the leaving-oneout-techniques, characteristic curves, estimating the composition of populations. Baye's classifier for normal patterns. Non Parametric Decision Making: histogram, kernel and window estimation, nearest neighbour classification techniques. Adaptive decision boundaries, adaptive discriminant functions, Minimum squared error discriminant functions, choosing a decision making techniques.	12
UNIT – 3	Clustering and Partitioning: Hierarchical Clustering: Introduction, agglomerative clustering algorithm, the single-linkage, complete-linkage and average-linkage algorithm. Ward's method Partition clustering-Forg's algorithm, K-means's Algorithm, Isodata algorithm.	12
UNIT – 4	Pattern Preprocessing and Feature selection: distance measures, clustering transformation and feature ordering, clustering in feature selection through entropy minimization, features selection through orthogonal expansion, binary feature selection.	12
UNIT – 5	Syntactic Pattern Recognition and Application of Pattern Recognition: Concepts from formal language theory, formulation of syntactic pattern recognition problem, syntactic pattern description, recognition grammars, automata as pattern recognizers, Application of pattern recognition techniques in bio-metric, facial recognition, IRIS scon, Finger prints, etc.	12
	Total	60

Reference books:

1. Pattern recognition and Image Analysis, Gose. JohnsonbaughJost, PHI.
2. Pattern Recognition Principle, Tou. Rafael. Gonzalez, Pea.
3. Pattern Classification, Richard duda, Hart., David Strok, Wiley

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY:: KAKINADA
UNIVERSITY COLLEGE OF ENGINEERING KAKINADA (AUTONOMOUS)
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Honor Course		L	T	P	C
		4	0	0	4
IMAGE AND VIDEO PROCESSING					

Pre-requisite: Signals & Systems, Digital Signal Processing.

Course Outcomes: At the end of the course, student will be able to

		Knowledge Level (K)#
CO1	Define the digital image, representation of digital image, importance of image resolution, applications in image processing.	K1
CO2	Express the advantages of representation of digital images in transform domain, application of various image transforms.	K2
CO3	Describe how an image can be enhanced by using histogram techniques, filtering techniques etc	K3
CO4	Discuss image degradation, image restoration techniques using spatial filters and frequency domain	K2
CO5	Discuss the detection of point, line and edges in images, edge linking through local processing, global processing	K2

 # Based on suggested Revised BTL

Mapping of course outcomes with program outcomes

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3	PSO4
CO1	H	M											H	M		
CO2	M				M	L							M	H		
CO3					H								L	H		
CO4					L								L	M		
CO5				H	L								M	M		

(Please fill the above with Levels of Correlation, viz., L, M, H)

Unit	Contents	Contact Hours
Unit – 1	**Fundamentals of Image Processing and Image Transforms:** Introduction, Image sampling, Quantization, Resolution, Image file formats, Elements of image processing system, Applications of Digital image processing Introduction, Need for transform, image transforms, Fourier transform, 2 D Discrete Fourier transform and its transforms, Importance of phase, Walsh transform, Hadamard transform, Haar transform, slant transform Discrete cosine transform, KL transform, singular value decomposition, Radon transform, comparison of different image transforms.	9 hrs
Unit – 2	**Image Enhancement:** Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters. Frequency domain methods: Basics of filtering in frequency domain, image smoothing, image sharpening, Selective filtering. **Image Restoration:** Introduction to Image restoration, Image degradation, Types of image blur, Classification of image restoration techniques, Image restoration model, Linear and Nonlinear image restoration techniques, Blind deconvolution	9 hrs
Unit – 3	**Image Segmentation:** Introduction to image segmentation, Point, Line and Edge Detection, Region based segmentation., Classification of segmentation techniques, Region approach to image segmentation, clustering techniques, Image segmentation based on thresholding, Edge based segmentation, Edge detection and linking, Hough transform, Active contour **Image Compression:** Introduction, Need for image compression, Redundancy in images, Classification of redundancy in images, image compression scheme, Classification of image compression schemes, Fundamentals of information theory, Run length coding, Shannon – Fano coding, Huffman coding, Arithmetic coding, Predictive coding, Transformed based compression, Image compression standard, Wavelet-based image compression, JPEG Standards.	9 hrs

Unit – 4	**Basic Steps of Video Processing:** Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, Filtering operations.	9 hrs
Unit – 5	**2-D Motion Estimation:** Optical flow, General Methodologies, Pixel Based Motion Estimation, Block- Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.	9 hrs
	Total	45 hrs

TEXT BOOKS:

1. Digital Image Processing – Gonzaleze and Woods, 3rd Ed., Pearson.
2. Video Processing and Communication – Yao Wang, JoemOstermann and Ya–quin Zhang. 1st Ed., PH Int.
3. S.Jayaraman, S.Esakkirajan and T.VeeraKumar, "Digital Image processing, Tata McGraw Hill publishers, 2009

REFRENCE BOOKS:

1. Digital Image Processing and Analysis-Human and Computer Vision Application with CVIP Tools – ScotteUmbaugh, 2nd Ed, CRC Press, 2011.
2. Digital Video Processing – M. Tekalp, Prentice Hall International.
3. Digital Image Processing – S.Jayaraman, S.Esakkirajan, T.Veera Kumar – TMH, 2009.
4. Multidimentional Signal, Image and Video Processing and Coding – John Woods, 2nd Ed, Elsevier.
5. Digital Image Processing with MATLAB and Labview – Vipula Singh, Elsevier.
6. Video Demystified – A Hand Book for the Digital Engineer – Keith Jack, 5th Ed., Elsevier.

.