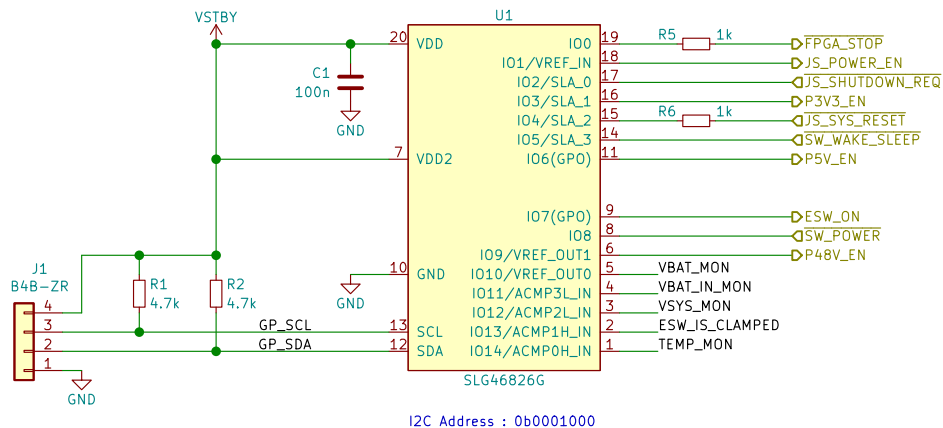
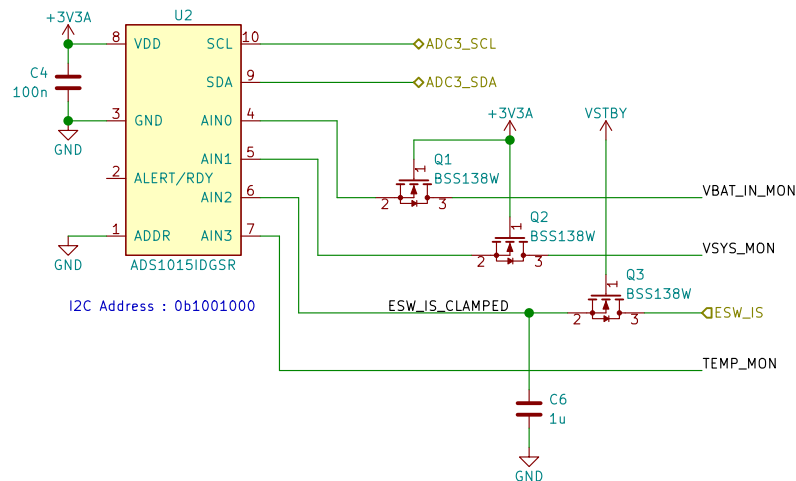


Power Sequencer



ADC3 (controller by Processor)



Voltage Divider



Sheet: /PowerSequencer/
File: PowerSequencer.sch

Title: Phoenix PVT

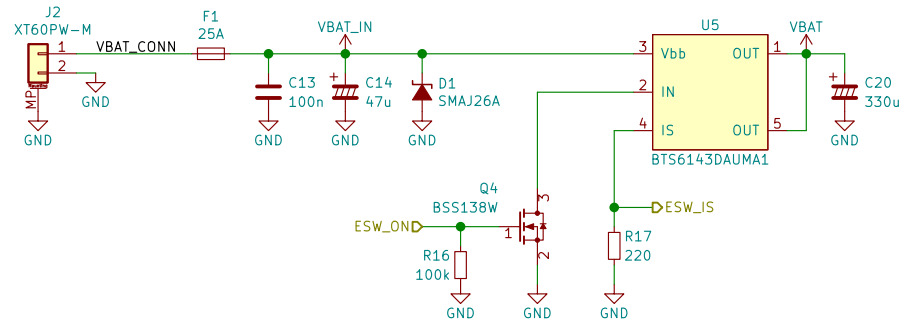
Size: A4 Date: 2020-07-19

KiCad E.D.A. kicad (5.1.8)-1

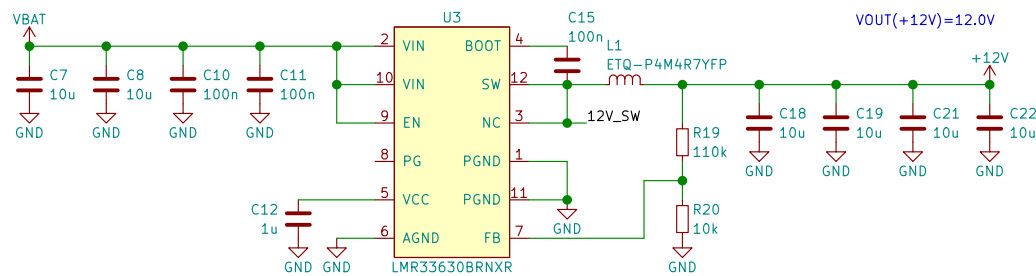
Rev: 1

Id: 2/24

Input Protection

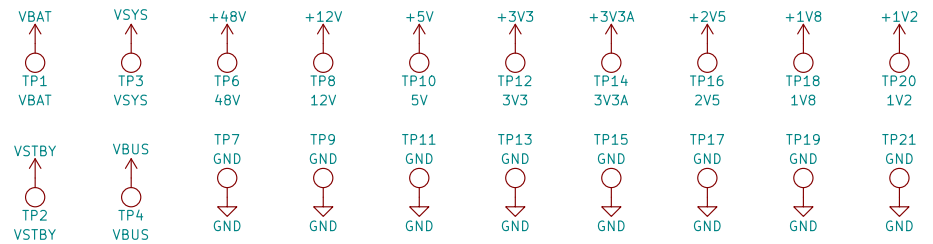
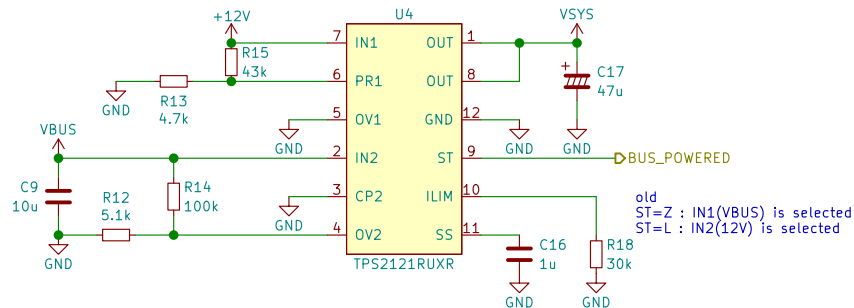


VBAT to 12V/3A Buck Converter

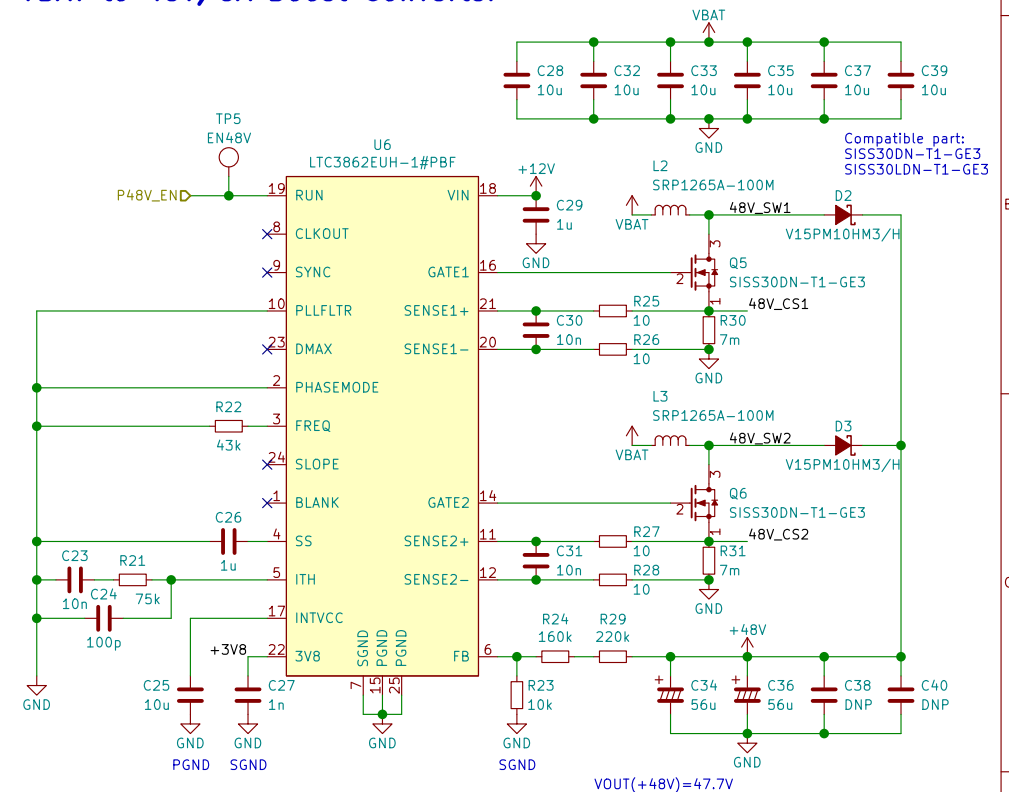


12V/VBUS Multiplexer

```
VBUS : Priority Threshold=None
      : Overvoltage Threshold=21.8V
+12V : Priority Threshold=10.8V
      : Overvoltage Threshold=None
```



VBAT to 48V/8A Boost Converter



Sheet: /Power1/
File: Power1.sch

Title: *Phoenix PVT*

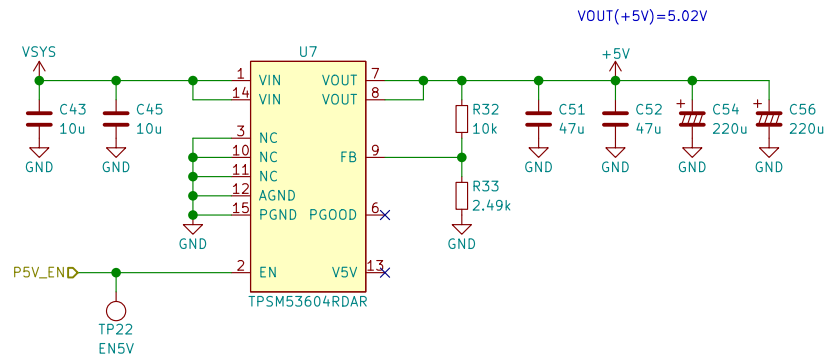
Size: A4	Date: 2020-07-19
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KiCad E.D.A.	kiCad (5.1.8)-1
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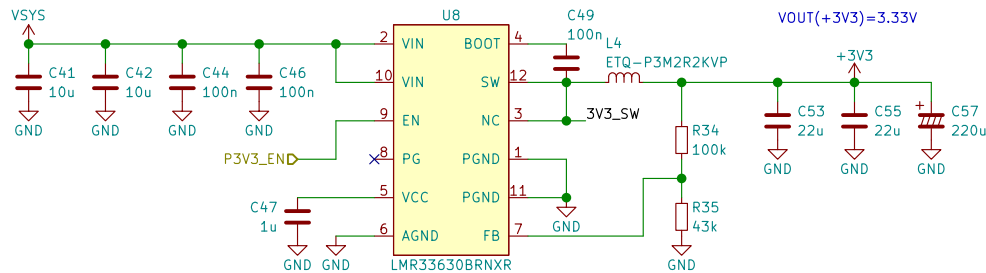
Rev: 1

Id: 3/24

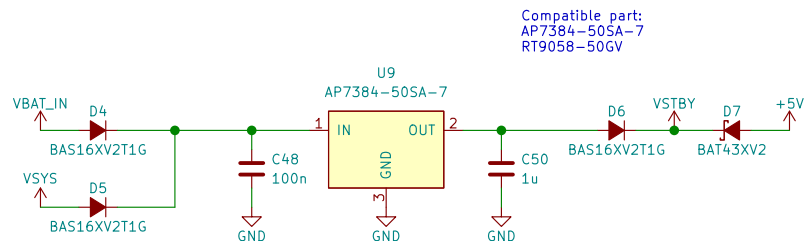
VSYS to 5V/4A Buck Regulator



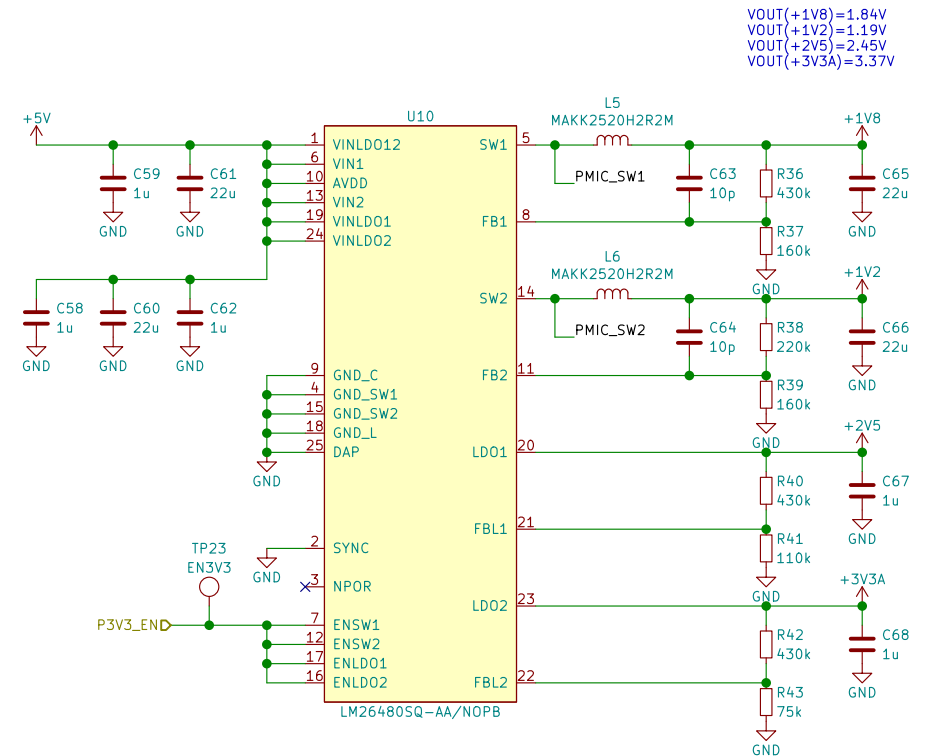
VSYS to 3.3V/3A Buck Regulator



Standby 5V LDO Regulator



5V to 1.2V/1.8V/2.5V/3.3V(Analog)



Sheet: /Power2/
File: Power2.sch

Title: Phoenix PVT

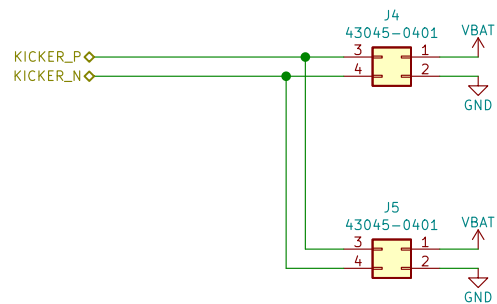
Size: A4 Date: 2020-07-19

KiCad E.D.A. kicad (5.1.8)-1

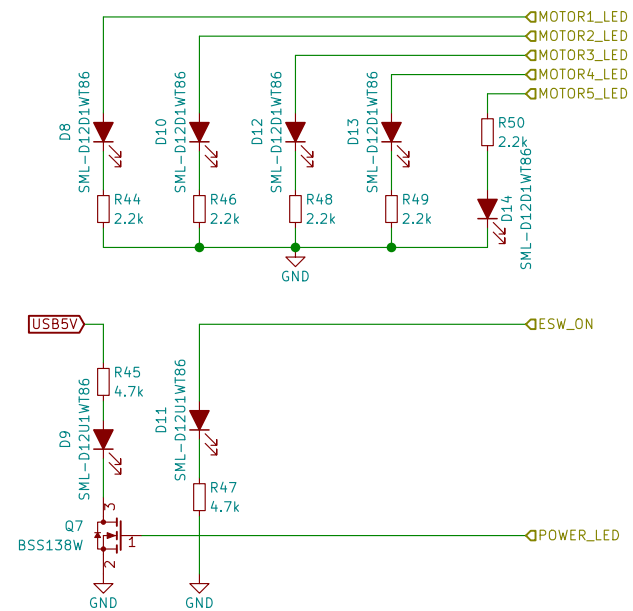
Rev: 1

Id: 4/24

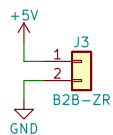
Kicker Interface



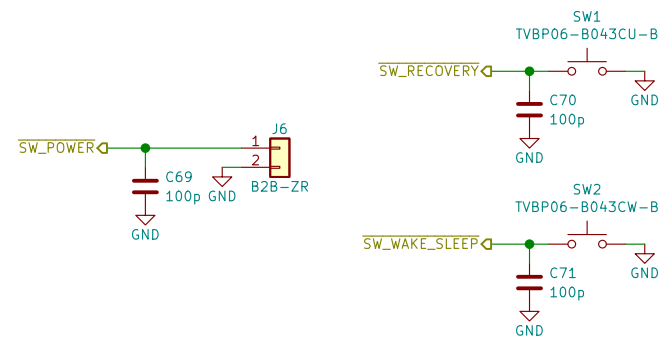
Indicators



Fan



Switches



Sheet: /Interface1/
File: Interface1.sch

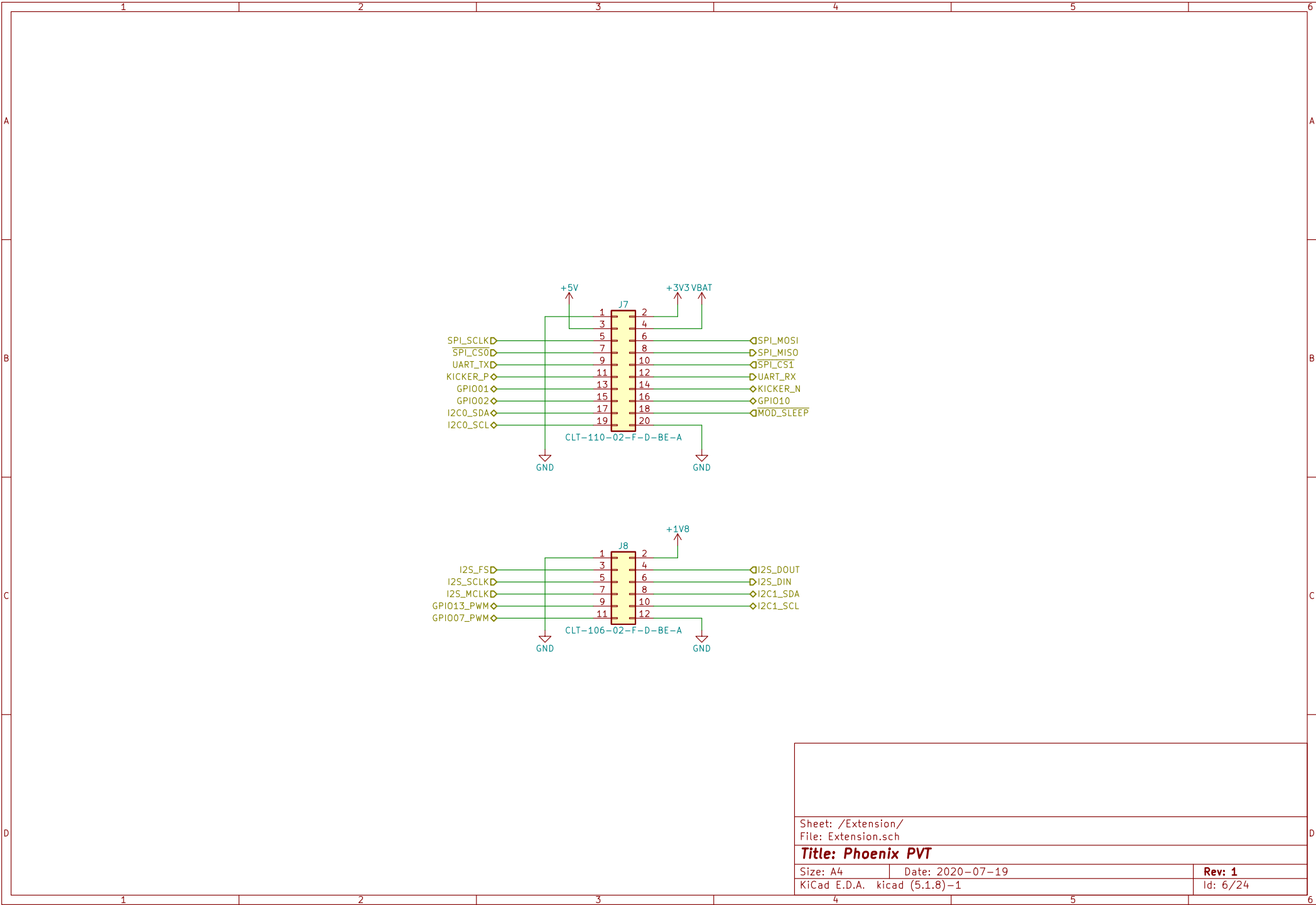
Title: Phoenix PVT

Size: A4 Date: 2020-07-19

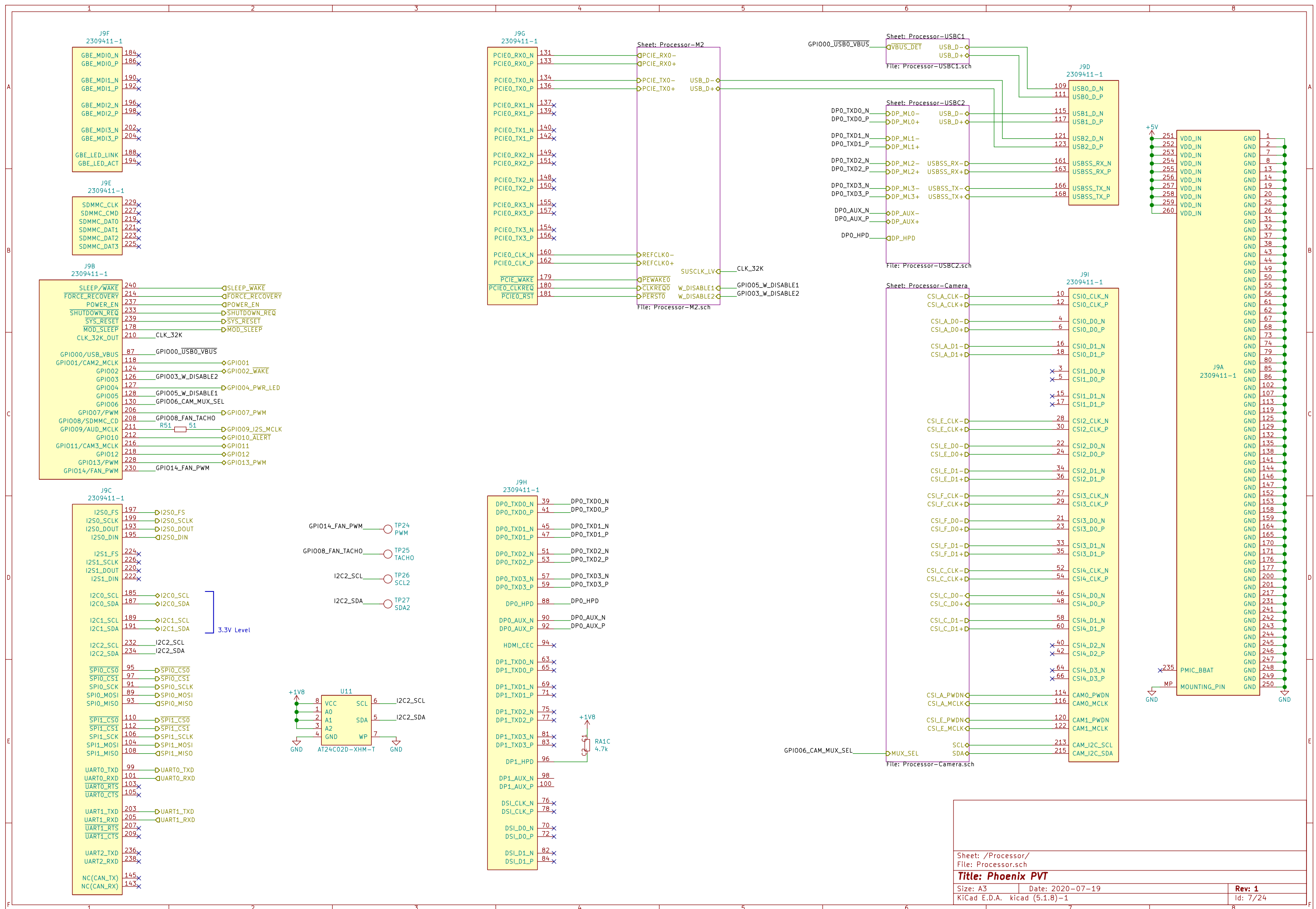
KiCad E.D.A. kicad (5.1.8)-1

Rev: 1

Id: 5/24



Sheet: /Extension/ File: Extension.sch		
Title: Phoenix PVT		
Size: A4	Date: 2020-07-19	Rev: 1
KiCad E.D.A. kicad (5.1.8)-1		Id: 6/24



Camera 2,3 are not used.

CSL_C_CLK+
CSL_C_CLK-

CSL_C_D0+
CSL_C_D0-

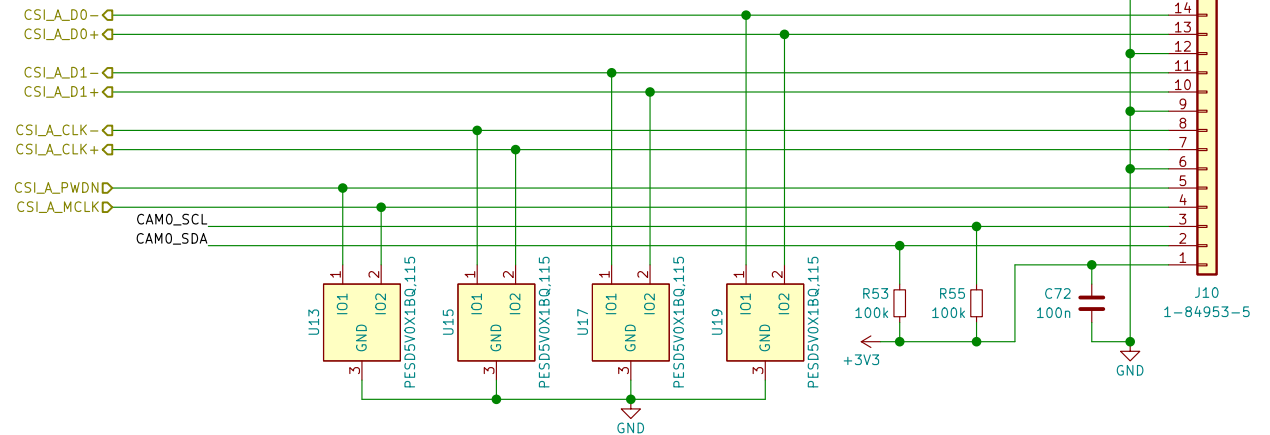
CSL_C_D1+
CSL_C_D1-

CSL_F_CLK+
CSL_F_CLK-

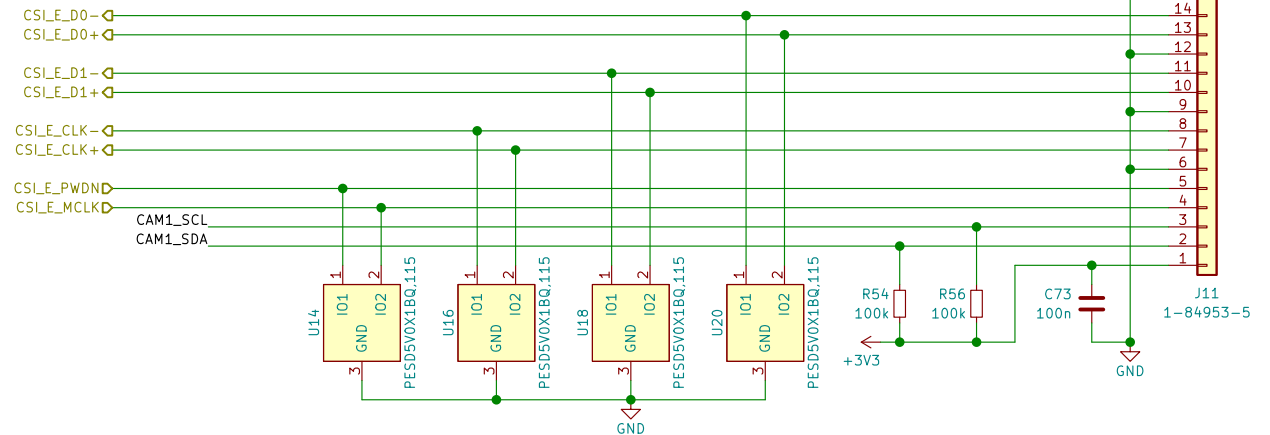
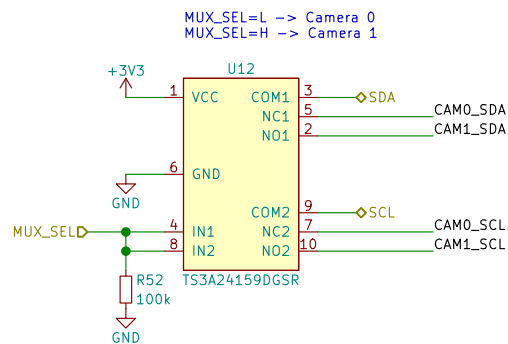
CSL_F_D0+
CSL_F_D0-

CSL_F_D1+
CSL_F_D1-

Camera 0 (CSI A)



Camera 1 (CSI E)



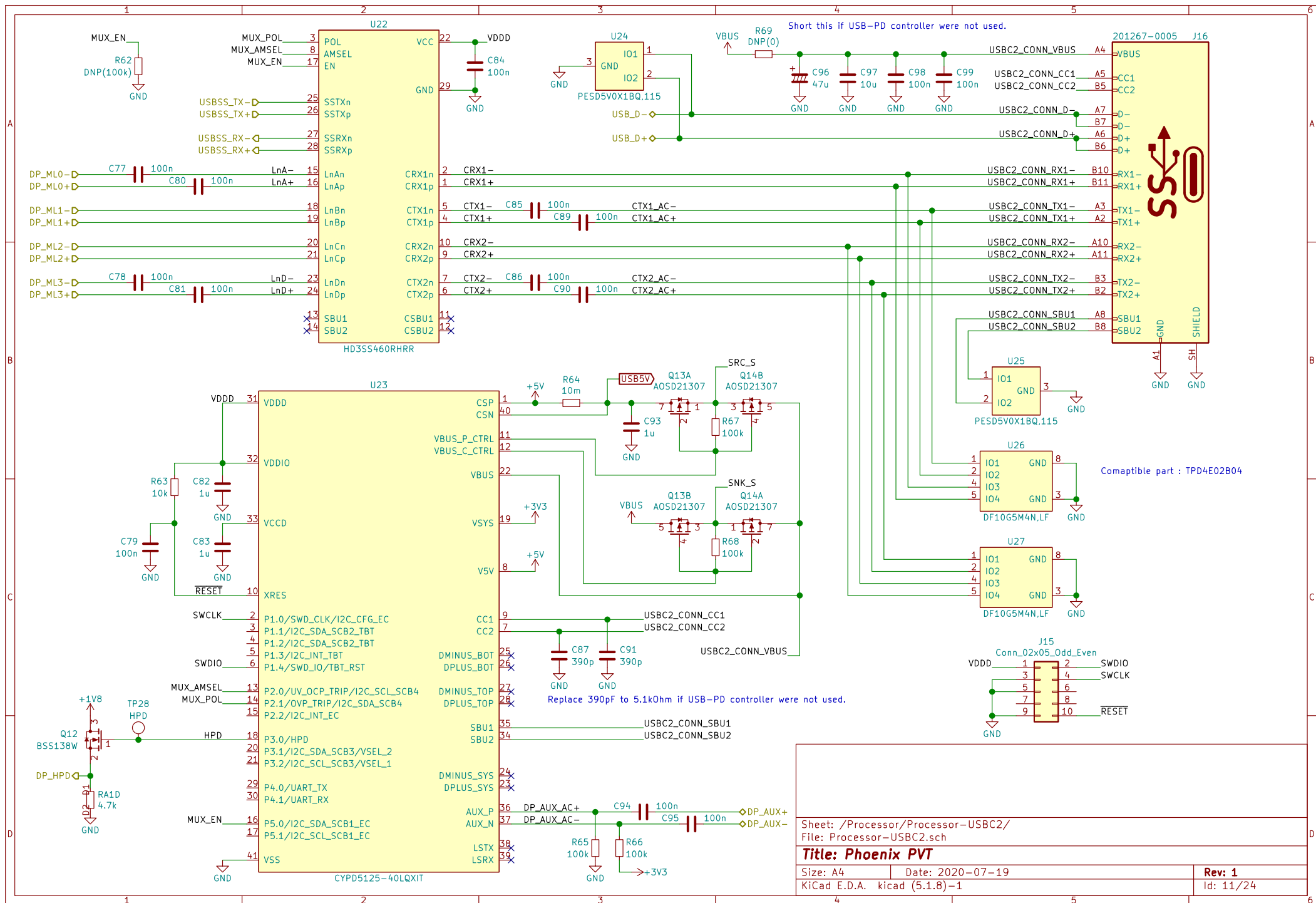
Sheet: /Processor/Processor-Camera/
File: Processor-Camera.sch

Title: Phoenix PVT

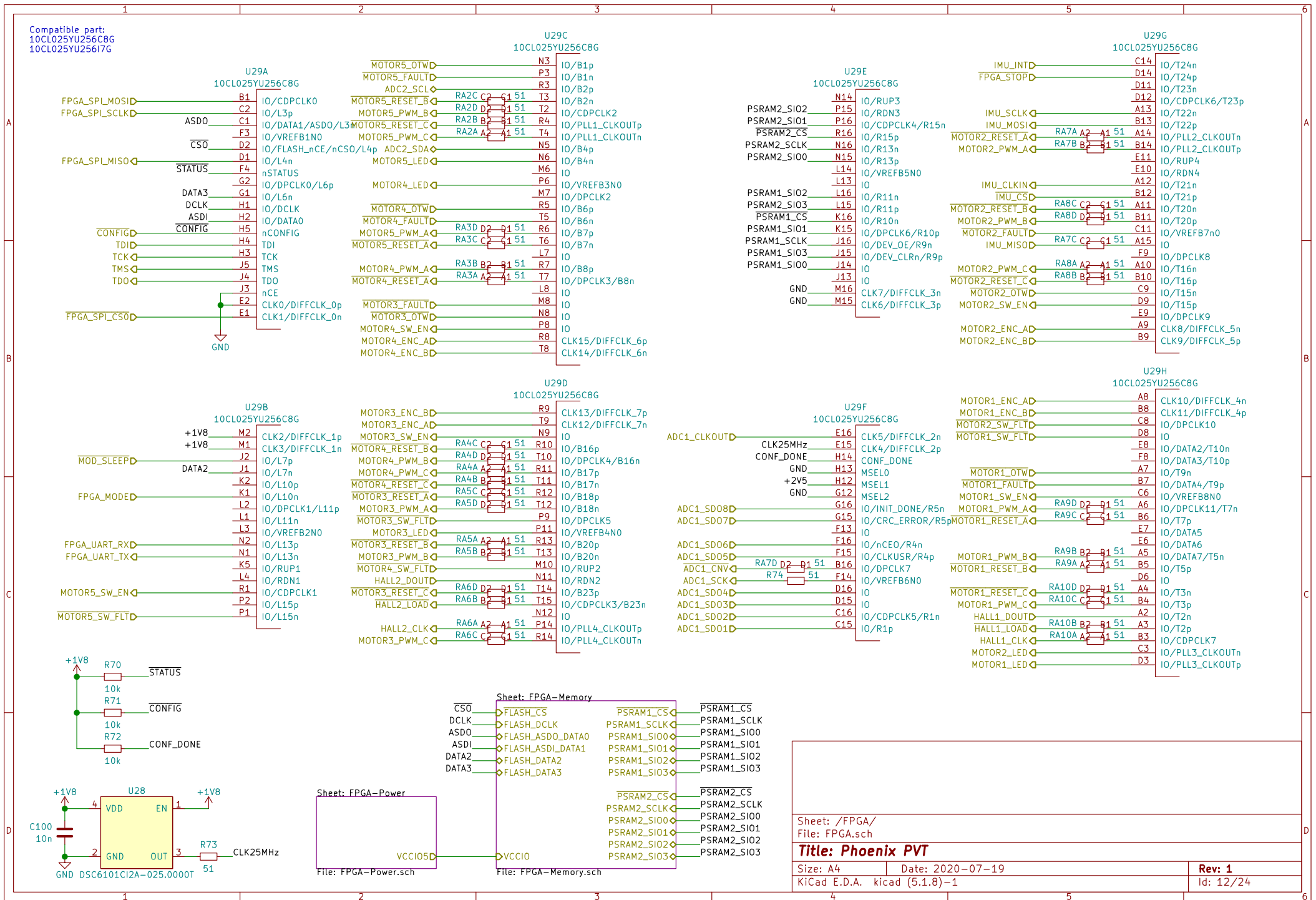
Size: A4 Date: 2020-07-19
KiCad E.D.A. kicad (5.1.8)-1

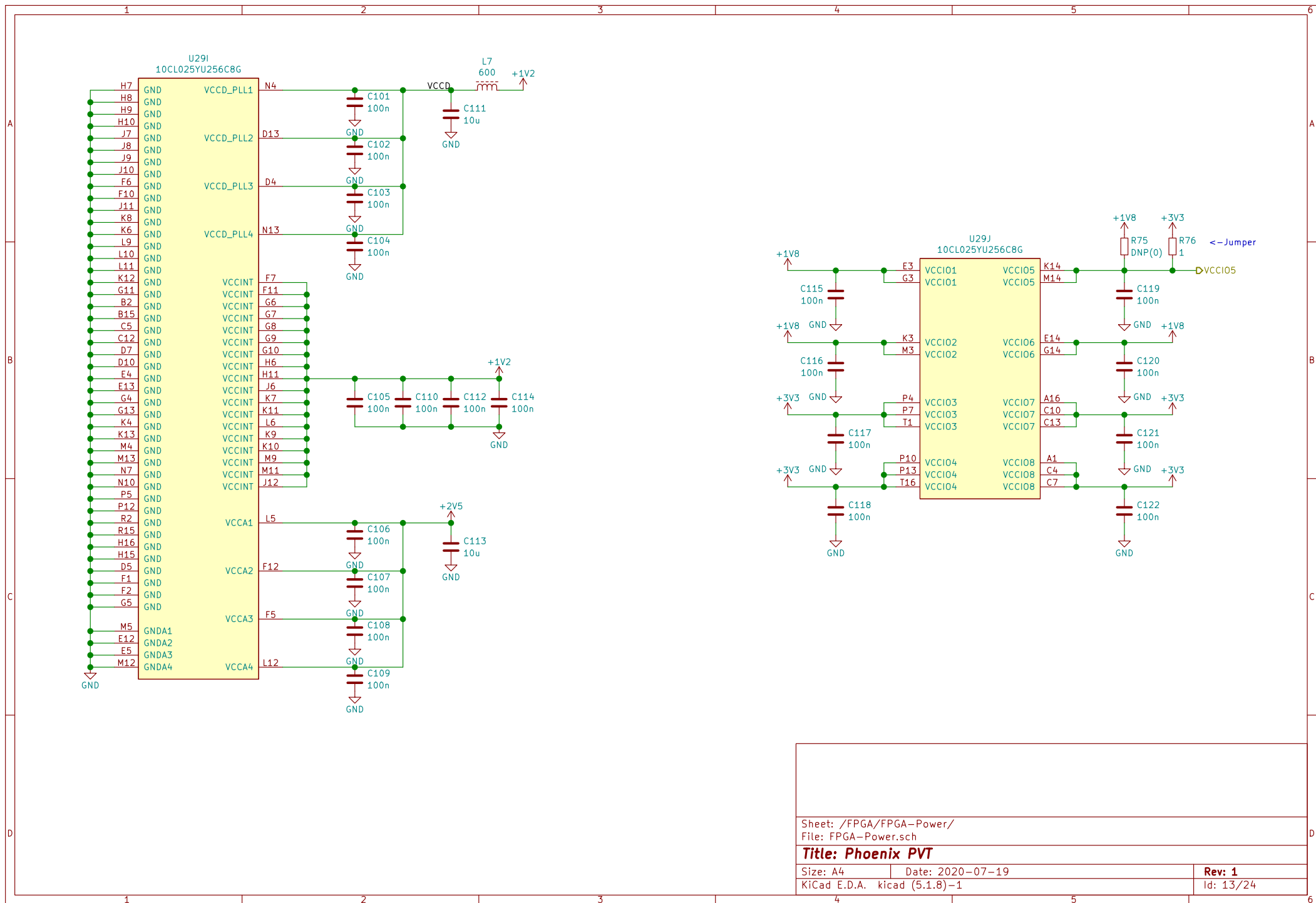
Rev: 1
Id: 8/24

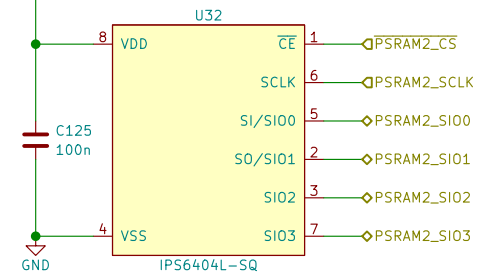
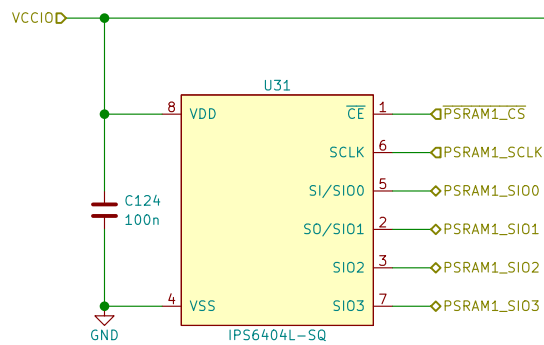
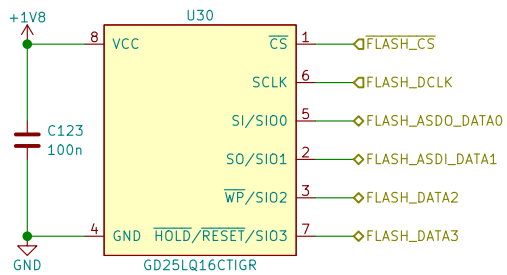




Compatible part:
10CL025YU256C8G
10CL025YU256I7G







PSRAMs are reserved for future use.

Sheet: /FPGA/FPGA-Memory/
File: FPGA-Memory.sch

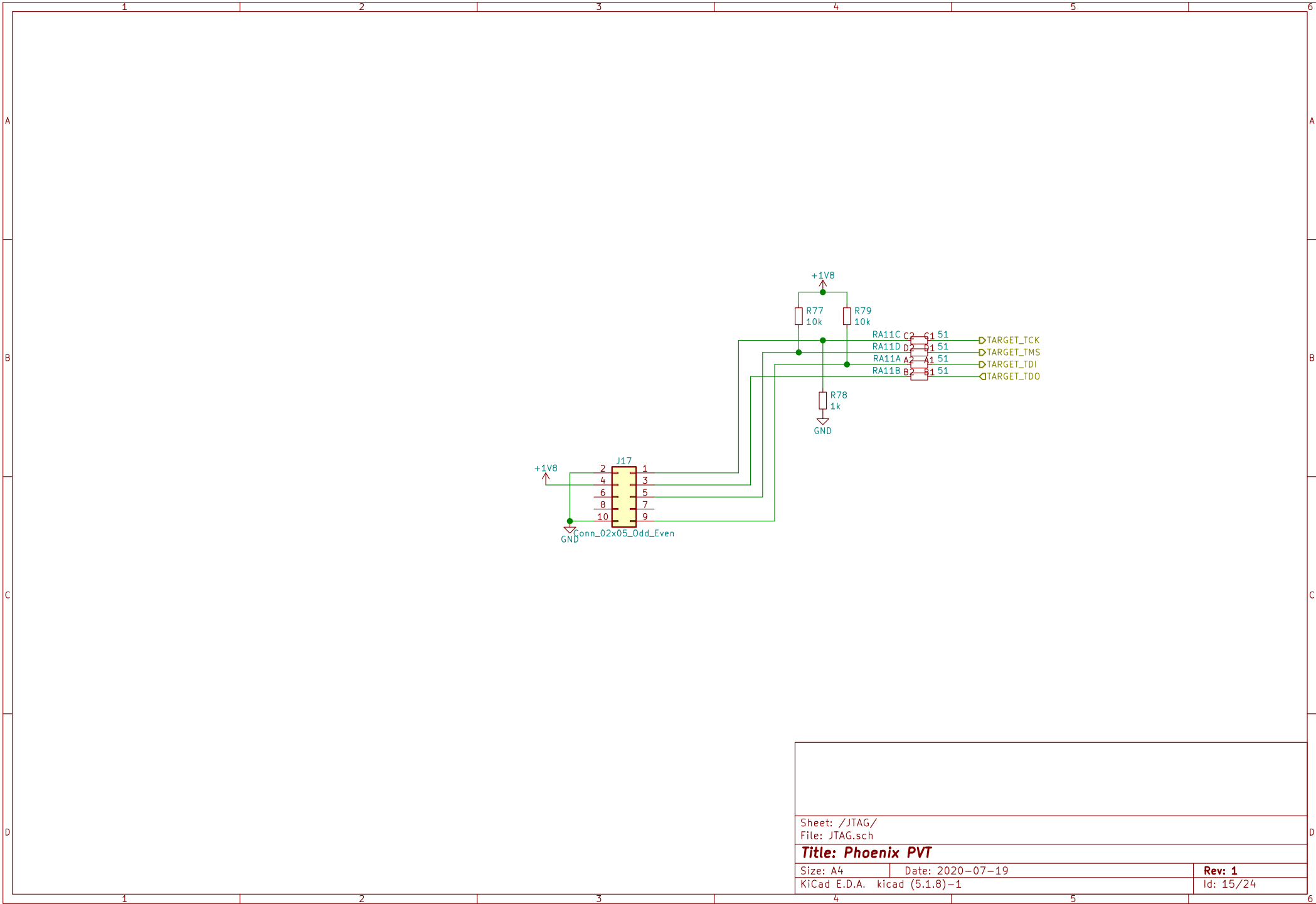
Title: Phoenix PVT

Size: A4 Date: 2020-07-19

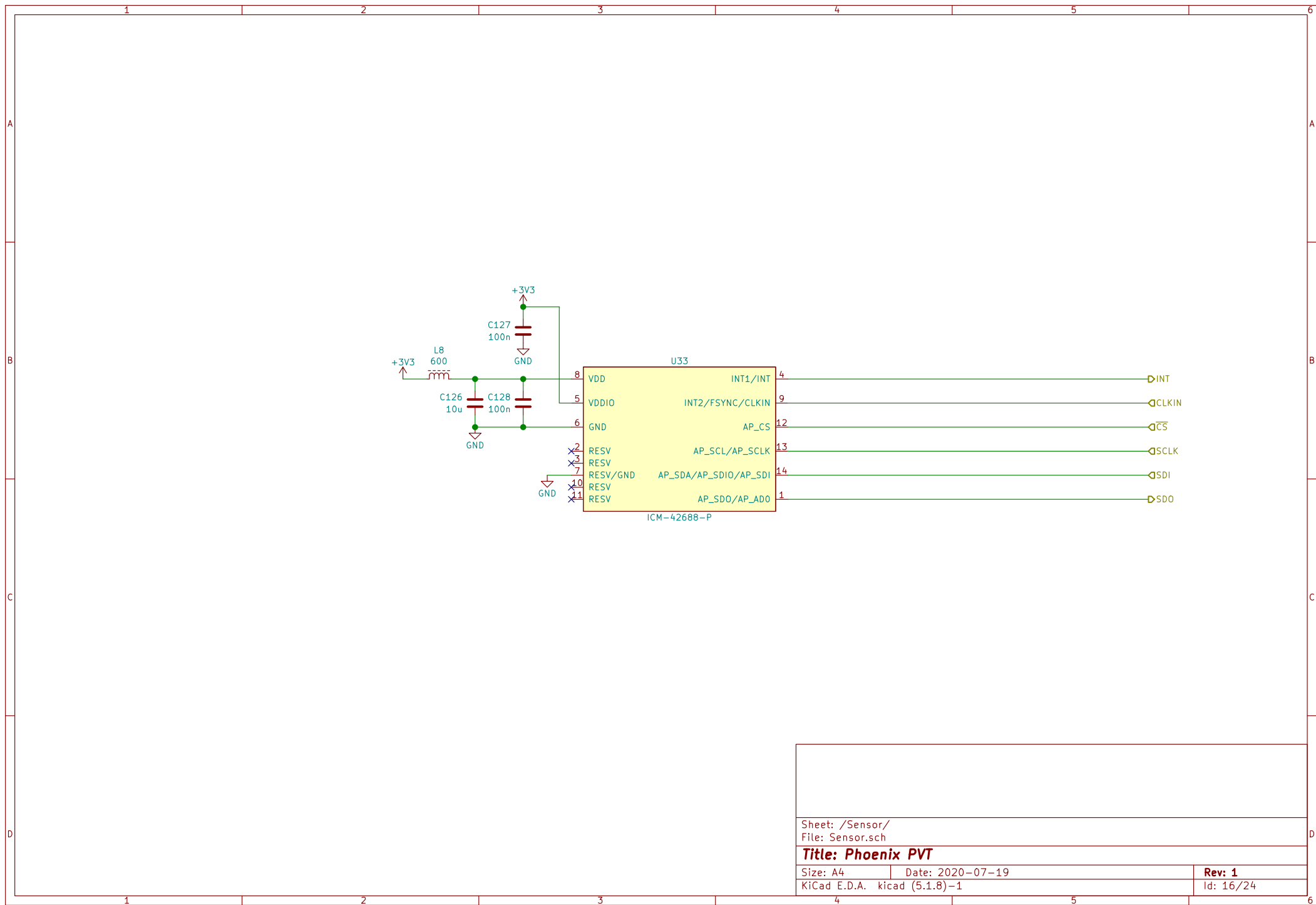
KiCad E.D.A. kicad (5.1.8)-1

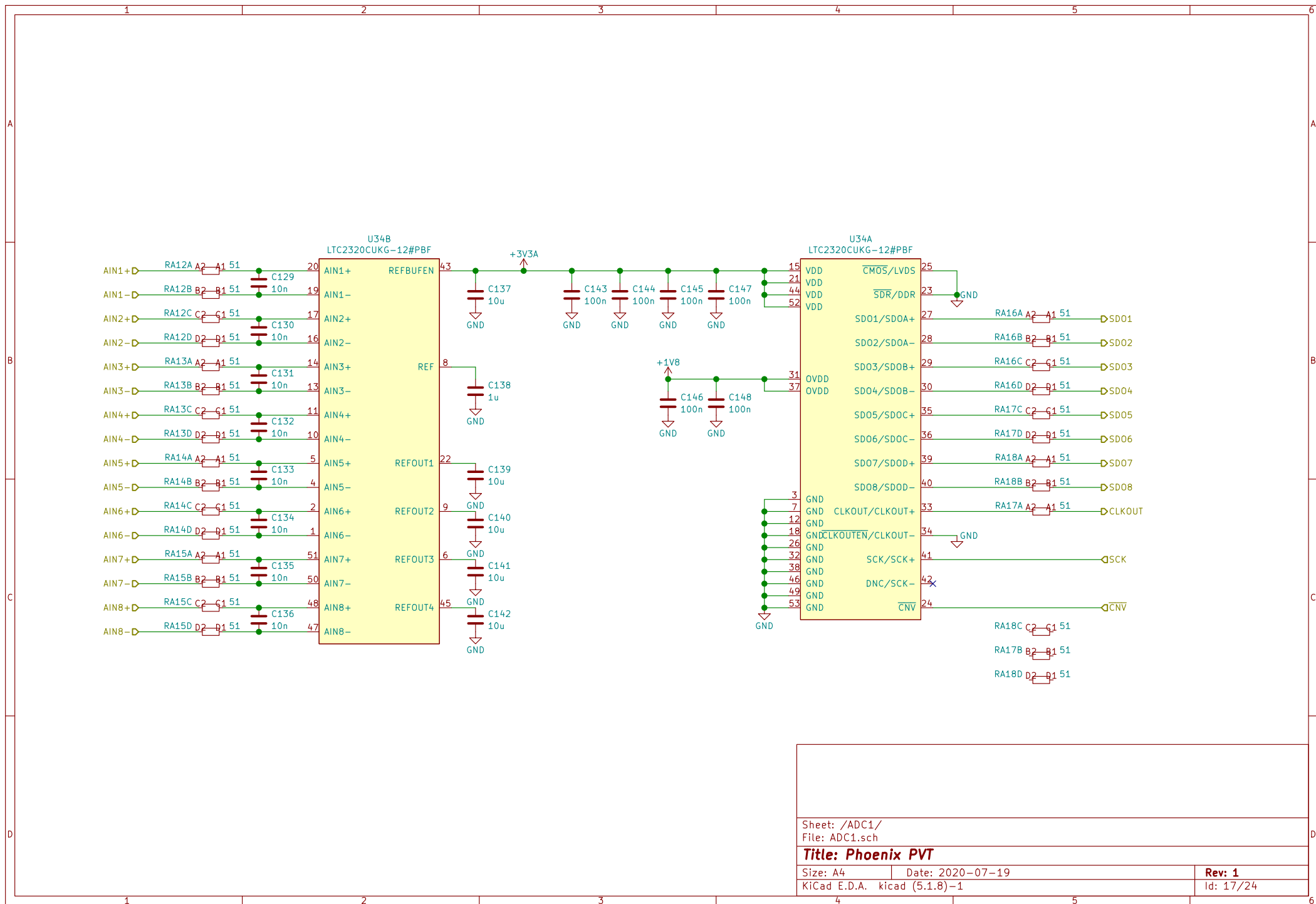
Rev: 1

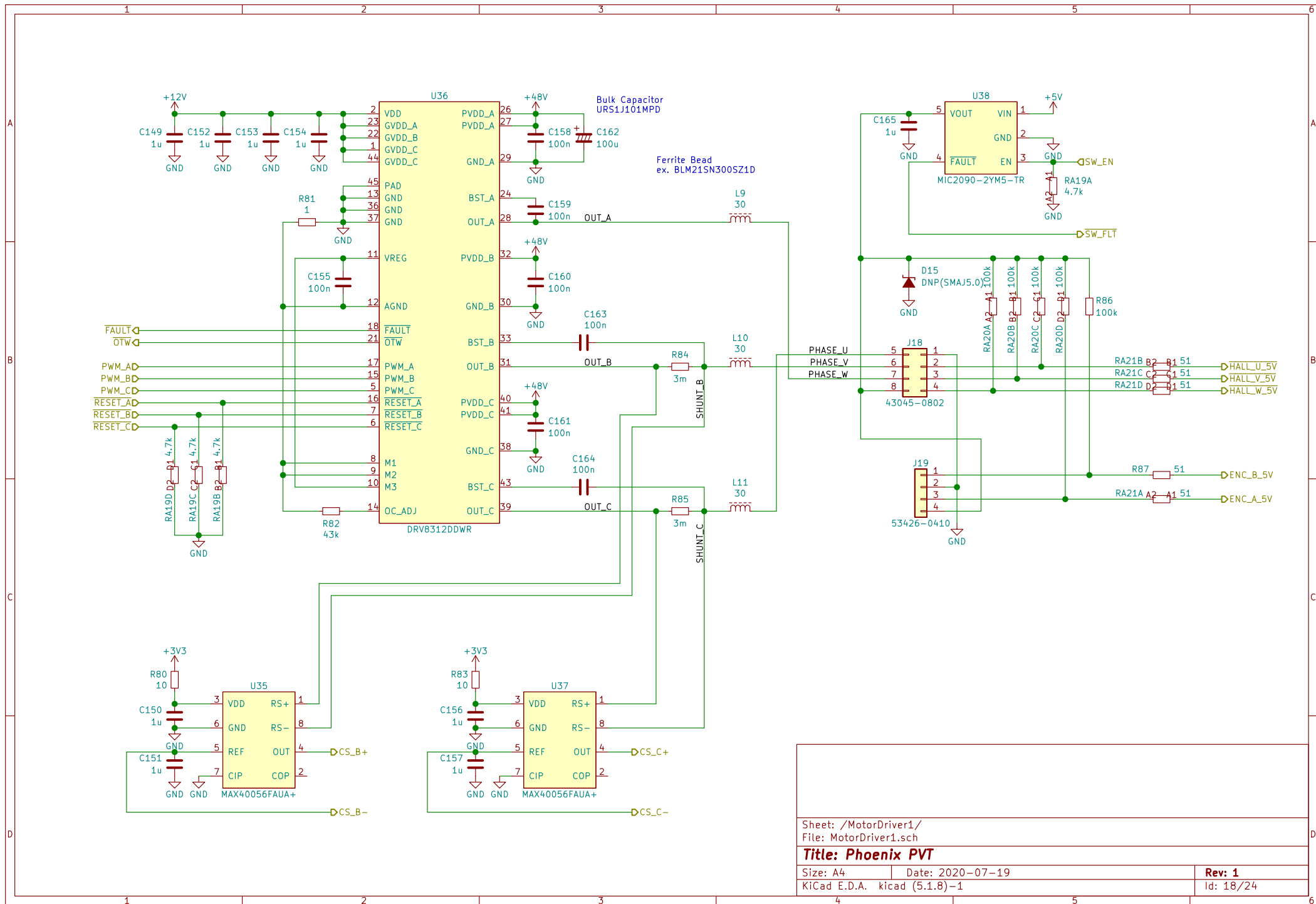
Id: 14/24

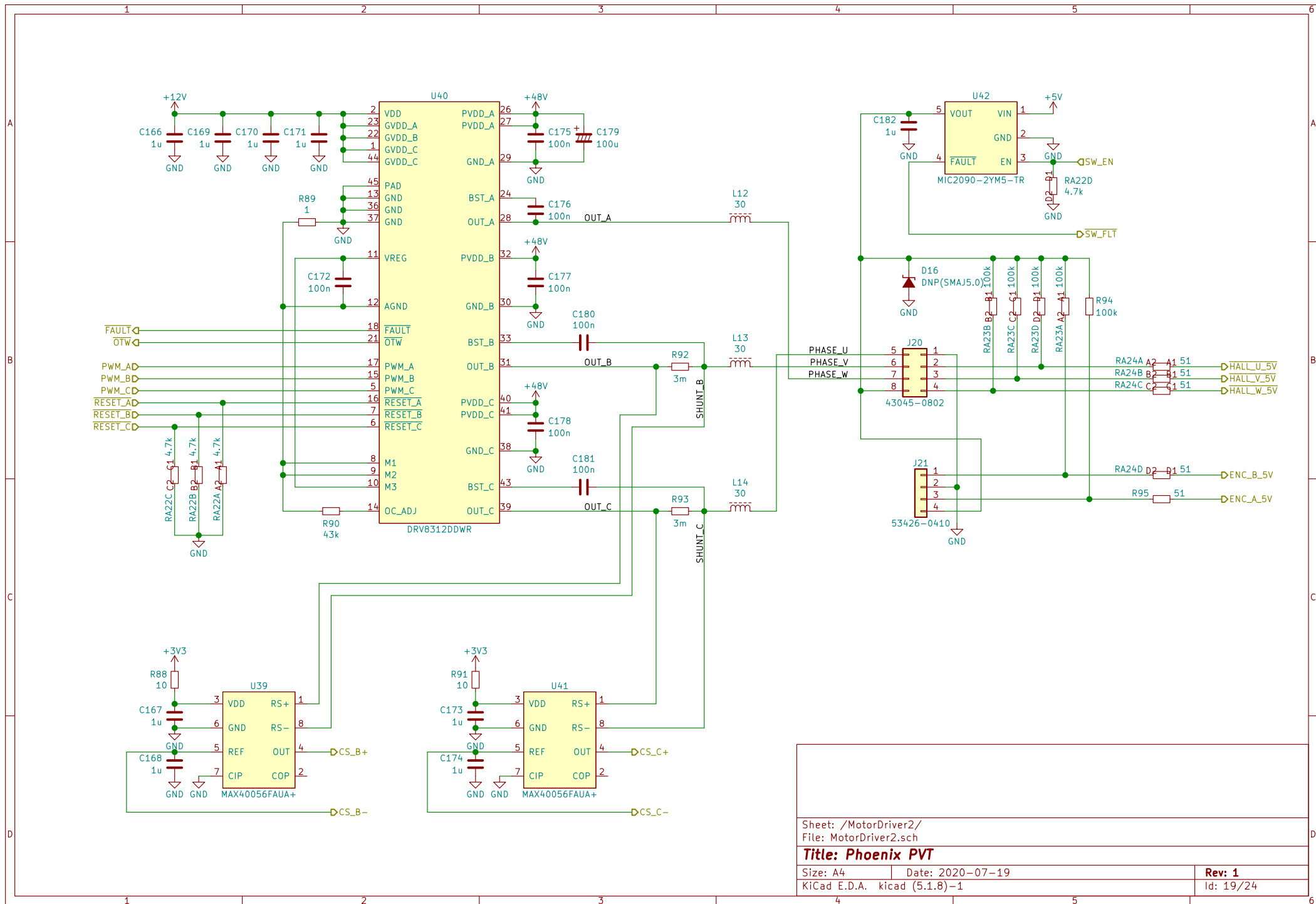


Sheet: /JTAG/ File: JTAG.sch		
Title: Phoenix PVT		
Size: A4	Date: 2020-07-19	Rev: 1
KiCad E.D.A. kicad (5.1.8)-1		Id: 15/24

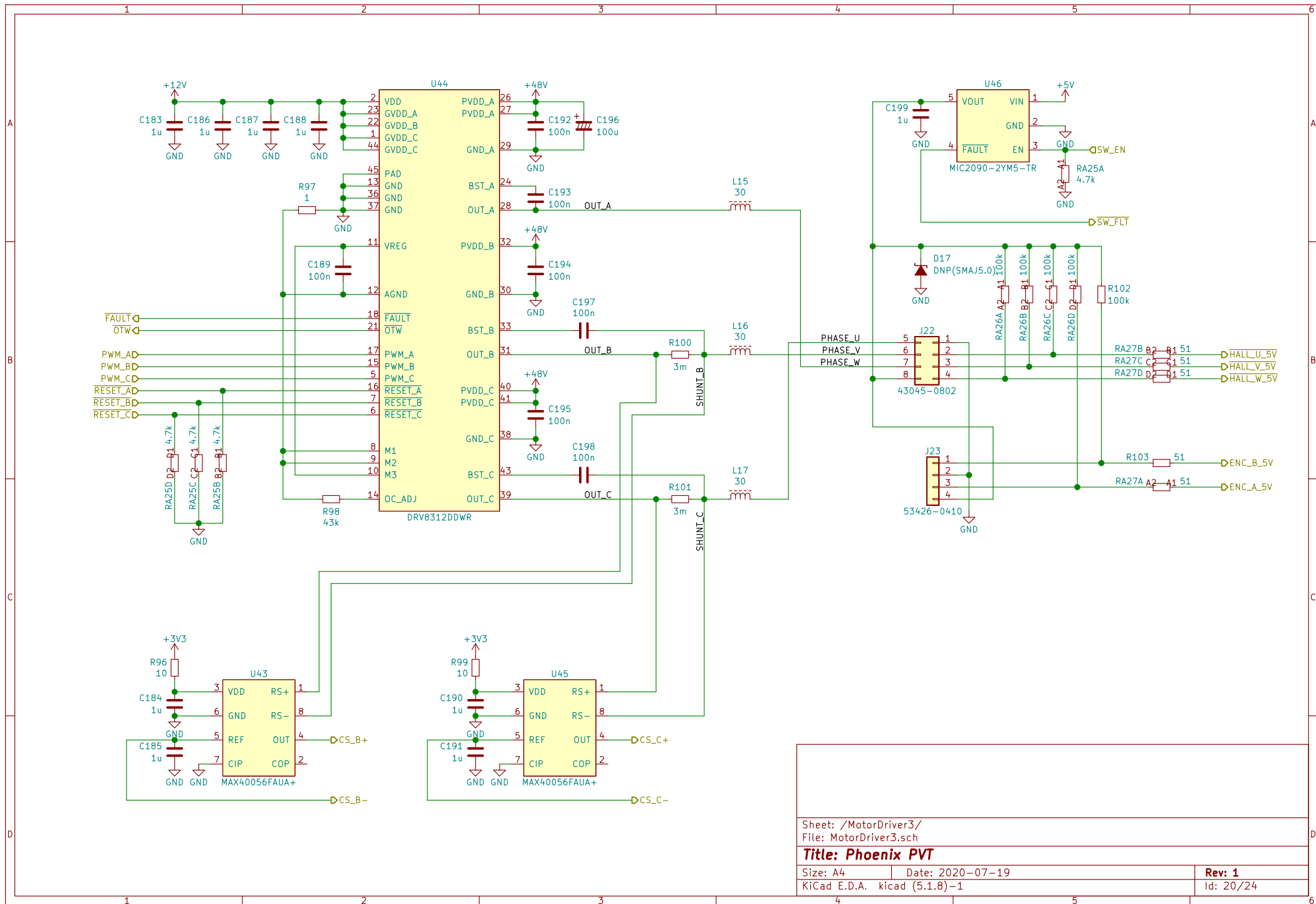




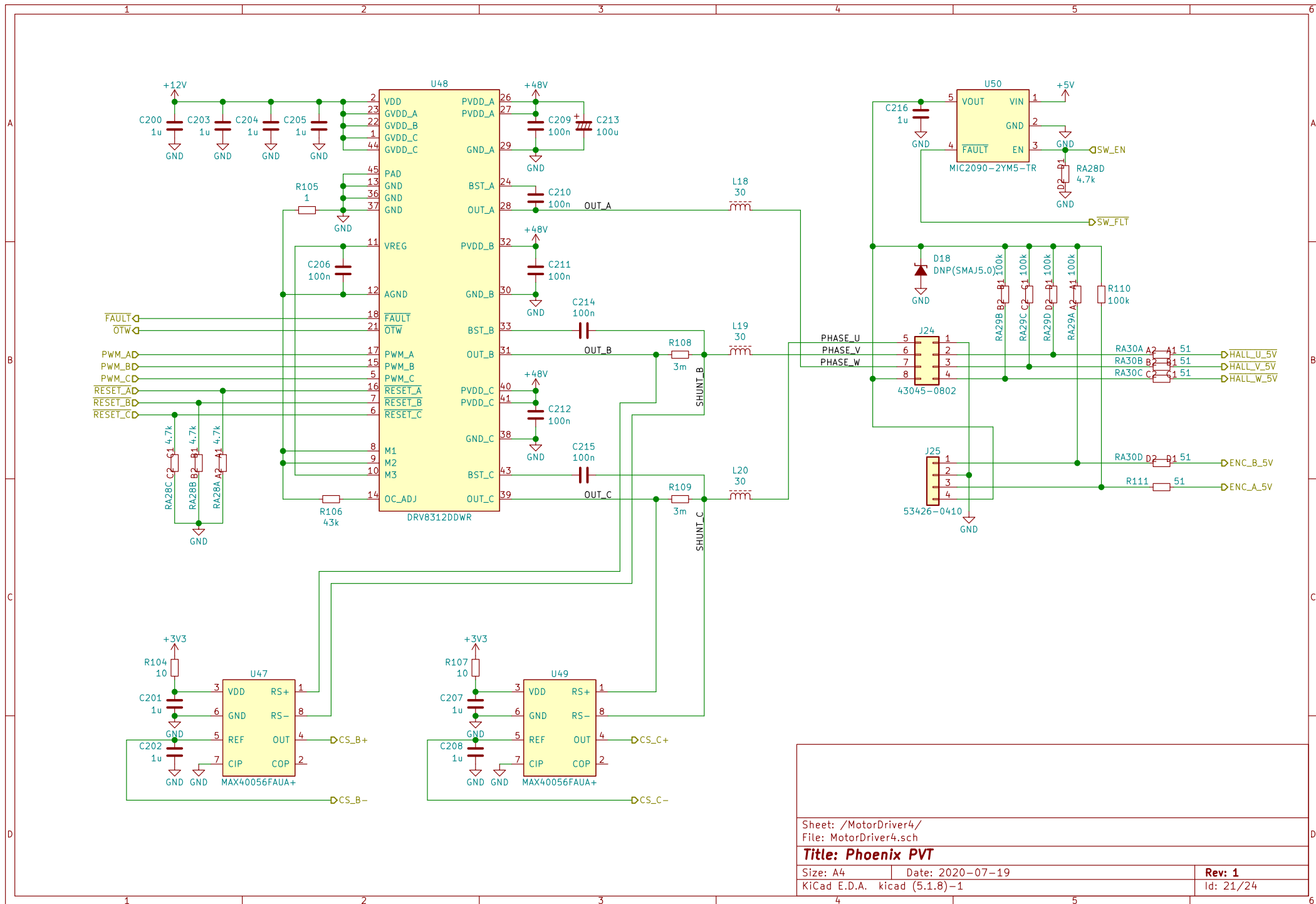




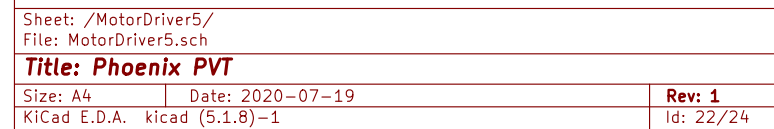
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File: MotorDriver2.sch	
Title: Phoenix PVT	
Size: A4	Date: 2020-07-19
KiCad E.D.A. kicad (5.1.8)-1	Rev: 1
	Id: 19/24

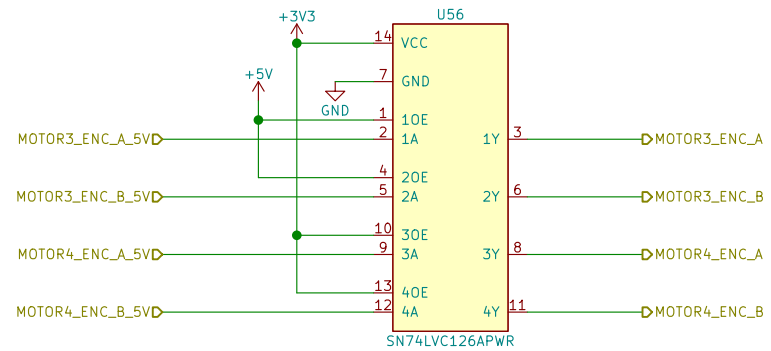
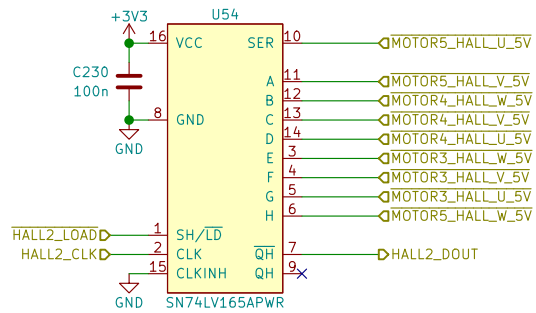
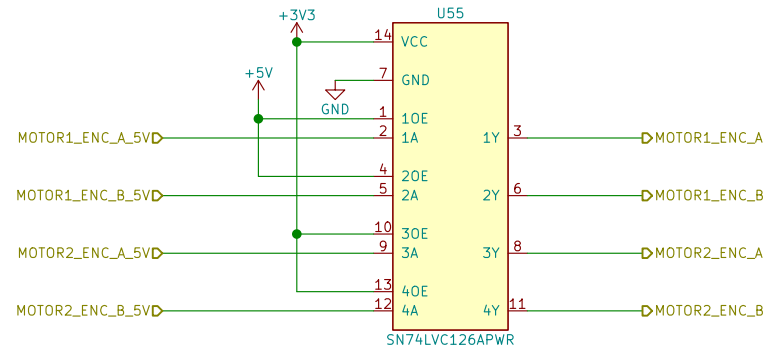
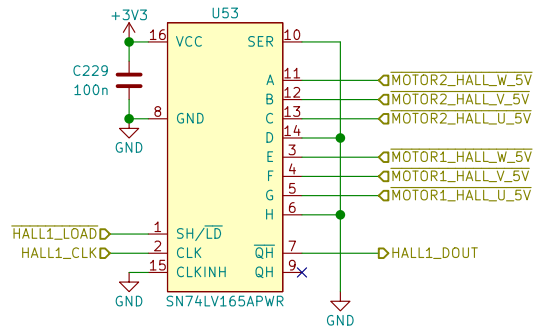


Sheet: /MotorDriver3/		
File: MotorDriver3.sch		
Title: Phoenix PVT		
Size: A4	Date: 2020-07-19	Rev: 1
KiCad E.D.A. kicad (5.1.8)-1		Id: 20/24



Sheet: /MotorDriver4/		
File: MotorDriver4.sch		
Title: Phoenix PVT		
Size: A4	Date: 2020-07-19	Rev: 1
KiCad E.D.A. kicad (5.1.8)-1		Id: 21/24





Sheet: /Interface2/
File: Interface2.sch

Title: Phoenix PVT

Size: A4 Date: 2020-07-19

KiCad E.D.A. kicad (5.1.8)-1

Rev: 1

Id: 23/24

