Modbus RTU, Modbus TCP protocol high frequency **RFID** reader control instruction document

V2.1

	Item device address	register address	Function	describe
1	40001	0X0000	reserved	
2	40002	0X0001	Configure station number	0-255 (0x00 address sends commands to all stations) Set address: 00 06 00 01 00 01 18 1B Reply command: 01 06 00 01 00 01 19 CA Query address: 00 03 00 01 00 01 D4 1B Query reply: 01 03 02 00 01 79 84
3	40003	0X0002	Configure baud rate	Store the baud rate of ModBusRTU communication of the reader/writer: Register value = 0, baud rate 115200 (default) Register value = 1, baud rate 19200 Register value = 2, baud rate 38400 Register value = 3, baud rate 57600 Register value = 4, baud rate 9600 Send data: 01 06 00 02 00 00 28 0A Receive data: [01 06 00 02 00 00 28 0A]
4	40004	0X0003	reserved	Reserved (TCP transport identifier)
5	40005	0X0004	Configure buzzer intermittent sound	The high 8bits are the period, the low 8bits are the times Send data: 01 06 00 13 05 15 BA 90 Receive data: [01 06 00 13 05 15 BA 90]
6	40006	0X0005	Configuration red light flashes	The high 8bits are the period, the low 8bits are the times Send data: 01 06 00 14 05 15 0B 51 Receive data: [01 06 00 14 05 15 0B 51]
7	40007	0X0006	Configuration green light flashes	The high 8bits are the period, the low 8bits are the times Send data: 01 06 00 15 05 15 5A 91 Receive data: [01 06 00 15 05 15 5A 91]
8	40008	0X0007	Configuration yellow light flashes	The high 8bits are the period, the low 8bits are the times Send data: 01 06 00 16 05 15 AA 91 Receive data: [01 06 00 16 05 15 AA 91]

9	40009	0X0008	switch red light	The lower 8bits are 00, closed is 01, and open Send data: 01 06 00 17 00 00 39 CE Receive data: [01 06 00 17 00 00 39 CE]
10	40010	0X0009	switch green light	The lower 8bits are 00, closed is 01, and open Send data: 01 06 00 15 05 15 5A 91 Receive data: [01 06 00 15 05 15 5A 91]
11	40011	0X000A	switch yellow light	The lower 8bits are 00, closed is 01, and open Send data: 01 06 00 16 05 15 AA 91 Receive data: [01 06 00 16 05 15 AA 91]
12	40012	0X000B	reset	System reset command Send data: 00 06 00 39 00 01 99 D6 Receive data: [01 06 00 39 00 01 98 07]
13	40013	0X000C	Switch buzzer	The switch buzzer flag bit is stored in flash and is valid after reset. 00 off 01 on Send data: 01 06 00 3A 00 01 68 07 Receive data: [01 06 00 3A 00 01 68 07]
14	40014	0X000D	Active upload switch	
	40015	0X000E	Green light prompt switch	
	40016	0X000F	reserve	
				15693 protocol large capacity difference of 8bytes per block (yellow mark)
15	40017 40018	0X0010 0X0011	Block 0	0X0010-0X0013 BLOCK0
16	40019 40020	0X0012 0X0013	Block 1	
17	40021 40022	0X0014 0X0015	Block 2	0X0014-0X0017 BLOCK1
18	40023 40024	0X0016 0X0017	Block 3	
19	40025 40026	0X0018 0X0019	Block 4	0X0018-0X001B BLOCK2
20	40027 40028	0X001A 0X001B	Block 5	
21	40029 40030	0X001C 0X001D	Block 6	0X001C-0X001F BLOCK3
22	40031 40032	0X001E 0X001F	Block 7	
23	40033 40034	0X0020 0X0021	Block 8	0X0020-0X0023 BLOCK4
24	40035 40036	0X0022 0X0023	Block 9	
25	40037 40038	0X0024 0X0025	Block 10	0X0024-0X0027 BLOCK5
26	40039 40040	0X0026 0X0027	Block 11	
27	40041 40042	0X0028 0X0029	Block 12	0X0028-0X002B BLOCK6
28	40043 40044	0X002A 0X002B	Block 13	
29	40045 40046	0X002C 0X002D	Block 14	0X002C-0X002F BLOCK7
30	40047 40048	0X002E 0X002F	Block 15	

31	40049 40050	0X0030 0X0031	Block 16	0X0030-0X0033 BLOCK8
32	40051 40052	0X0032 0X0033	Block 17	
33	40053 40054	0X0034 0X0035	Block 18	0X0034-0X0037 BLOCK9
34	40055 40056	0X0036 0X0037	Block 19	
35	40057 40050	0X0038 0X0039	Block 20	0X0038-0X003B BLOCK10
36	40059 40060	0X003A 0X003B	Block 21	
37	40061 40062	0X003C 0X003D	Block 22	0X003C-0X003F BLOCK11
38	40063 40064	0X003E 0X003F	Block 23	
39	40065 40066	0X0040 0X0041	Block 24	0X0040-0X0043 BLOCK12
40	40067 40068	0X0042 0X0043	Block 25	
41	40069 40070	0X0044 0X0045	Block 26	0X0044-0X0047 BLOCK13
42	40071 40072	0X0046 0X0047	Block 27	
43	40073 40074	0X0048 0X0049	Block 28	0X0048-0X004B BLOCK14
44	40075 40076	0X004A 0X004B	Block 29	
45	40077 40078	0X004C 0X004D	Block 30	0X004C-0X004F BLOCK15
46	40079 40080	0X004E 0X004F	Block 31	
47	40081	0X0050	15693 page turn	0X00 represents blocks 00-31 0X01 represents blocks 32-63
48	40082-40084	0X0051 0X0052 0X0053	Automatically read and upload	0X0051 Write 0XCF in the lower 8bits to indicate 15693 Write 0XA1 in the upper 8bits to upload UID 0X0052 The lower 8 bits are written to 0X00 to represent NTAG and the upper 8 bits of 15693 are written to represent the interval *75MS 0X0053 The lower 8bits represent the start block and the higher 8bits represent the end block.
49	40085-40088	0X0054-0X0057	UID	read only
50	40089-40098	0X0058-0X0061	Reserved	
51	40099-40100	0X0062-0X0063	4-byte sector number	UID read only
52	40101 40104	0X0064-0X0067	configuration key class Type and key	Key type KeyA or KeyB 6-byte key
53	401050X0068		retention	
55	40106-40113	0X0069-0X0070	Block 1data reading and writing	
56	40114-40121	0X0071-0X0078	Block 2data reading and writing	
57	40122-40129	0X0079-0X0080	Block 3data reading and writing	
58	40130-40137	0X0081-0X0088	Block 4data reading and writing	
59	40138-40140	0X0089-0X008B	Initialize wallet	0X0089 is the sector 0X008A-0X008B is the initial value of the wallet
60	40141-40143	0X008C-0X008E	Wallet recharge	0X008C is the sector 0X008D-0X008E is the wallet recharge
61	40144-40146	0X008F-0X0091	Wallet depreciation	0X008F is the sector 0X0090-0X0091 is the wallet depreciation
62	40147-40150	0X0092-0X0095	eserved	
63	40151-40154	0X0096-0X0099	NTAG UID number	
64	40155-40156	0X009A-0X009B	Key authentication	
65	40157	0X009	CNTAG page turn	
66	40158-40159	0X009D-0X009E	PAGE4	

67	40160-40161	0X009F-0X00A0	PAGE5	
68	40162-40163	0X00A1-0X00A2	PAGE6	
69	40164-40165	0X00A3-0X00A4	PAGE7	
70	40166-40167	0X00A5-0X00A6	PAGE8	
71	40168-40169	0X00A7-0X00A8	PAGE9	
72	40170-40171	0X00A9-0X00AA	PAGE10	
73	40172-40173	0X00AB-0X00AC	PAGE11	
74	40174-40175	0X00AD-0X00AE	PAGE12	
75	40176-40177	0X00AF-0X00B0	PAGE13	
76	40178-40179	0X00B1-0X00B2	PAGE14	
77	40180-40181	0X00B3-0X00B4	PAGE15	
78	40182-40183	0X00B5-0X00B6	PAGE16	
79	40184-40185	0X00B7-0X00B8	PAGE17	
80	40186-40187	0X00B9-0X00BA	PAGE18	
81	40188-40189	0X00BB-0X00BC	PAGE19	
82	40190-40191	0X00BD-0X00BE	PAGE20	
83	40192-40193	0X00BF-0X00C0	PAGE21	
84	40194-40195	0X00C1-0X00C2	PAGE22	
85	40196-40197	0X00C3-0X00C4	PAGE23	
86	40198-40199	0X00C5-0X00C6	PAGE24	
87	40200-40201	0X00C7-0X00C8	PAGE25	
88	40202-40203	0X00C9-0X00CA	PAGE26	
89	40204-40205	0X00CB-0X00CC	PAGE27	
90	40206-40207	0X00CD-0X00CE	PAGE28	
91	40208-40209	0X00CF-0X00D0	PAGE29	
92	40210-40211	0X00D1-0X00D2	PAGE30	
93	40212-40213	0X00D3-0X00D4	PAGE31	
94	40214-40215	0X00D5-0X00D6	PAGE32	
95	40216-40217	0X00D7-0X00D8	PAGE33	
96	40218-40219	0X00D9-0X00DA	PAGE34	
97	40220-40221	0X00DB-0X00DC	PAGE35	
98	40222-40223	0X00DD-0X00DE	PAGE36	
99	40224-40225	0X00DF-0X00E0	PAGE37	
100	40226-40227	0X00E1-0X00E2	PAGE38	
101	40228-40229	0X00E3-0X00E4	PAGE39	

15693 Automatically read PLC to get data register address

15693 protocol large capacity difference of 8bytes per block (yellow mark)

102	40273-40274	0X0110 0X0111	Block 0	0X0110-0X0113 BLOCK0
103	40275-40276	0X0112 0X0113	Block 1	
104	40277-40278	0X0114 0X0115	Block 2	0X0114-0X0117 BLOCK1

105	40279-40280	0X0116 0X0117	Block 3	
106.	40281-40282	0X0118 0X0119	Block 4	0X0118-0X011B BLOCK2
107	40283-40284	0X011A 0X011B	Block 5	
108	40285-40286	0X011C 0X011D	Block 6	0X011C-0X011F BLOCK3
109	40287-40288	0X011E 0X011F	Block 7	
110	40289-40300	0X0120 0X0121	Block 8	0X0120-0X0123 BLOCK4
111	40291-40292	0X0122 0X0123	Block 9	
112	40293-40294	0X0124 0X0125	Block 10	0X0024-0X0027 BLOCK5
113	40295-40296	0X0126 0X0127	Block 11	
114	40297-40298	0X0128 0X0129	Block 12	0X0128-0X012B BLOCK6
115	40299-40300	0X012A 0X012B	Block 13	
116	40301-40302	0X012C 0X012D	Block 14	0X012C-0X012F BLOCK7
117	40303-40304	0X012E 0X012F	Block 15	
118	40305-40306	0X0130 0X0131	Block 16	0X0130-0X0133 BLOCK8
119	40307-40308	0X0132 0X0133	Block 17	
120	40309-40300	0X0134 0X0135	Block 18	0X0134-0X0137 BLOCK9
121	40311-40312	0X0136 0X0137	Block 19	
122	40313-40314	0X0138 0X0139	Block 20	0X0138-0X013B BLOCK10
123	40315-40316	0X013A 0X013B	Block 21	
124	40317-40318	0X013C 0X013D	Block 22	0X013C-0X013F BLOCK11
125	40319-40320	0X013E 0X013F	Block 23	
126	40321-40322	0X0140 0X0141	Block 24	0X0140-0X0143 BLOCK12
127	40323-40324	0X0142 0X0143	Block 25	
128	40325-40326	0X0144 0X0145	Block 26	0X0144-0X0147 BLOCK13
129	40327-40328	0X0146 0X0147	Block 27	
130	40329-40330	0X0148 0X0149	Block 28	0X0148-0X014B BLOCK14
131	40331-40332	0X014A 0X014B	Block 29	
132	40333-40334	0X014C 0X014D	Block 30	0X014C-0X014F BLOCK15
133	40335-40336	0X014E 0X014F	Block 31	

14443M1 card automatically reads PLC to obtain data register address

134	40362-40369	0X0169-0X0170	Block 1 data reading and writing
135	40370-40377	0X0171-0X0178	Block 2 data reading and writing
136	40378-40385	0X0179-0X0180	Block 3 data reading and writing
137	40386-40387	0X0181-0X0188	Block 4 data reading and writing