Noah G. Dorfman

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Objective

I am Computer Engineering student pursuing an Aerospace Engineering minor, experienced in embedded systems, circuit development, and software design, seeking an internship or co-op for the Spring or Summer.

Education

Georgia Institute of Technology | Atlanta, GA   *August 2018 – Present*
Bachelor of Science in Computer Engineering, GPA: 3.31   Expected Graduation, May 2022
Minor in Aerospace Engineering

Rowan University | Glassboro, NJ   *August 2016 – May 2018*
Dual-Enrollment Transfer with 30 Credit Hours, GPA: 3.56

Relevant Experience

Software Integration and Testing Engineer (Intern)   *Summer 2019*
Lockheed Martin RMS (Moorestown, NJ) / AEGIS Radar MMSP Group   *40 hours per week*
- Tested software changes for complex, signal processing architectures using BASH and C++
- Designed an Arduino-based radar and interceptor-missile system for a supplemental intern group project

Solar Racing Powertrain Sub-Team (Team-Based)   *Fall 2018 - Present*
Georgia Tech Solar Racing / VIP GTSR Research Group   *15 hours per week*
- Responsible for all powertrain systems, from our custom driver controls to the Marand motors
- Designed a TIVA breakout / GPIO board for prototyping and testing of solar car electronics
- Designed a new CAN bus junction board to efficiently route the power train CAN line
- Built a dashboard box from Divinycell foam core and Kevlar polycarbonate to organize all low voltage systems

Solar Racing Race Operations Lead / Executive Board Member   *Summer 2019 - Present*
Georgia Tech Solar Racing   *10 hours per week*
- Preparing the team for participation in annual Formula Sun race competitions
- Organizing mock races, holding debriefs, and developing race strategies using custom simulation tools
- Involved in weekly high-level team discussion, decision making, and problem solving

LEGV8 Processor Design Project (Small Team-Based)   *Spring 2018*
Computer Architecture Course (Rowan University)   *10 hours per week*
- Designed a functional processor using a subset of ARM processor instructions to run on a DE0 FPGA Board
- Implemented the data path, control unit, and all individual components (memory, program counter, ALU, etc.)
- Delegated individual task assignments to 3 team members for completion of the processor data sheet

Escape Room South Jersey | Glassboro, NJ | Collingswood, NJ   *June 2017 – August 2018*
Electronic Puzzle Designer and Engineer   *15 hours per week*
- Optimized the host position's tasks by automating room functions for easier control
- Developed unique puzzles with Arduino and electronics for 5 room attractions

Skills

Programming: Python, C++, C, ARM and MIPS Assembly, JavaScript, HTML, CSS, VHDL
Platforms: Windows, Mac, Linux
Software: MS Office, Unity, Altera Quartus II, Altium Circuit Maker, GitHub, IBM Change Management Suite
Hardware: Arduino, Raspberry Pi, DE0 FPGA Board, Soldering, Benchtop Electronic Equipment
Communication: Technical Reports, Datasheets, Schematics, Presentations