

# NOAH GEORGE

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**OBJECTIVE** Seeking full-time employment as an electrical engineer, ideally in the field of FPGA Development

**EDUCATION** **Iowa State University** – Ames, IA  
Expected Bachelor of Science in Electrical Engineering  
May 2022 GPA: 3.86

**PROFESSIONAL WORK EXPERIENCE** **Collins Aerospace** – Cedar Rapids, IA  
*FPGA Internship - Remote*  
June to August 2020

- Developed several VHDL blocks using project requirements
- Created software testbenches to verify the operation of several blocks
- Integrated design in hardware to verify operation
- Reported project progress to Agile team via video conferencing

June to August 2019 **Collins Aerospace** – Cedar Rapids, IA  
*Engineering Intern*

- Developed empty architectures files that reduced simulation time
- Modified register files to reflect changes made in hardware
- Added pre-written VHDL blocks to top level design
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**SELECT ACADEMIC PROJECTS** **Undergraduate Research** – Ames, IA  
*Dr. Peschel*

- Used computer vision to identify and count hogs
- Used python and OpenCV library

**Arduino Projects**

- Arduino port of the game Catch Phrase
- Temperature module to display room temp

**RELEVANT SKILLS** **Hardware:** Oscilloscopes, Integration Testing, Soldering  
**Software Applications and Languages:** C, Java, VHDL, python, ModelSim, Vivado, Matlab, Microsoft Office, SPICE, Linux

**HONORS, LEADERSHIP & ACTIVITIES** ISU Deans List '18 – '20  
Tau Beta Pi General Member  
ISU Cardinals Award & ISU Presidents Scholarship