

```

timescale 1ns / 1ps
/*****
* Module: rx_top
*
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* Class: ECEN 220, Section 3, Fall 2020 - ECEN 220, Section 1, Winter 2020 * Date: 2
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*
* Description: Sets the buttons and leds on the board to the inputs and outputs of
the uart receiver
*
*
*****/
`default_nettype none

module rx_top(
    input wire logic clk, reset, ser_in, Received,
    output logic parityErr, Receive,
    output logic[7:0] rxData
);

    rx RX1(.clk(clk), .Reset(reset), .Sin(ser_in), .Received(Received),
.Receive(Receive), .parityErr(parityErr), .Dout(rxData));
endmodule

```