```
restart
# run for 100ns so the FDCE can properly reset
run 100 ns

# set inputs low
add_force CLR 0
add_force INC 0

# add oscillating clock input with 10ns period
add_force CLK {0 0} {1 5ns} -repeat_every 10ns

# run 3 cycles before loading anything
run 30 ns

# load a 0
add_force CLR 1
run 20ns
add_force CLR 0

# change DIN and run some time
# notice that the register doesn't
# load this new value because
# the load signal is low
add_force INC 1
run 20ns

# now let's load the register
add_force CLR 1
run 10ns
add_force CLR 0
add_force INC 0
run 10ns

# now we will apply various
# data input values and watch
# the register load them
# on succeeding clock edges
add_force INC 1
run 10ns
add_force CLR 1
run 10ns
add_force INC 0
run 10ns
run 10ns
run 10ns
add_force INC 1
```

```
run 10ns
run 10ns
add_force INC 0

add_force INC 1
add_force CLR 0
run 200ns
```