

**Inputs:** S, M1, D**Outputs:** L, A**Pre-Lab 2**

S	M1	D	L	A
0	0	0	0	0
0	0	1	0	0
0	1	0	1	0
0	1	1	0	0
1	0	0	1	0
1	0	1	1	0
1	1	0	1	1
1	1	1	1	0

