

SANTA CLARA UNIVERSITY	ELEN 115 – Spring 2023	S. Krishnan
Homework #9		

1. Figure 1 shows a multistage amplifier in DC operation with the transistors having V_{EBON} or $V_{BEON} = 0.7\text{ V}$, $\beta = 100$, $V_{CEsat} = 0.2\text{ V}$ and $V_T = 25\text{ mV}$.

(a) Find the value of voltages V_1 , V_2 , V_3 , V_4 and V_5 and currents I_1 , I_2 , I_3 and I_4 .

Clearly show all the steps of your work.

(b) Justify any assumptions you make.

