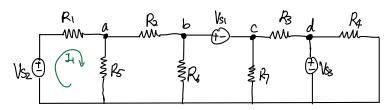
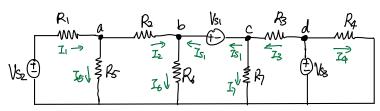
Mesh Method: set up arbitrary currents and strictly follow the defined current directions in each loop.

* when seeing a voltage source, if the defined current enters the voltage source from the negative terminal. the voltage will be deemed to be "negative". For example, in Loop 1. It enters the voltage source from its negative terminal. Therefore, we should write $-Vs_1+I_1R_1+(I_1-I_2)R_5=0$



Nodal Analysis Method: set up KCL/KVL equations at each mode.



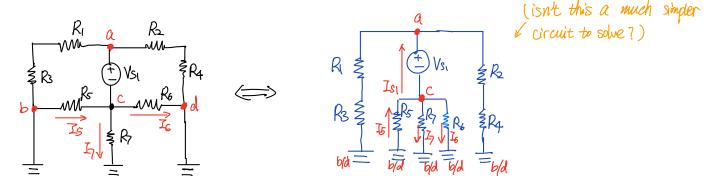
- . When it gets to the voltage sources Vs. and Vs., it's impossible to apply Chm's law to find their currents.
- · Assume Vs. Current as Is, , Vs. current as Is3 as placeholders to solve for current
- · It would be safe to assume the placeholding currents one coming out from the voltage sources.
- · If still confused, go read the definitions of KCL and KVL. I believe the cononical definition of KCL is only an one-line statement.

Superposition: assume all sources were turned off and then turn one source at a time to solve for currents and voltages due to that source. Circuit simplification can most likely be performed.

Consider 2 basic questions:

- a) if I turn off a voltage source (V_s = something \rightarrow V_s =0), what does the circuit look like?
- b) if I turn off a current source (Is = something \rightarrow Zs =0), what does the circuit look like?

As an example, I just feel like turning off Usz and Vsz for the reason of ... because I can. Here is what it leads to:



(I've answered question (A) implicitly for you and you are welcome)

Note the directions of currents! You have to stick with the same set of directions to make the superposition work across each activated source. Otherwise, when you add up the results, it'd be garbage.