

8051 Instructions

1. Data Transfer Instructions (7)

MOV

MOVC

MOVB

PUSH

POP

XCH

XCHD

2. Arithmetic Instructions (8)

ADD

ADDC

SUBB

INC

DEC

MUL

DIV

DA

3. Logical Instructions (10)

ANL

ORL

XRL

CLR

CPL

RL

RLC

RR

RRC

SWAP

4. Bit manipulation & Branching Instructions (23)

CLR

SETB

CPL

ANL

ORL

MOV

JC

JNC

JB

JNB

JBC

ACALL

LCALL

RET

RETI

AJUMP

LJUMP

SJUMP

JMP

JZ

JNZ

CJNE

DJNZ

8051 Flag Bits & PSW Register

CY	AC	F0	RS1	RS0	OV	---	P
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CY = Carry

AC = Auxiliary Carry

**F0 = Available to user for general
purpose**

RS1, RS0 = Register Bank Sector Bits

OV = Overflow

P = Parity