

LIST OF PUBLICATIONS

- [1] K. Rosvall, T. Mohammadat, G. Ungureanu, J. berg, and I. Sander, "Exploring power and throughput for dataflow applications on predictable noc multiprocessors," in *2018 21st Euromicro Conference on Digital System Design (DSD)*, Aug 2018, pp. 719–726. [Online]. Available: <https://ieeexplore.ieee.org/document/8491891>
- [2] F. Robino and J. berg, "The heartbeat model: A platform abstraction enabling fast prototyping of real-time applications on noc-based mpsoc on fpga," in *2013 8th International Workshop on Reconfigurable and Communication-Centric Systems-on-Chip (ReCoSoC)*, July 2013, pp. 1–8. [Online]. Available: <https://ieeexplore.ieee.org/document/6581536>
- [3] J. Öberg, F. Robino, H. Attarzadeh, and I. Sander, "NoC System Generator : a Tool for Fast Prototyping of Multi-Core Systems on FPGAs," p. 2012, 2012. [Online]. Available: <https://people.kth.se/~frobino/data/DATE13/date13.pdf>
- [4] F. Robino and J. . Berg, "From simulink to noc-based mpsoc on fpga," in *2014 Design, Automation Test in Europe Conference Exhibition (DATE)*, March 2014, pp. 1–4. [Online]. Available: <https://ieeexplore.ieee.org/document/6800542>
- [5] J. Öberg and F. Robino, "A noc system generator for the sea-of-cores era," in *Proceedings of the 8th FPGAWorld Conference*, ser. FPGAWorld '11. New York, NY, USA: ACM, 2011, pp. 4:1–4:6. [Online]. Available: <http://doi.acm.org/10.1145/2157871.2157875>