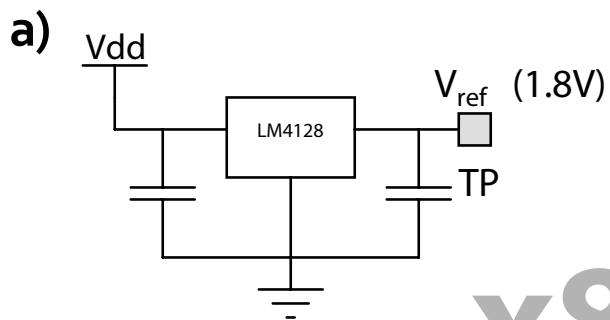
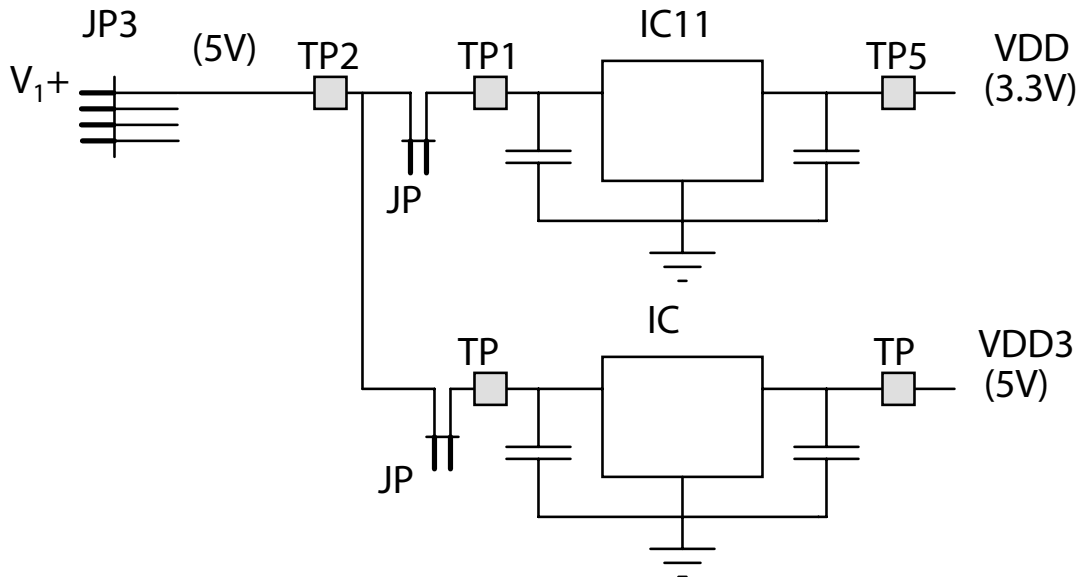


Esquemáticos

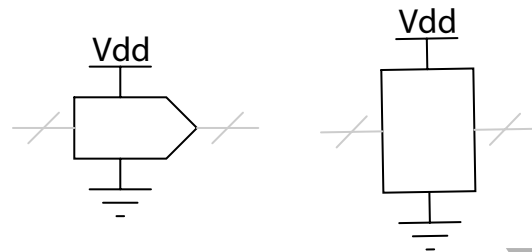
Alimentación:

1. Vdd



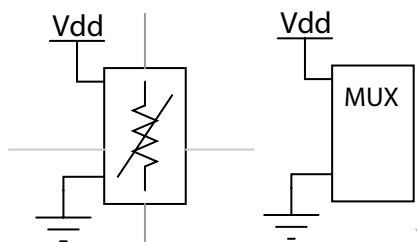
x8

b)



x1

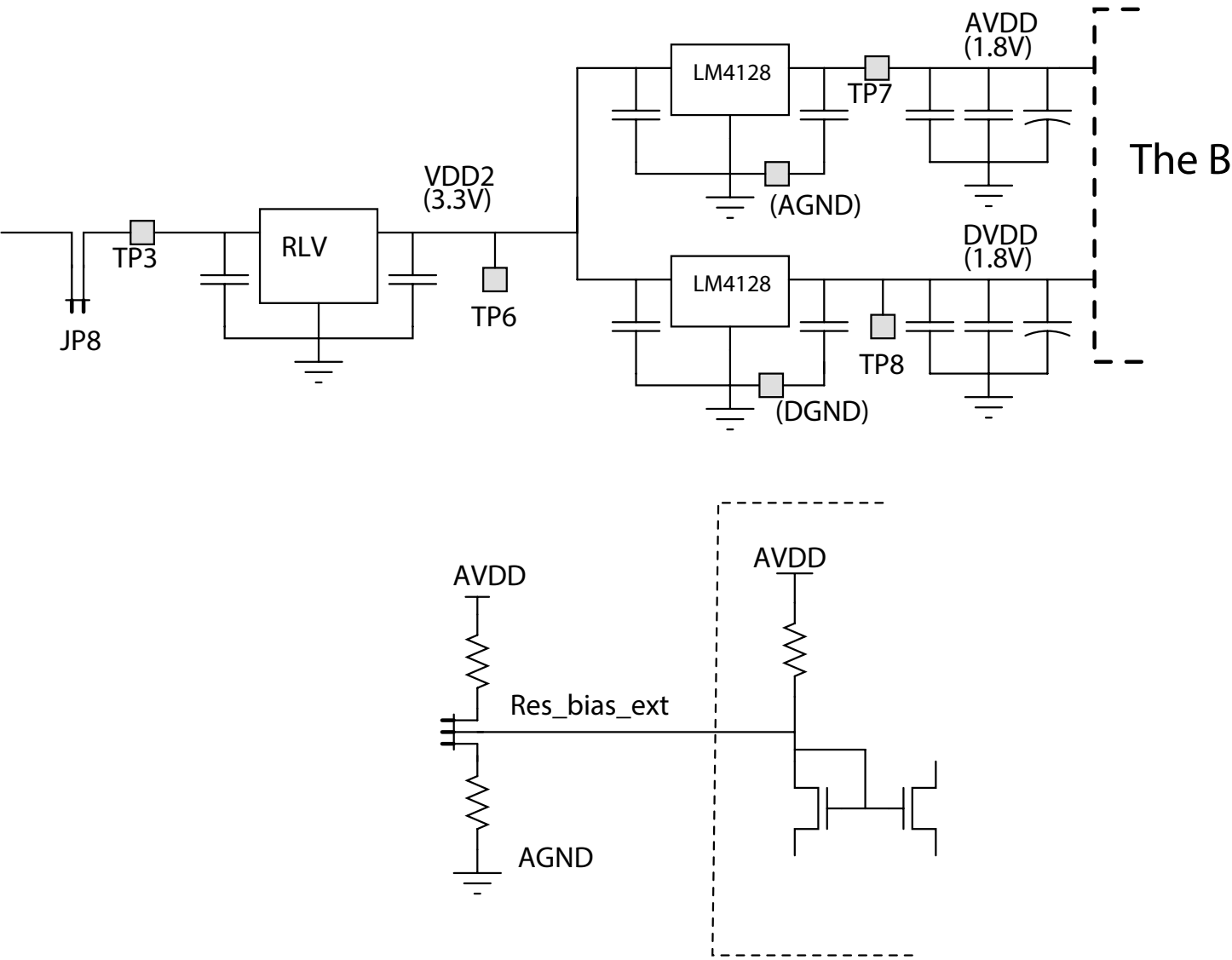
c)



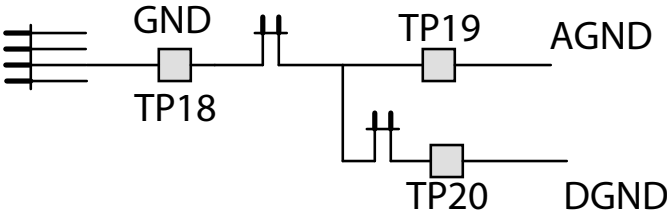
x3

d)

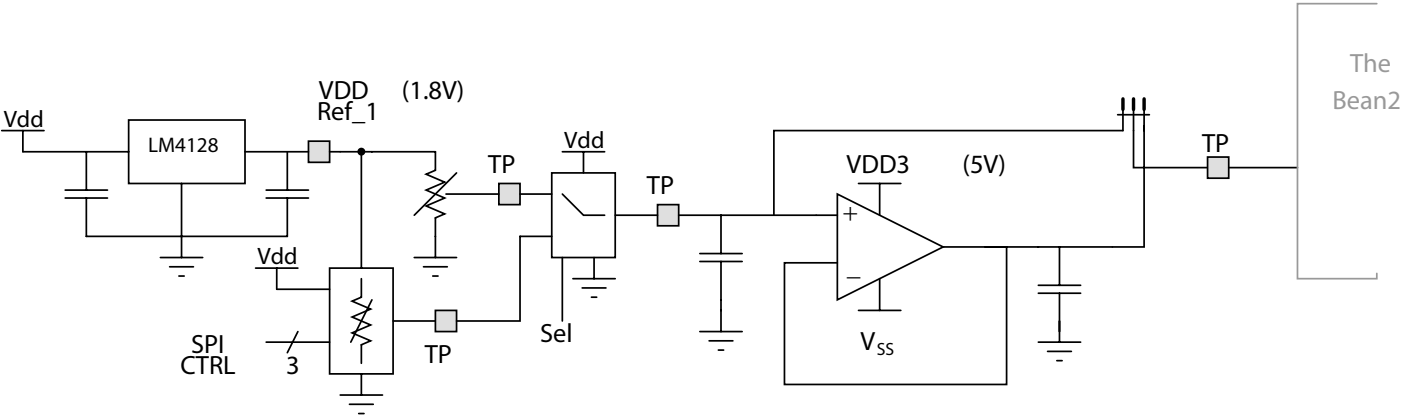
2. Vdd del chip



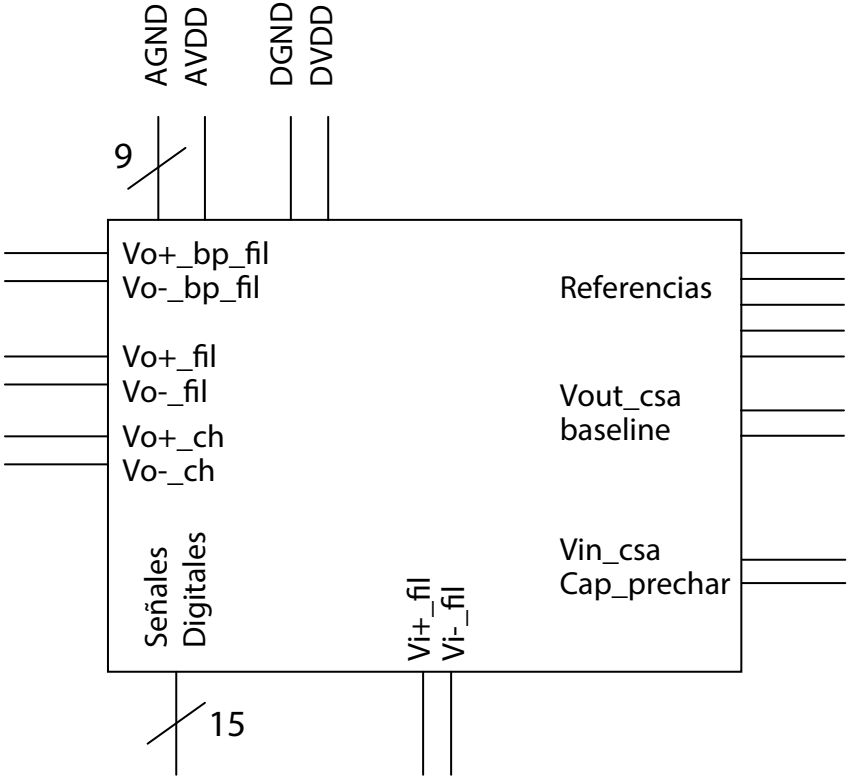
3. Tierras



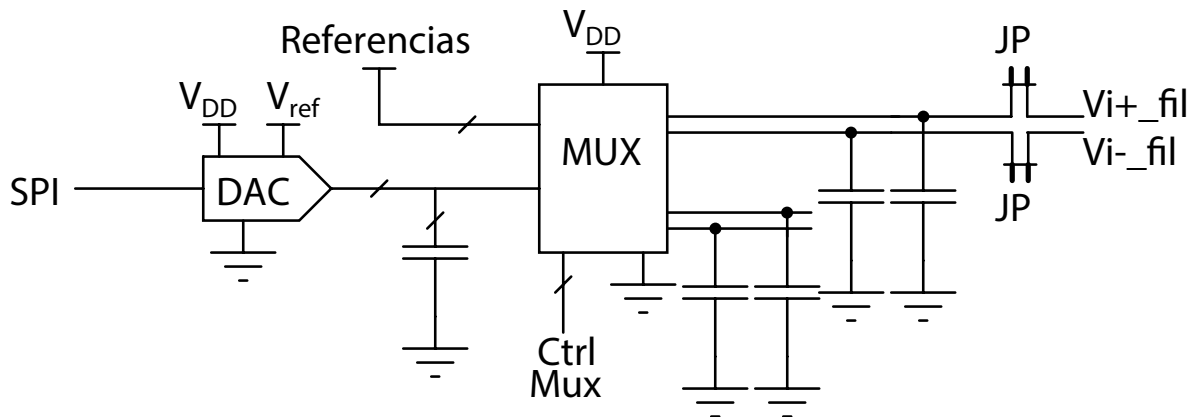
4. Referencias



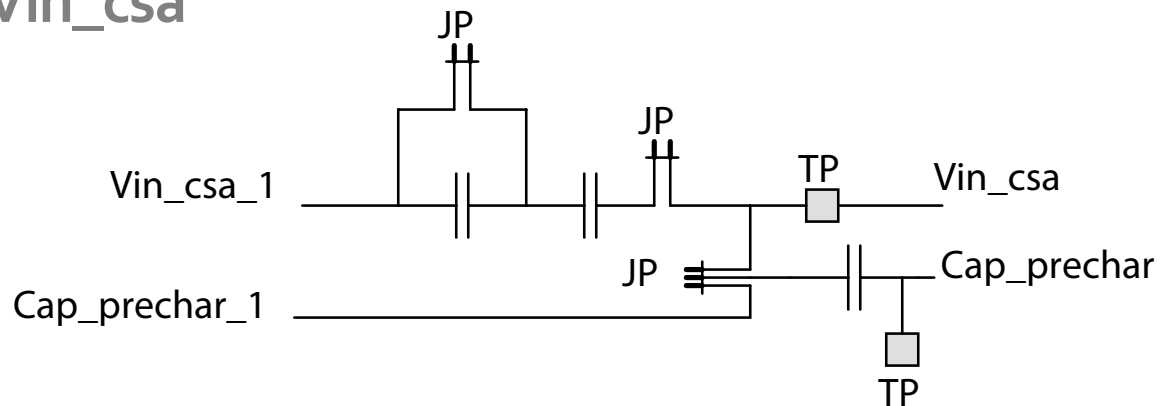
The Bean 2



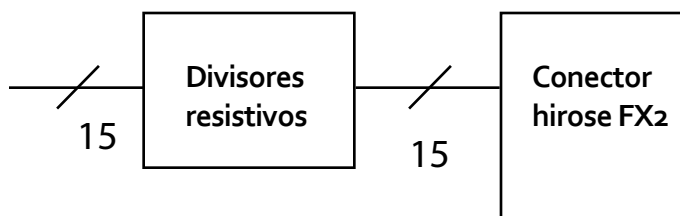
DAC



Vin_csa



Señales digitales



ADC

