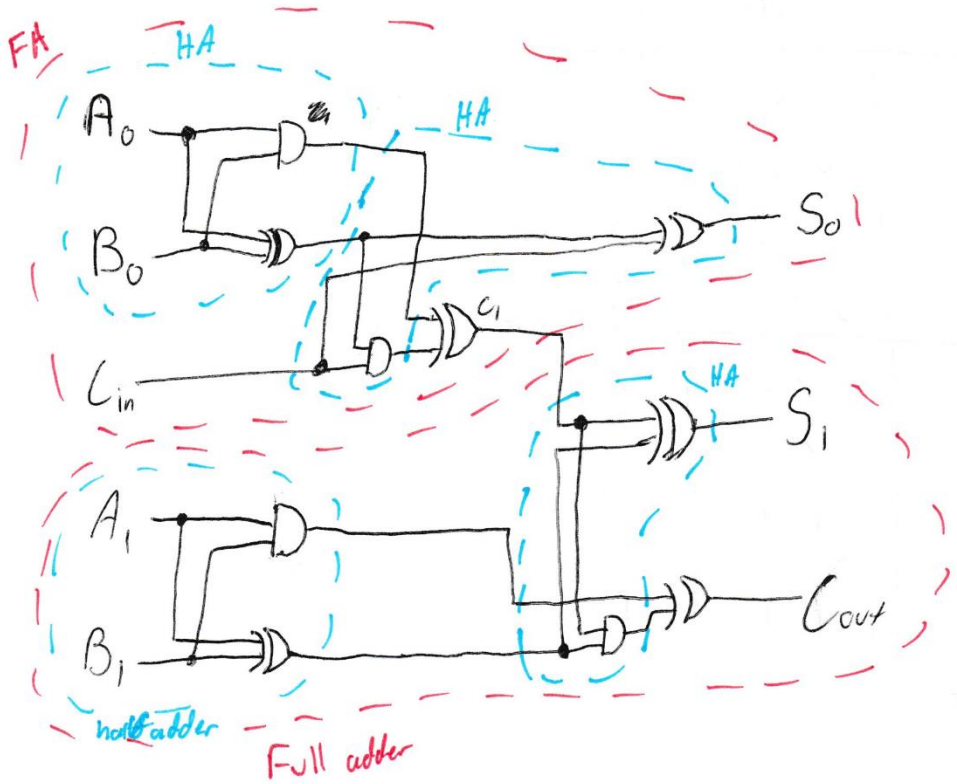
A_1A_0 B_1B_0 C_{in} $\rightarrow C_{out}S_1S_0$



2-bit	3-bit	Dec
0 0	0 0 0	0
0 1	0 0 1	1
	0 1 0	2
	0 1 1	3
	1 0 0	-4
	1 0 1	-3
1 0	1 1 0	-2
1 1	1 1 1	-1