

Module: R5: RV-fpga**Section: C Programming Task: Mirror Switches to LEDs****Assessment 2.3****Mirror Switches to LEDs**

➤ Mirror Switches to LEDs:

- **Description:** This program mirrors the value of the switches on the LEDs. For instance, if the switches are set to 1010 0001, the LEDs display 1000 0101.

- **Code Explanation:**

- 1. Main Function:**

Two variables are declared: en is set to 0xFFFF to enable all GPIO pins, and inp_val is declared to store the input value from the switches. Another variable, output, is declared to store the mirrored value.

- 2. Infinite Loop:**

The program enters an infinite loop where it continuously reads the input value from the switches. The input value is then shifted 16 bits to the right to isolate the relevant bits.

The reverse function is called to mirror the bits of the shifted input value. The mirrored value is then written to the LEDs.

- 3. Reverse Function:**

The reverse function takes a 16-bit integer and returns its bitwise mirror. The function initializes a variable to store the reversed value. It iterates 16 times, once for each bit in the 16-bit integer.

During each iteration, it shifts the reversed value left by one bit and copies the least significant bit of the input number to the reversed value.

The input number is then shifted right by one bit. After all iterations, the function returns the mirrored value.

- 4. Terminal Output:**

Use the following command in source folder.

```
make flash
```

make watch

```

xe-user106@noman-10xengineers: ~/Baseline_RVfpga
xe-user106@noman-10xengineers: ~/Ba...  xe-user106@noman-10xengineers: ~/Ba...
PACKAGES:
- framework-wd-riscv-sdk @ 0.0.0-alpha+sha.ca4b2392d8
- tool-openocd-riscv-chipsalliance @ 1.1100.211104+sha.857b5cec1b
- tool-verilator-swervolf @ 0.0.201130
- toolchain-riscv @ 1.80300.190927 (8.3.0)
LDF: Library Dependency Finder -> https://bit.ly/configure-pio-ldf
LDF Modes: Finder ~ chain, Compatibility ~ soft
Found 2 compatible libraries
Scanning dependencies...
No dependencies
Building in release mode
Compiling .pio/build/flash/src/others.o
Linking .pio/build/flash/firmware.elf
Generating disassembly
riscv64-unknown-elf-objdump -d ".pio/build/flash/firmware.elf" > ".pio/build/flash/firmware.dis"
Checking size .pio/build/flash/firmware.elf
Advanced Memory Usage is available via "PlatformIO Home > Project Inspect"
RAM: [ ] 1.0% (used 12308 bytes from 1216512 bytes)
Flash: [ ] 0.0% (used 464 bytes from 16777216 bytes)
Building .pio/build/flash/firmware.bin
Building .pio/build/flash/firmware.vh
Running Program in RVfpga-VidBo or RVfpga-PipelineSimulator

```

Visit this link to visualize Virtual Board:

<http://localhost:8000/nexys-a7.html>

