**Module: SV for Verification**

**Section:** Testbench Basics **Task:** Interface

**Task 1**

Creating Driver & Module tb

* **What is the key difference between the execution of $strobe and $display?**

The key difference between $display and $strobe in SystemVerilog lies in **when** they capture and print values:

* **$display**:
  + Prints values **immediately** when executed.
  + Captures the **current** values of variables.
  + If variables change later in the same time step, $display won’t reflect those updates.
* **$strobe**:
  + Prints values at the **end of the current time step**, after all events are processed.
  + Captures the **final** values of variables at that time.
  + Useful for ensuring you get the stabilized values after all changes.

In short, **$display** gives the immediate value, while **$strobe** ensures you get the final value at the end of a time step.

* **What is the key difference between the execution of $strobe and $display?**

The key difference between $strobe and $monitor in SystemVerilog:

* **$strobe**:
  + Prints **once** at the **end of the current time step** after all events are processed.
  + Captures the **final values** of variables at that time.
* **$monitor**:
  + Continuously prints whenever any of the monitored variables **change**.
  + Runs throughout the simulation, automatically printing updated values.

In short, **$strobe** provides a one-time snapshot at the end of a time step, while **$monitor** continuously tracks and prints variable changes during the entire simulation.