

Module: UVM-1

Section: UVM Concepts Task: TLM

TLM

Task 3

➤ Task 1:

```

@Log  <Share
analysis_port uvm_analysis_port - @358
comp_b        component_b      - @368
analysis_imp  uvm_analysis_imp  - @377
-----
UVM_INFO component_a.sv(25) @ 0: uvm_test_top.comp_a [component_a] transaction randomized
UVM_INFO component_a.sv(26) @ 0: uvm_test_top.comp_a [component_a] Printing trans,
-----
Name      Type      Size  Value
-----
trans     transaction -    @395
addr      integral    4      'h9
wr_rd     integral    1      'h0
wdata     integral    8      'hf5
-----

UVM_INFO component_a.sv(28) @ 0: uvm_test_top.comp_a [component_a] Before calling port write method
UVM_INFO component_b.sv(20) @ 0: uvm_test_top.comp_b [component_b] Inside write method. Received trans on Analysis Imp Port
UVM_INFO component_b.sv(21) @ 0: uvm_test_top.comp_b [component_b] Printing trans,
-----
Name      Type      Size  Value
-----
trans     transaction -    @395
addr      integral    4      'h9
wr_rd     integral    1      'h0
wdata     integral    8      'hf5
-----

UVM_INFO component_a.sv(30) @ 0: uvm_test_top.comp_a [component_a] After calling port write method
UVM_INFO /apps/vcsmx/vcs/U-2023.03-SP2/etc/uvmm-1.2/src/base/uvmm_objection.svh(1276) @ 0: reporter [TEST_DONE] 'run' phase is ready to proceed to the 'extract' phase
UVM_INFO /apps/vcsmx/vcs/U-2023.03-SP2/etc/uvmm-1.2/src/base/uvmm_report_server.svh(904) @ 0: reporter [UVM/REPORT/SERVER]
--- UVM Report Summary ---

```

➤ Task 2:

```

UVM_INFO component_a.sv(25) @ 0: uvm_test_top.comp_a [component_a] transaction randomized
UVM_INFO component_a.sv(26) @ 0: uvm_test_top.comp_a [component_a] Printing trans,
-----
Name      Type      Size  Value
-----
trans     transaction -    @405
addr      integral    4      'h9
wr_rd     integral    1      'h0
wdata     integral    8      'hf5
-----

UVM_INFO component_a.sv(28) @ 0: uvm_test_top.comp_a [component_a] Before calling port write method
UVM_INFO component_b.sv(28) @ 0: uvm_test_top.comp_b [component_b] Inside write_port_a method. Received trans on Analysis Imp Port
UVM_INFO component_b.sv(29) @ 0: uvm_test_top.comp_b [component_b] Printing trans,
-----
Name      Type      Size  Value
-----
trans     transaction -    @405
addr      integral    4      'h9
wr_rd     integral    1      'h0
wdata     integral    8      'hf5
-----

UVM_INFO component_b.sv(33) @ 0: uvm_test_top.comp_b [component_b] Inside write_port_b method. Received trans on Analysis Imp Port
UVM_INFO component_b.sv(34) @ 0: uvm_test_top.comp_b [component_b] Printing trans,
-----
Name      Type      Size  Value
-----
trans     transaction -    @405
addr      integral    4      'h9
wr_rd     integral    1      'h0
wdata     integral    8      'hf5
-----

UVM_INFO component_a.sv(30) @ 0: uvm_test_top.comp_a [component_a] After calling port write method
UVM_INFO /apps/vcsmx/vcs/U-2023.03-SP2/etc/uvmm-1.2/src/base/uvmm_objection.svh(1276) @ 0: reporter [TEST_DONE] 'run' phase is ready to proceed to the 'extract' phase
UVM_INFO /apps/vcsmx/vcs/U-2023.03-SP2/etc/uvmm-1.2/src/base/uvmm_report_server.svh(904) @ 0: reporter [UVM/REPORT/SERVER]
--- UVM Report Summary ---

```

➤ Task 3:

```
UVM_INFO component_a.sv(28) @ 0: uvm_test_top.comp_a [component_a] Before calling port write method
UVM_INFO component_b.sv(28) @ 0: uvm_test_top.comp_b0 [component_b] Inside write_port_a method. Received trans on Analysis Imp Port
UVM_INFO component_b.sv(29) @ 0: uvm_test_top.comp_b0 [component_b] Printing trans,
```

```
-----
Name      Type      Size  Value
-----
trans     transaction -    @434
  addr    integral   4     'h9
  wr_rd   integral   1     'h0
  wdata   integral   8     'hf5
-----
```

```
UVM_INFO component_b.sv(33) @ 0: uvm_test_top.comp_b0 [component_b] Inside write_port_b method. Received trans on Analysis Imp Port
UVM_INFO component_b.sv(34) @ 0: uvm_test_top.comp_b0 [component_b] Printing trans,
```

```
-----
Name      Type      Size  Value
-----
trans     transaction -    @434
  addr    integral   4     'h9
  wr_rd   integral   1     'h0
  wdata   integral   8     'hf5
-----
```

```
UVM_INFO component_b.sv(28) @ 0: uvm_test_top.comp_b1 [component_b] Inside write_port_a method. Received trans on Analysis Imp Port
UVM_INFO component_b.sv(29) @ 0: uvm_test_top.comp_b1 [component_b] Printing trans,
```

```
-----
Name      Type      Size  Value
-----
trans     transaction -    @434
  addr    integral   4     'h9
  wr_rd   integral   1     'h0
  wdata   integral   8     'hf5
-----
```

```
UVM_INFO component_b.sv(33) @ 0: uvm_test_top.comp_b1 [component_b] Inside write_port_b method. Received trans on Analysis Imp Port
UVM_INFO component_b.sv(34) @ 0: uvm_test_top.comp_b1 [component_b] Printing trans,
```

```
-----
Name      Type      Size  Value
-----
trans     transaction -    @434
  addr    integral   4     'h9
  wr_rd   integral   1     'h0
  wdata   integral   8     'hf5
-----
```

➤ Task 4:

```

-----
UVM_INFO component_a.sv(25) @ 0: uvm_test_top.comp_a [component_a] transaction randomized
UVM_INFO component_a.sv(26) @ 0: uvm_test_top.comp_a [component_a] Printing trans,
-----
Name      Type      Size  Value
-----
trans     transaction -    @444
  addr    integral   4    'h9
  wr_rd    integral   1    'h0
  wdata    integral   8    'hf5
-----

UVM_INFO component_a.sv(28) @ 0: uvm_test_top.comp_a [component_a] Before calling port write method
UVM_INFO component_a.sv(30) @ 0: uvm_test_top.comp_a [component_a] After calling port write method
UVM_INFO component_b.sv(23) @ 0: uvm_test_top.comp_b [component_b] Received Transaction from Analysis_FIFO
-----
Name      Type      Size  Value
-----
trans     transaction -    @444
  addr    integral   4    'h9
  wr_rd    integral   1    'h0
  wdata    integral   8    'hf5
-----

UVM_INFO /apps/vcsmx/vcs/U-2023.03-SP2//etc/uvm-1.2/src/base/uvm_objection.svh(1276) @ 0: reporter [TEST_DONE] 'run' phase is ready to proceed to the 'extract' phase
UVM_INFO /apps/vcsmx/vcs/U-2023.03-SP2//etc/uvm-1.2/src/base/uvm_report_server.svh(904) @ 0: reporter [UVM/REPORT/SERVER]
--- UVM Report Summary ---

** Report counts by severity
UVM_INFO : 8
UVM_WARNING : 0
UVM_ERROR : 0
UVM_FATAL : 0
** Report counts by id
[RNTST] 1
[TEST_DONE] 1
[UVM/RELNOTES] 1
[component_a] 4
[component_b] 1
-----
#finish called from #11: /apps/vcsmx/vcs/U-2023.03-SP2//etc/uvm-1.2/src/base/uvm_report_server.svh(904)

```