

Module: UVM-2 Register Abstraction Layer

RAL

Assignment 05

➤ **Write() method:**

```
regmodel.temp_reg_inst.write(status, 4'h4)
```

In this call, the purpose of each argument is given as following:

1. **status**: Retrieves the outcome of the write operation.
2. **4'h4**: The 4-bit value (4) written to the register `temp_reg_inst`.

All other arguments use their defaults:

- **Access Path (UVM_DEFAULT_PATH)**: Uses the default path.
- **Map (null)**: Relies on the register's default map.
- **Parent Sequence (null)**: No parent sequence is specified.
- **Priority (-1)**: Default priority is used.
- **Extension, Filename, and Line Number**: Not provided.

This call writes the value 4 to the register and captures the write operation's status.