**Module: UVM-2**

**Register Abstraction Layer**

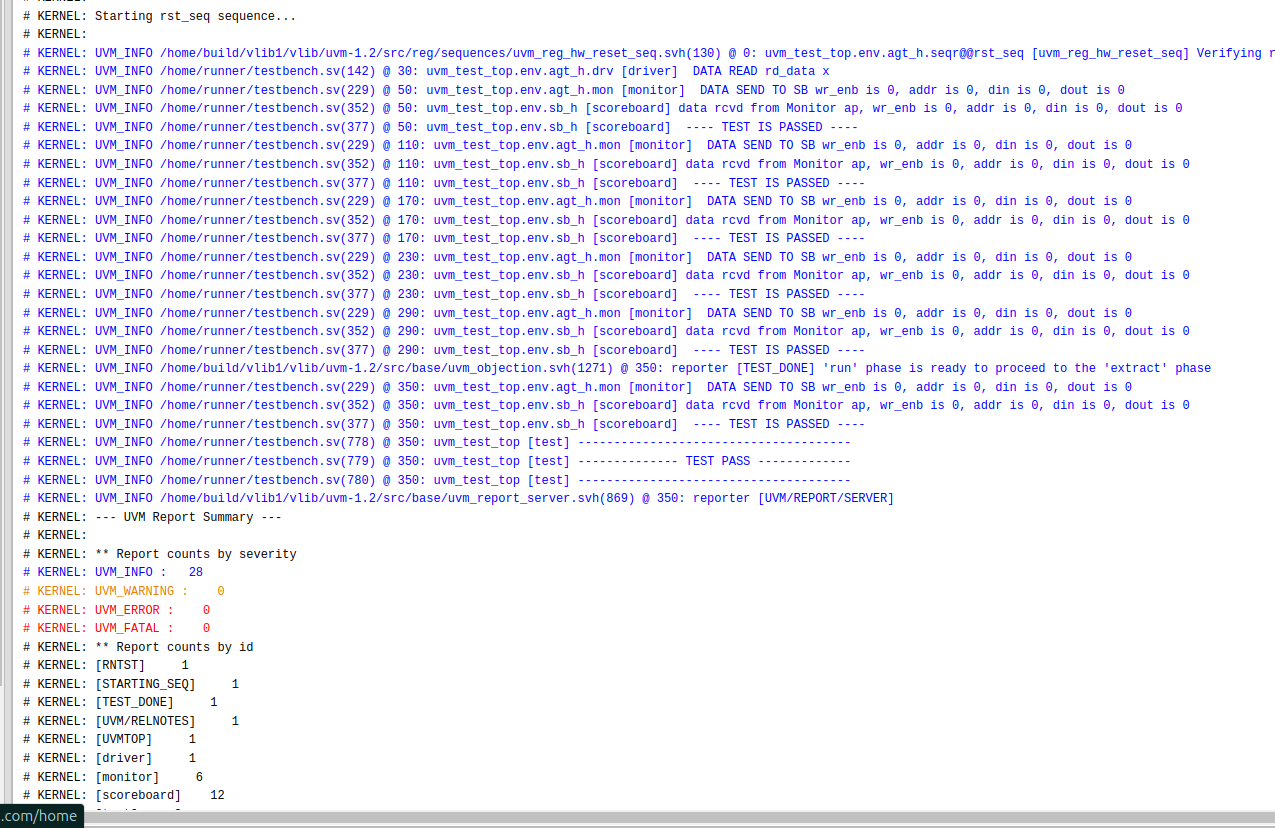
**RAL**

UVM RAL - [EDA Link](https://www.edaplayground.com/x/UURE)

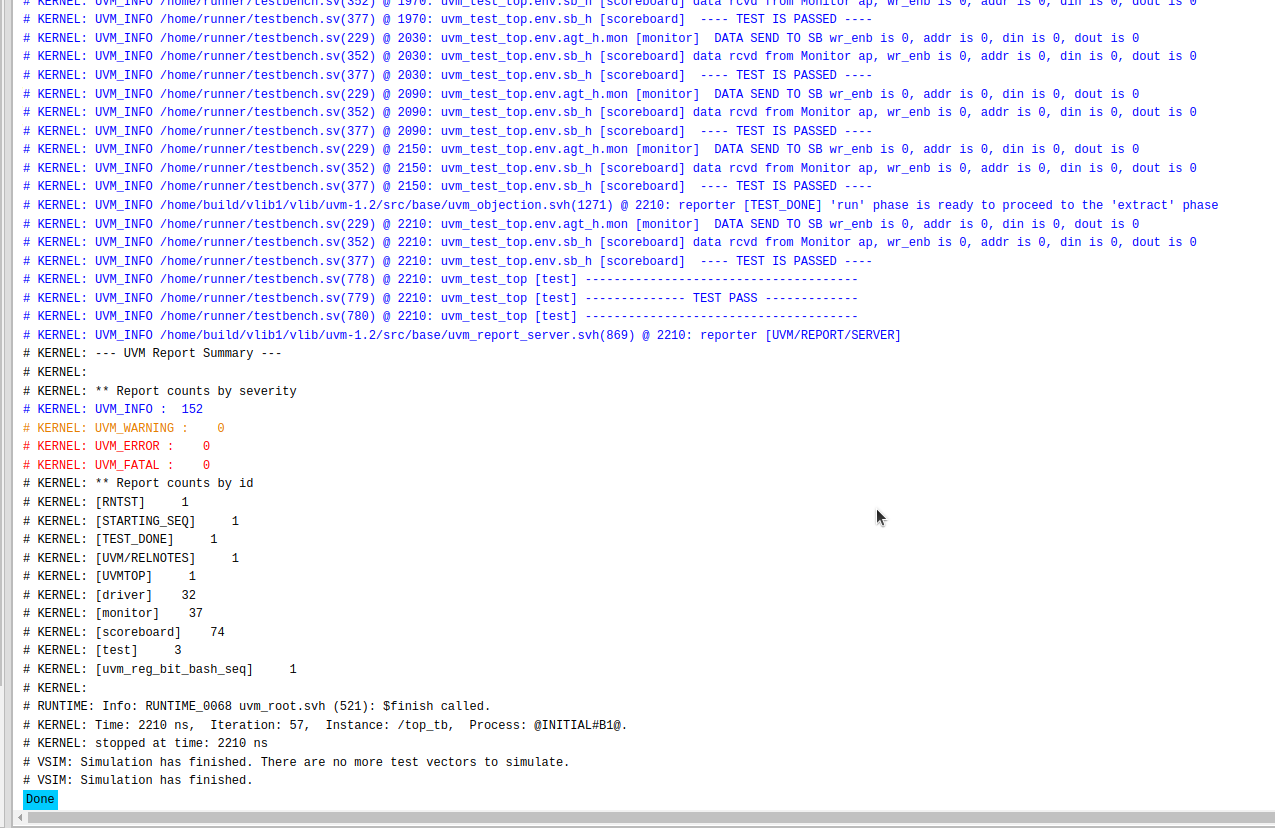
* **Predefined Sequences:**

| **uvm\_reg\_hw\_reset\_seq** |
| --- |

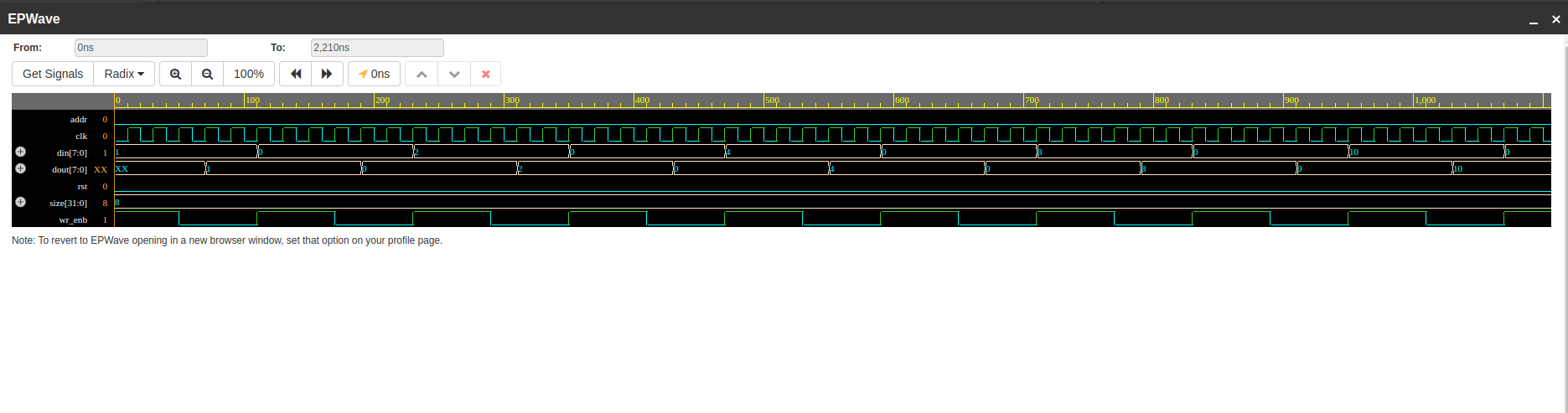
* + **Reset Sequence:**
    - **Console Output:**

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* + **Bit Bash Sequence:**
    - **Console Output:**

****

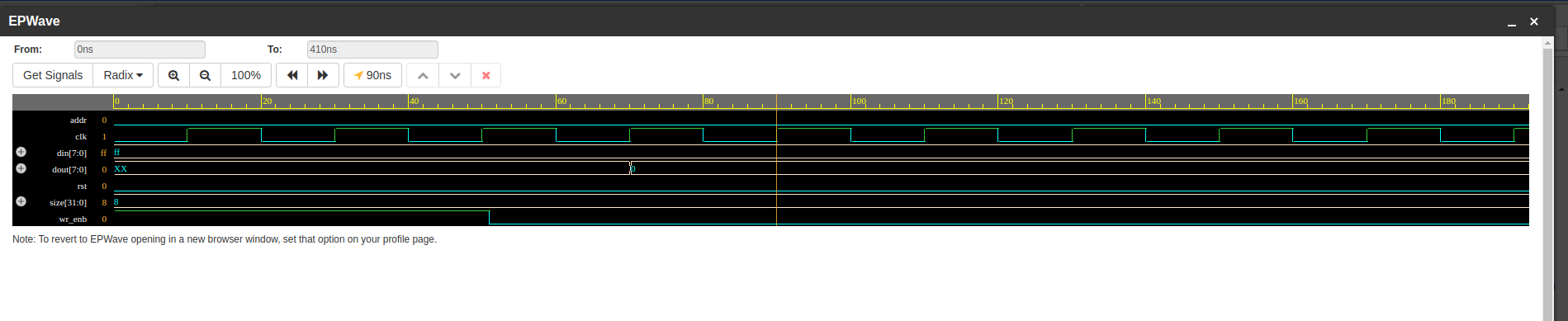
* + - **Waveform:**

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* + **Access Sequence:**
    - **Console Output:**

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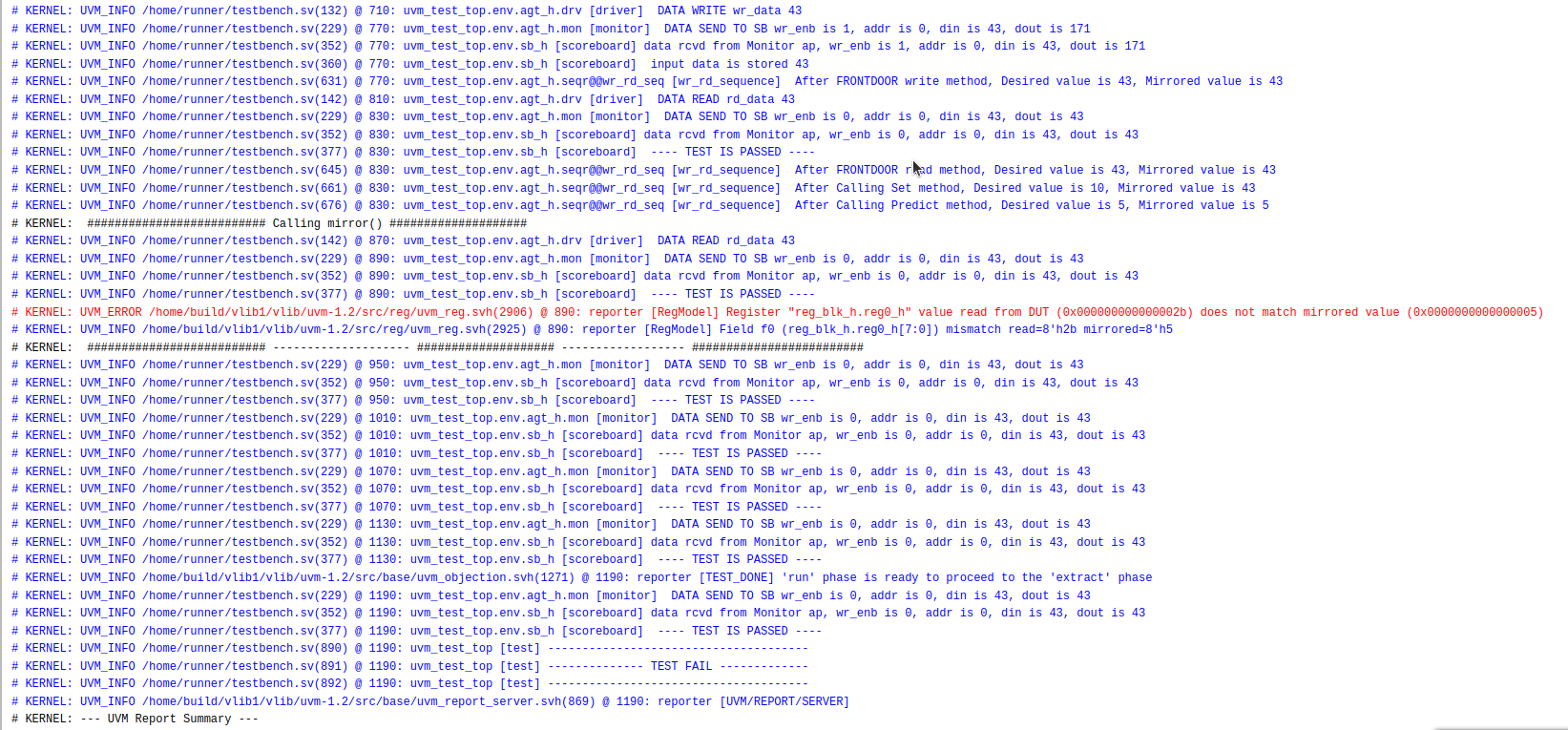
* + - **Waveform:**

****

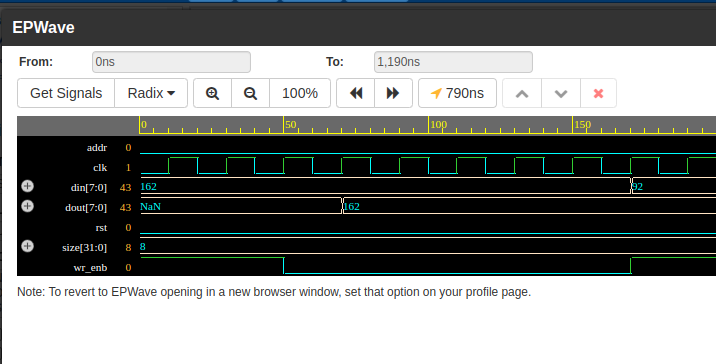
* **User-Defined Sequence:**

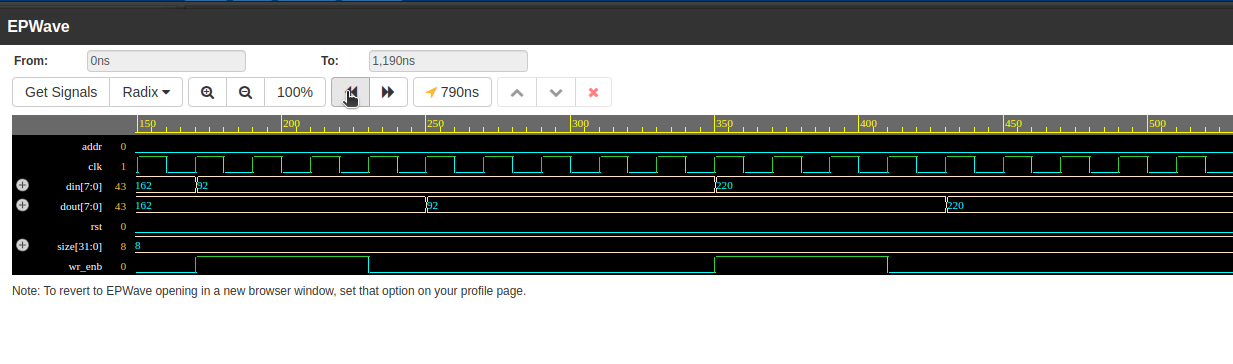
| **wr\_rd\_sequence** |
| --- |

* + - **Console Output:**

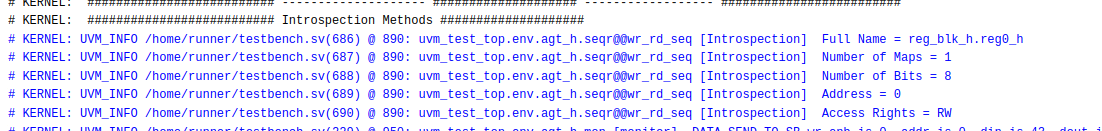
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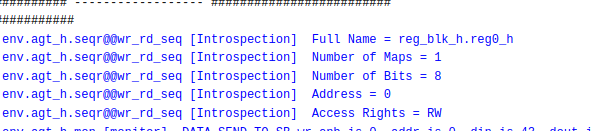
* + - **Waveform:**

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* **Introspection Methods:**
  + **Output:**

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