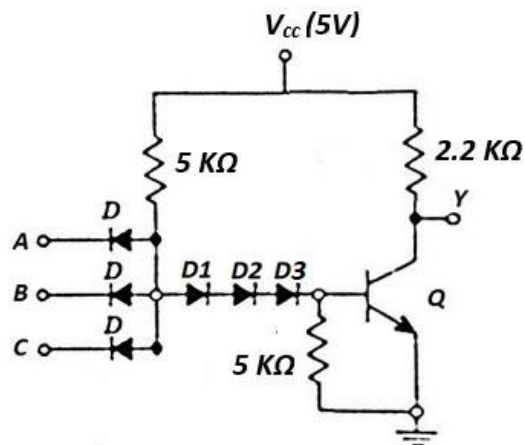


1. For the following circuit, assume that $V_{BE(sat)} = 0.8 \text{ V}$, $V_\gamma = 0.5 \text{ V}$ and $V_{CE(sat)} = 0.2 \text{ V}$. The drop across a conducting diode is 0.7 V and $V_{\gamma(\text{diode})} = 0.6 \text{ V}$ and $h_{FE} = 30$. Here Y is the output of the given circuit.



- Prove that the circuit works like a NOR gate and also find the minimum value of h_{FE} so that the circuit will properly operate as a negative logic NOR gate. 5
- Calculate $NM(0)$ and $NM(1)$. 3
- Find the average power P dissipated by the gate. 2

Solution:

Case-1:

$$A = 0.2 \text{ V} = V(1)$$

$$B = 5 \text{ V} = V(0)$$

$$V_p = 0.2 + 0.7 = 0.9 \text{ V}$$

Minimum amount of voltage required to turn on the transistor, Q is:

$$\begin{aligned} & D1 + D2 + D3 + Q \\ &= 0.6 + 0.6 + 0.6 + 0.5 \quad [\text{Taking the cut-in voltages of diodes and transistor}] \\ &= 2.3 \text{ V} \end{aligned}$$

As, $V_p \ll 2.3 \text{ V}$ so the transistor will operate in cut-off region.

$$\text{So, } Y = V_o = 5.0 \text{ V} = V(0)$$

The same case will happen for the case $A = 5 \text{ V}$, $B = 0.2 \text{ V}$ and $A = 0.2 \text{ V}$, $B = 0.2 \text{ V}$

Case-2:

$$A = B = 5 \text{ V} = V(0)$$

$$\begin{aligned}
 V_p &= D1+D2+D3+Q \\
 &= 0.7+0.7+0.7+0.8 \\
 &= 2.9 \text{ V}
 \end{aligned}$$

$$\begin{aligned}
 I_1 &= (5-2.9)/5 \text{ K} = 0.42 \text{ mA} \\
 I_2 &= (0.8-0)/5 \text{ K} = 0.16 \text{ mA} \\
 I_B &= I_1-I_2 = 0.42-0.16 = 0.26 \text{ mA} \\
 I_C &= (5-0.2)/2.2 \text{ K} = 2.182 \text{ mA}
 \end{aligned}$$

$$h_{FE} = I_C/I_B = 2.182/0.26 = 8.392$$

In order to turn the transistor in saturation the value h_{FE} should be more than 8.392.

A	B	V_o
V(1)	V(1)	V(0)
V(1)	V(0)	V(0)
V(0)	V(1)	V(0)
V(0)	V(0)	V(1)

Here, $V(1) = 0.2 \text{ V}$, $V(0) = 5 \text{ V}$

Conclusion: The circuit is acting as a **negative logic NOR gate**.

NM(0):

When at least one input is $V(1) = 0.2 \text{ V}$ then output $V_o = 5 \text{ V} = V(0)$

$$V_p = 0.2+0.7 = 0.9 \text{ V}$$

Minimum amount of voltage required to turn on the transistor, Q is:

$$\begin{aligned}
 &D1+D2+D3+Q \\
 &= 0.6+0.6+0.6+0.5 \quad [\text{Taking the cut-in voltages of diodes and transistor}] \\
 &= 2.3 \text{ V}
 \end{aligned}$$

$$NM(0) = 2.3-0.9 = +1.4 \text{ V} \quad ['+' \text{ because the voltage might increase up to } 1.4 \text{ V}]$$

NM(1):

When all the inputs are $V(0) = 5 \text{ V}$ then output $V_o = 0.2 \text{ V} = V(1)$

$$V_p = D1+D2+D3+Q = 0.7+0.7+0.7+0.8 = 2.9 \text{ V}$$

$$\text{Reverse bias by: } (5-2.9) = 2.1 \text{ V}$$

$$\text{Forward bias by: } 0.6 \text{ V} \quad [\text{cut-in voltage of diode is } 0.6 \text{ V}]$$

$$NM(1) = 2.1+0.6 = -2.7 \text{ V} \quad ['-' \text{ because the voltage might decrease up to } 2.7 \text{ V}]$$

Power(P):

$$I(1) = I_1 + I_C = 0.42 + 2.182 = 2.602 \text{ mA}$$

$$I(0) = (5 - 0.9) / 5 \text{ K} = 0.82 \text{ mA}$$

$$P(1) = V \cdot I(1) = 5 \cdot 2.602 = 13.01 \text{ W}$$

$$P(0) = V \cdot I(0) = 5 \cdot 0.82 = 4.1 \text{ W}$$

$$\text{Power, } P = (P(1) + P(0)) / 2 = (13.01 + 4.1) / 2 = 8.555 \text{ W}$$