JONATHAN BRO

GAME PROGRAMMER

SOCIAL

in linkedin.com/in/jonathanbro

@J A Bro

PROJECTS

https://nomy1.github.io

CONTACT

hello.jonbro@gmail.com

O United States (Open to relocation)

US Permission to Work

SUMMARY

Responsible senior game programmer with 7+ years experience interested in creating immersive VR experiences. Loves to tinker with new technology.

RELEVANT SKILLS

- Unity 3D
- iOS Development
- Android Development
- XR Development
- Phaser
- Flutter
- Git

PROGRAMMING LANGUAGES

C# | Swift | Dart | Typescript | C++ Go | Kotlin

SPEAKING

- Native English
- Professional working profeciency in Japanese

INTERESTS

- Learning game frameworks/engines
- Playing with new programming paradigms and languages
- Experimenting with Digital Authoring Tools
- Games (RPG, FPS, MMO, Strategy, Platforming, Puzzle... too many to list)

WORK EXPERIENCE

Senior Game Developer

Fantamstick, Ltd. (Tokyo) February 2019 - Current

3 years, 7 months

- Lead developer for mobile game with over 2,000 organic reviews (4.4 star rating).
- Developed process and guidelines for team to rapidly produce HTML games using the Phaser game framework with Typescript for a client.
- Responsible for maintaining over 10 games on the AppStore, GooglePlay and Amazon TV.

Game Developer

MoonIsland Factory, Ltd. (Tokyo) February 2015 - January 2019

4 years

- Implemented enemy AI for arena gametype for an online multiplayer action RPG title using behavior trees.
- Procedurally constructed with 3D primitives simple buildings in AR using Vuforia SDK.
- Developed front-end battle screen featuring client-side prediction and server reconciliation with hand-built behaviour tree driven AI enemies.

Field Service Engineer

DCG Systems (acquired by FEI in 2015).

June 2010 - November 2013

3 years, 5 months

- Specialized in diagnosis and repair of 200mm/300mm wafer analysis Electrical Failure Analysis (EFA) systems for Japan, China, SIngapore and South Korea.
- Promoted to Field Service manager of Japan.

LSI Hardware Engineer

Inventure (acquired by Synopsys in 2012). October 2007 - May 2010

2 years, 7 months

- Designed, developed, and tested against PCI Express, USB, OCP hardware specifications.
- Programmed FPGA for designs created with Verilog.

EDUCATION

Bachelors of Science

University of Memphis (Cum Laude, 2006)

- Bachelors of Science in Electrical Engineering
- Bachelors of Science in Computer Engineering