CS 530: High-Performance Computing Benchmarking Matrix Multiplication: Sequential vs Multithreaded

Nathan Chapman

Department of Computer Science Central Washington University

May 14, 2024

Contents

1	Introduction	1
2	Methods	1
3	Results	2
4	Discussion	5
5	Conclusion	5

1 Introduction

There are many applications which can greatly benefit from being executed in parallel. The "best" of these are those that are "embarissingly parallel", in which no data needs to be shared between threads. One such context is that of matrix multiplication. Matrix multiplication and matrix powers also serve as an excellent cases on which to highlight the differences that can be gained from parallel execution compared to sequential.

Parallelism as it is normally implemented for a task such as matrix multiplication comes in the form of *multithreading*. Multithreading is the act of implementing a program in such a way that it makes use of the extra processing units in the CPU at the same time. There is also multi-*processing*, which is out of the scope of this investigation, but otherwise requires the use of message passing between different regions in memory.

2 Methods

While there is support for the manual creation and management of threads in C with pthread_create and pthread_join, the Julia programming language natively supports creating thread pools when starting the Julia runtime process with julia -t n, where n is the number of threads to created in the thread pool. Additionally, Julia can automatically assign tasks in a for loop to its different threads with the use of OpenMP-pragma-like macros such as Threads. Cthreads. For example, a matrix multiplication parallelized over the rows of the product matrix can be seen in algorithm 1. Further detail and examples can be found in the source code bundled with this report.

In the representation shown in algorithm 1 it can be seen that the runtime complexity of this implementation is reduced from the traditional implementation of matrix multiplication as $O(N^3)$ to $O(N^2)$. This simplication comes from the fact that, while there are still N column calculations that need to be considered, each with N terms needing to be added together, these N^2 calculations are done at the same time, effectively transforming the outer-most N operations in a factor of simply 1. Table 1 shows the associated runtime complexities for other forms of parallelized matrix multiplication.

For any form of parallelism (none, row, column, or element), when considering the *power* p of a matrix, the runtime complexity is simply linearly scaled by p. This additional linear factor arises from each **for** loop needing to be evaluated p times, essentially creating another **for** loop in p. Algorithm 2 shows how algorithm 1 can be modified to yield the power of matrix, while also highlighting how the runtime complexity changes.

Algorithm 1: Row-parallelized matrix multiplication using Julia's Threads.@threads

```
 \begin{tabular}{ll} \hline \textbf{Input: } N \times L \ \text{Matrix A, } L \times M \ \text{Matrix B} \\ \hline \textbf{Output: } \text{Matrix C of zeros} \\ \hline \textbf{Threads.@threads for } i \in \{1,2,\ldots,N\} \ \textbf{do} \\ & | \ \textbf{for } j \in \{1,2,\ldots,M\} \ \textbf{do} \\ & | \ \textbf{for } k \in \{1,2,\ldots,L\} \ \textbf{do} \\ & | \ \textbf{C[i,j]} \leftarrow \textbf{C[i,j]} + \textbf{A[i,k]} \times \textbf{B[k,j]} \\ & | \ \textbf{end} \\ & \ \textbf{end} \\ \hline \end \\ \end \\ \hline \end \\ \end \\ \hline \end \\ \hline \end \\ \end \\ \end \\ \hline \end \\ \end \\
```

Parallelism	Rows	Columns	Summands	Complexity	Power Complexity
Sequential	N	N	N	$O(N^3)$	$O(pN^3)$
Row	1	N	N	$O(N^2)$	$O(pN^2)$
Column	N	1	N	$O(N^2)$	$O(pN^2)$
Element	1	1	N	O(N)	O(pN)

Table 1: Different runtime complexities for different levels of paralleism in matrix multiplication.

Algorithm 2: Row-parallelized matrix power using Julia's Threads. @threads

```
 \begin{split} \textbf{Input:} \ N \times N \ \text{Matrix A, Power } P \\ \textbf{Output:} \ \text{Matrix C of zeros} \\ \textbf{for } p \in \{1, 2, \dots, P\} \ \textbf{do} \\ & \mid \ \textbf{Threads.@threads for } i \in \{1, 2, \dots, N\} \ \textbf{do} \\ & \mid \ \textbf{for } j \in \{1, 2, \dots, N\} \ \textbf{do} \\ & \mid \ \textbf{for } k \in \{1, 2, \dots, N\} \ \textbf{do} \\ & \mid \ \textbf{C[i,j]} \leftarrow \textbf{C[i,j]} + \textbf{A[i, k]} \times \textbf{B[k, j]} \\ & \mid \ \textbf{end} \\ & \mid \
```

Finally, the results shown here are using the **@elapsed** macro in Julia. The time() function, as it is in C, was also used but did not show differnt results.

3 Results

Figures (1, 2, 3) show the timings of matrix powers for different regions of the dimension-power space. Figure (1) shows the comparison between the sequential and paralleized matrix powers for different forms such as element-wise parallelism, row-wise, and column-wise. Additionally, the relative timings, as compared to the sequential execution, are also displayed for comparison. The other figures follow the same scheme.

Figure (4) shows how the execution time of the paralleized matrix power depends on the power to which the matrix is raised.

The results in figures (2, 4, 1) were gathered on a machine with the specifications shown in table 2, while the results in figure (3) were gathered on a machine with specifications as show in table (3).

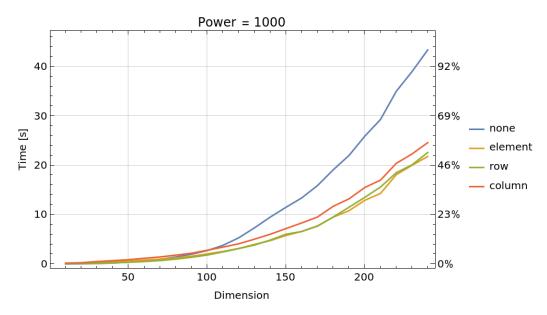


Figure 1: Benchmarking for different forms of parallelism over matrix dimension, and a power of 1000.

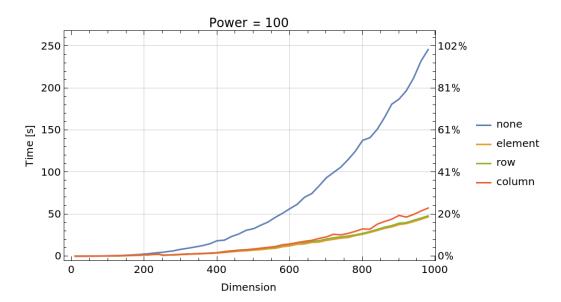


Figure 2: Benchmarking for different forms of parallelism over matrix dimension, and a power of 100

OS	Manjaro Linux
Kernel	Linux 6.6.30-2-MANJARO
Shell	bash 5.2.26
CPU	13th Gen Intel i7-13700K (24) @ 5.3GHz
GPU	NVIDIA GeForce RTX 3060 Ti Lite Hash Rate
Memory	31.18GiB
GPU Driver	NVIDIA 550.78

Table 2: Different runtime complexities for different levels of paralleism in matrix multiplication.

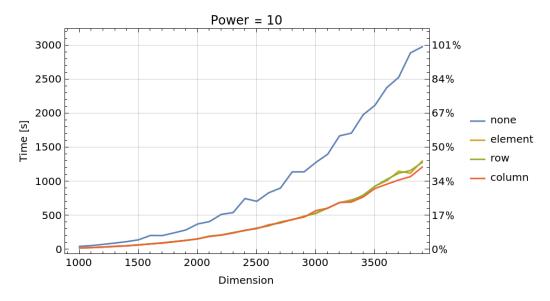


Figure 3: Benchmarking for different forms of parallelism over large matrix dimensions, and a power of 10

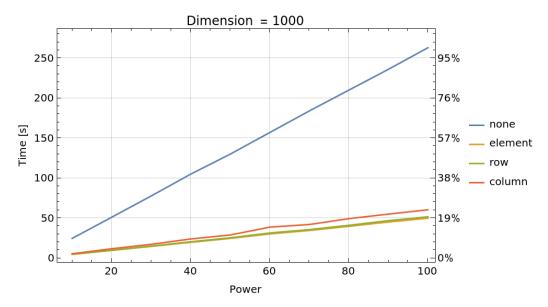


Figure 4: Benchmarking for different forms of parallelism over large matrix dimensions, and a power of 10

OS	Arch Linux		
Kernel	Linux 6.6.30-2		
Shell	bash 5.2.26		
CPU	6th Gen Intel i5-6500 (4) @ 3.2GHz		
GPU	NVIDIA GeForce GTX 1660 Super		
Memory	16GiB		

Table 3: Different runtime complexities for different levels of paralleism in matrix multiplication.

4 Discussion

While each figure could not more clearly suggest that using multiple threads to execute matrix multiplications results in drastically smaller execution times, there are discrepancies between them. When considering matrices with a "medium" dimension as in figure (2), the execution time of multiplication is quite staggering at around an 80% reduction in execution time. This is in contrast to the timings found for "small" matrices as in figure (1) where there was only a reduction of around 50%. Assuming this trend continues, one should assume the timings for "large" matrices (as in figure (3)) should yield an even larger reduction in execution time. Figure (3) though does not show a consistency in this trend, only showing a reduction by about 50%. There could be many factors causing this discrepancy, but most notably is the use of a different machine. The large-matrix timings were found on a machine with specifications as shown in table (3), whereas the other results were found on a machine with specifications as shown in figure (2). The most relevant difference between these two machines is the number of cores in the CPU: the large-matrix machine only used 4 cores and 4 threads at 3.2 GHz while the medium-matrix machine used 20 cores with 20 threads at 5.2 GHz. In order to accurately extrapolate a trend for these results, the large-matrix timings would need to be evaluated on the same machine as the small- and medium-matrix results.

The results in each of varying-dimension cases are consistent with the prediction that the execution time of sequential matrix powers depends cubically on the dimension of the matrix. Likewise, the execution time of matrix powers when evaluated in row- or column-wise parallel depends quadratically on the dimension of the matrix. As for element-wise parallelism, the prediction is that the execution time should scale linearly (assuming there is a core for every thread), but each of the dimension figures (1, 2, 3) all show the execution time of the element-wise parallelism to match with the row- and column-wise results. This discrepancy between the prediction and experiment certainly warrants further investigation. The results as shown in figure (4) are consistent with the prediction at the execution time of matrix powers should increase linearly with the power.

5 Conclusion

Many applications can benefit from being executed in parallel over multiple threads. A subset of these applications require the use of *many* threads in order to execute in a reasonable time. Low level approaches like C's pthread_create, pthread_join, etc. are excellent resources when the need arises to have direct control over the creation, synchronization, and management of threads on a near hardware level. This capability, though not using pthreads, is further highlighted when using massively parallel architecture such as a GPU with the CUDA parallel computing framework. For other applications, a higher level approach, such as using Julia, is sufficient and allows for more rapid development with minimal introduced execution overhead.

Some of the aforementioned applications are so-called "embarissingly parallel", such as matrix multiplication. It is in these applications that creating multithreaded algorithms and implementations is easy. Other applications, namely those in which the threads need to share data by either reading or writing concurrently, require much more sophisticated approaches utilizing tools such as mutex locks or atomic variables. Though when done properly, the extra work can lead to massive reduction in execution time.

By far and away, the most substantial effect parallelism has on an execution is the massive reduction in execution time. As with matrix multiplication, the differences between a sequential implementation and a parallel implementation could lead to 80% reduction in computation time. For example, in the case of considering an autonomous vehicle, it would be best for the instructions to be evaluated in parallel so in the case where a pedestrian is in the way, there is minimal time between the machine gathering the data, evaluting, and stopping. Other applications, such as evaluating the terms of the Fibonacci sequence would not benefit from parallelism because it is inherently a recursive algorithm and thus must be done sequentially. Ultimately, there are many applications where the use of parallelism would greatly (80%!) reduce the time it takes to yield a result, but there are many others where parallelism is impossible.