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FACULTY OF ELECTRICAL AND ELECTRONICS ENGINEERING

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LABORATORY REPORT

Waveform generator

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SUBJECT: Digital signal processing on FPGA

GROUP: 08

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Ho Chi Minh, ../../20..

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1.1.1 Module Sine Waveform

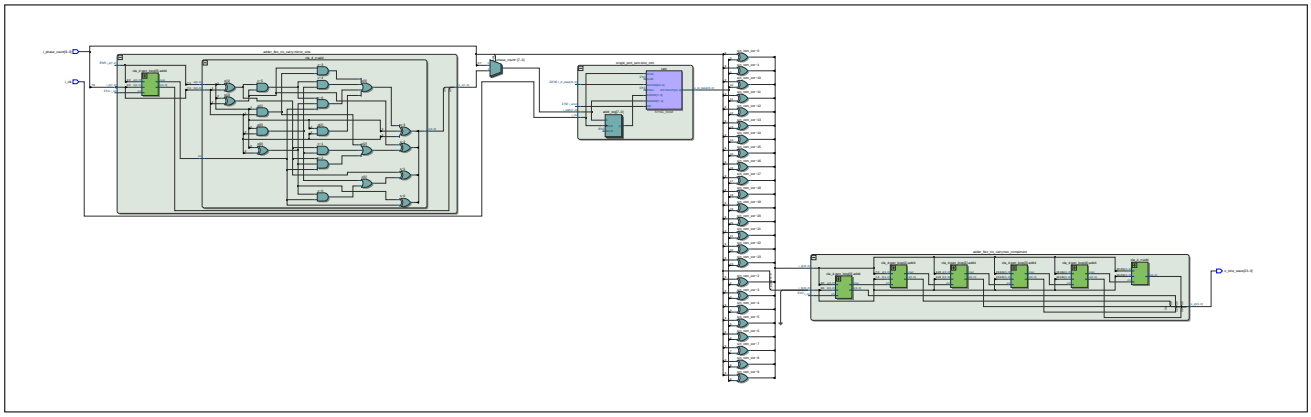

Figure 1.1: Block diagram for Module Sin_Wave.

1.1.2 Module Square Waveform

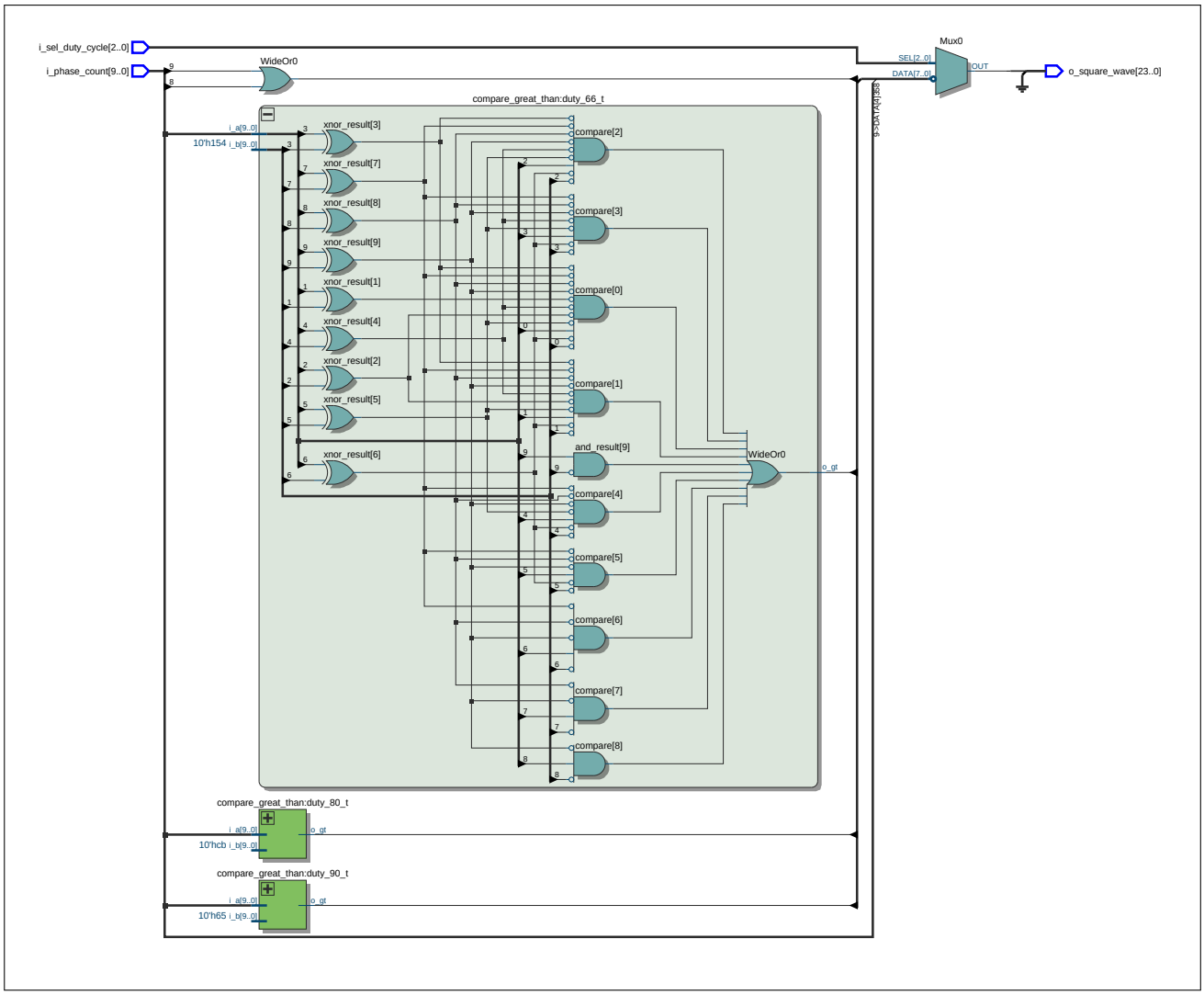

Figure 1.2: Block diagram for Module Square_wave.

1.1.3 Module Triangle Waveform

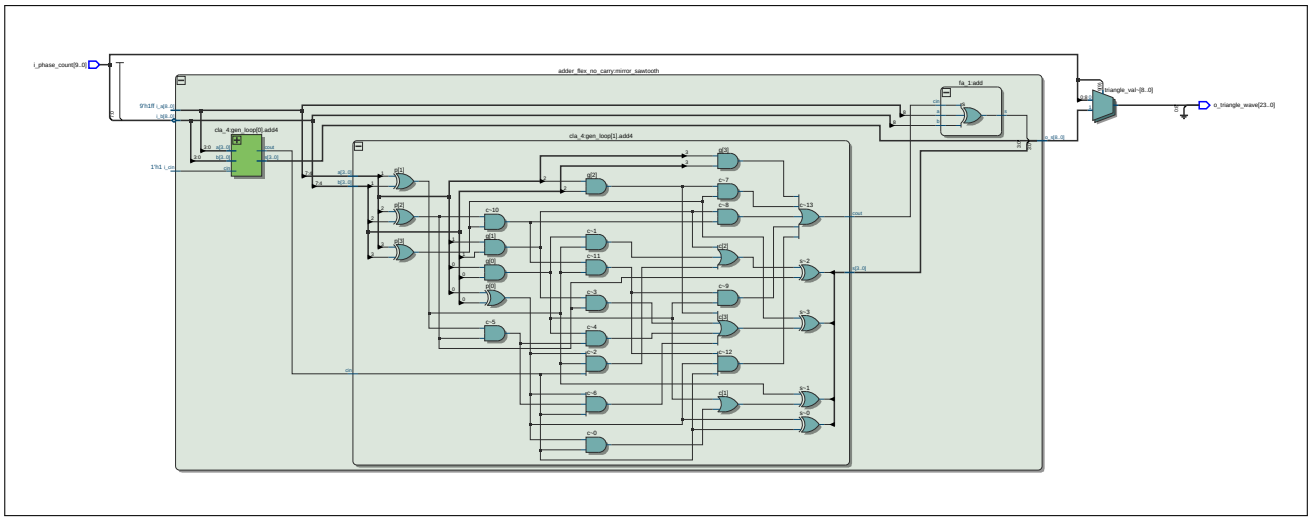

Figure 1.3: Block diagram for Module Triangle_wave.

1.1.4 Module Sawtooth Waveform

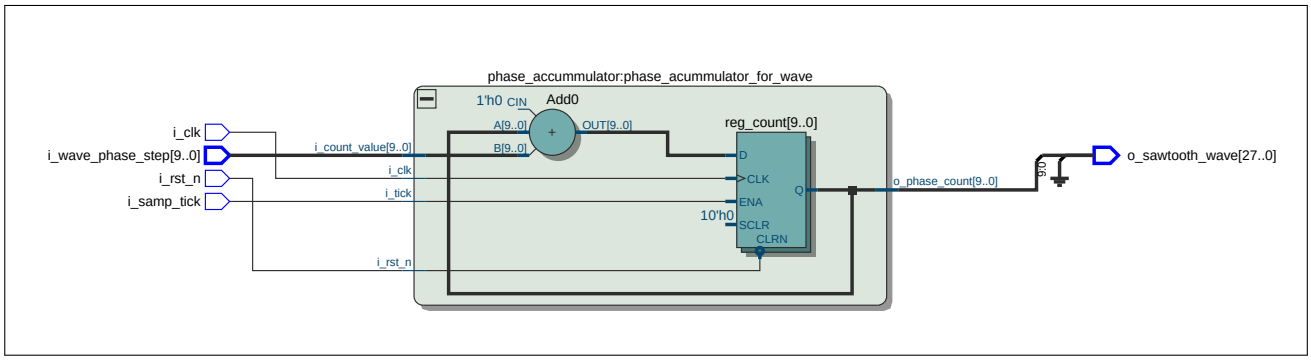

Figure 1.4: Block diagram for Module Sawtooth_wave.

1.1.5 Module ECG Waveform

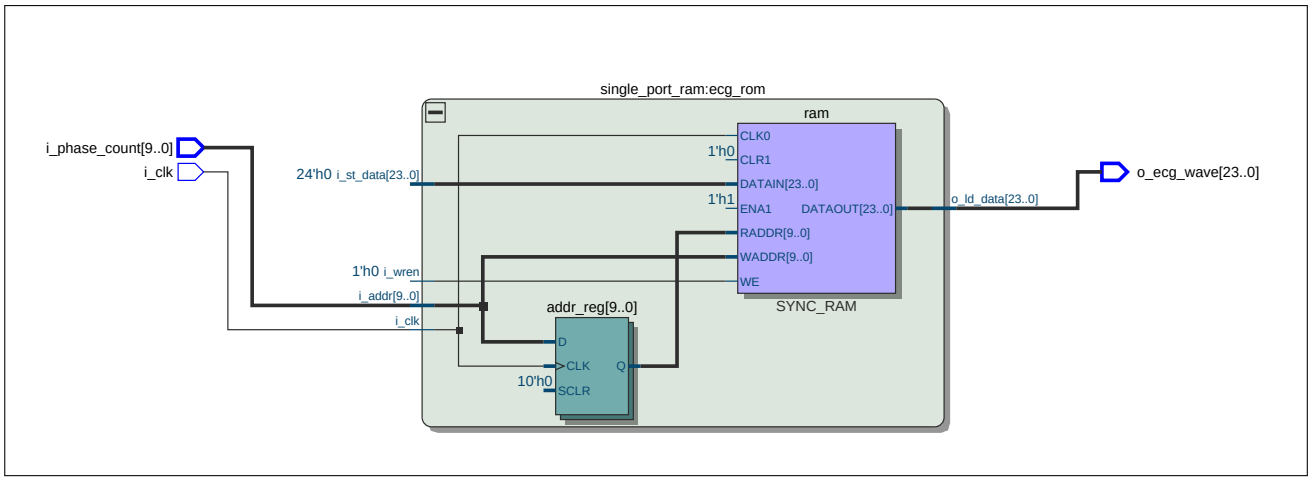

Figure 1.5: Block diagram for Module ECG_wave.

1.1.6 Module Waveform Generator

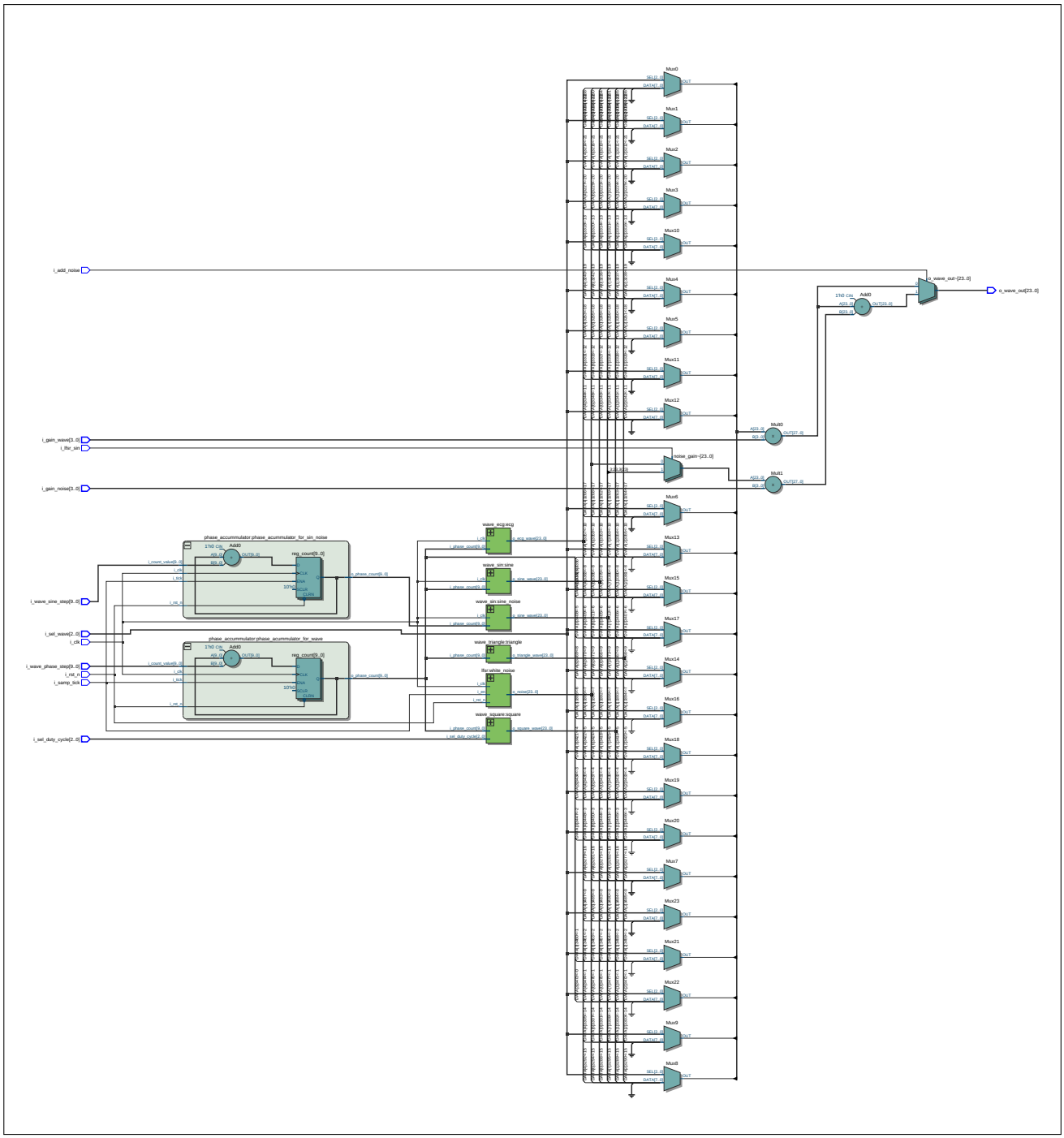

Figure 1.6: Block diagram for Module Waveform Generator.

1.2 Module Control value

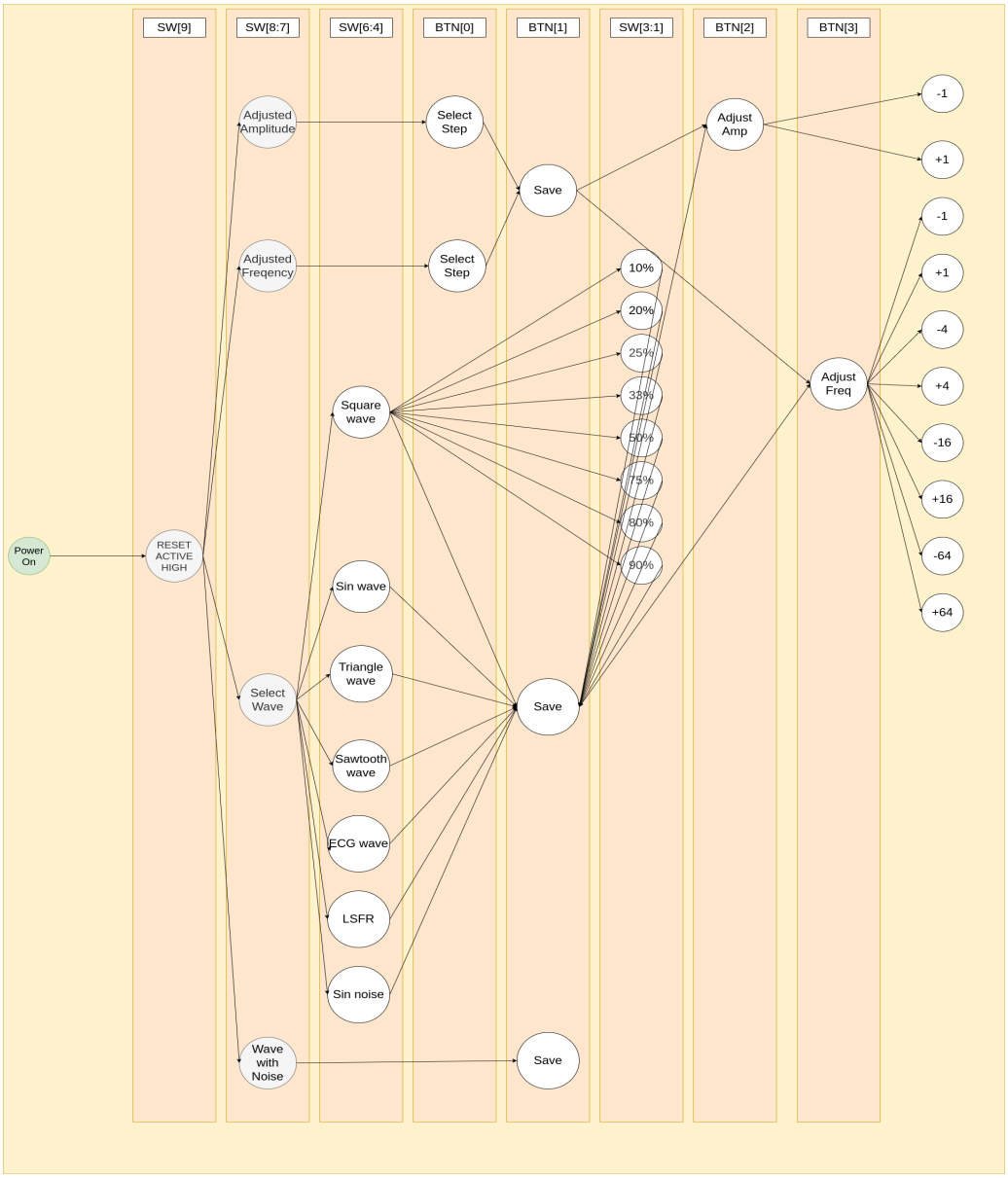

Figure 1.7: Communication operations between the user and the Control Value Module.

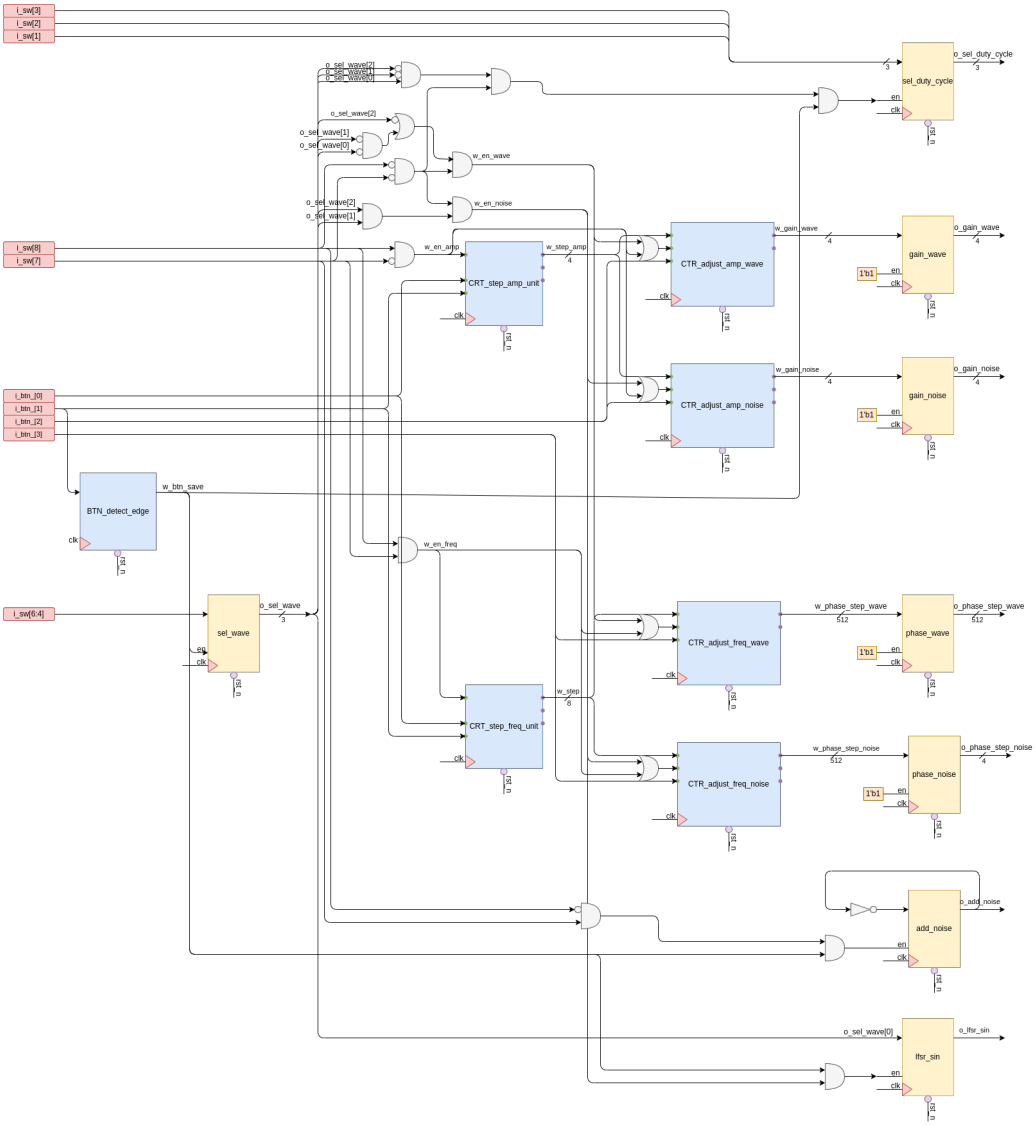

Figure 1.8: Block diagram for Module Control value.