VIETNAM NATIONAL UNIVERSITY HO CHI MINH CITY

HO CHI MINH CITY UNIVERSITY OF TECHNOLOGY

FACULTY OF ELECTRICAL AND ELECTRONICS ENGINEERING

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LABORATORY REPORT

Waveform generator

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SUBJECT: Digital signal processing on FPGA

GROUP: 08

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1.1 Module Waveform Generator

1.1.1 Module Sine Waveform

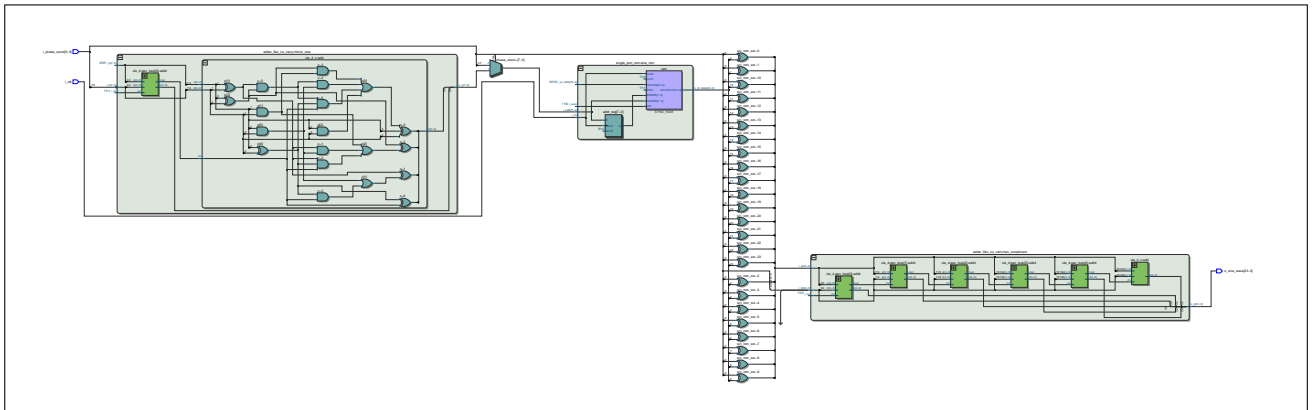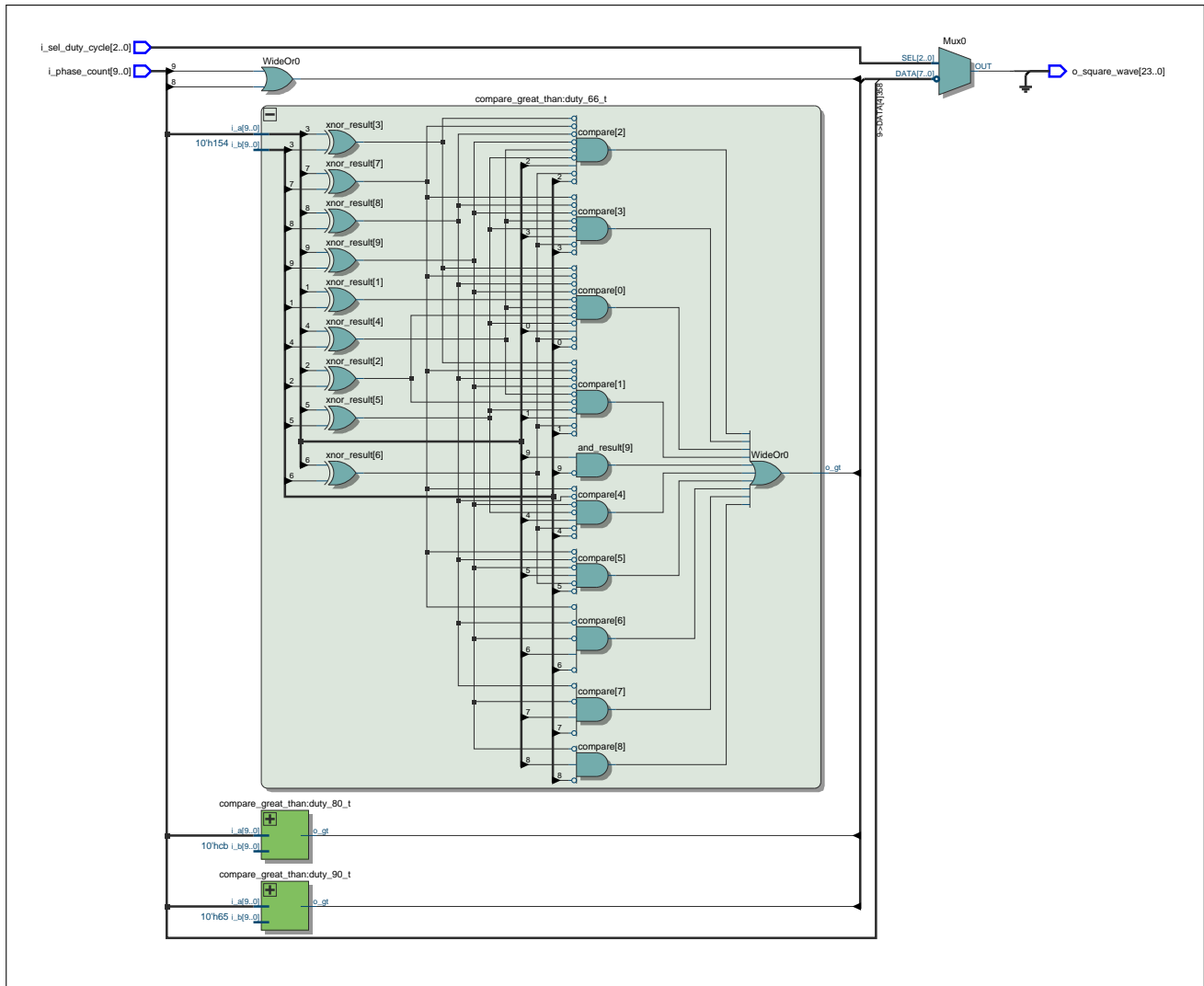Figure 1.1: Block diagram for Module Sin_Wave.

1.1.2 Module Square Waveform

Figure 1.2: Block diagram for Module Square_wave.

1.1.3 Module Triangle Waveform

Figure 1.3: Block diagram for Module Triangle_wave.