Karnaugh Map Solver

Function Info

Output Name:

One string for function result

o_sel_exp[1]

Input Names:

Comma separated list of variable names

is_E_one_a, is_E_zero_a, is_M_zer

Settings:

- (•) Sum of Products
- ( ) Product of Sums

(very slow with >10 variables)

- [x] Draw Kmap
- [x] Draw groupings

Reset Everything

Terms

Minterms:

Comma separated list of numbers

3,4,5,11,12,13,19,20,21,27,28,29,32,33,34,

Don't Cares:

Comma separated list of numbers

Reset Terms

Solutions:

Generic:

o_sel_exp[1](is_E_one_a, is_E_zero_a, is_M_zero_a, is_E_one_b, is_E_zero_b, is_M_zero_b) =
is_E_one_a'is_E_one_b'is_E_zero_bis_M_zero_b +
is_E_one_a'is_E_one_bis_E_zero_b' +

VHDL:

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s

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Verilog:

assign o_sel_exp[1] =

Karnaugh Map

o_sel_exp[1]	*is_E_one_b,is_E_zero_b,is_M_zero_b*							
	000	*001*	*011*	*010*	*110*	*111*	*101*	*100*
is_E_one_a,is_E_zero_a,is_M_zero_a *000*	0	0	1	0	0	0	1	1
001	0	0	1	0	0	0	1	1
011	0	0	1	0	0	0	1	1
010	0	0	1	0	0	0	1	1
110	0	0	0	0	0	0	0	1
111	0	0	0	0	0	0	0	1
101	1	1	1	1	0	0	1	1
100	1	1	1	1	1	1	1	1

Feel free to send any bugs or feedback to *kmaps (at) charlie-coleman.com*