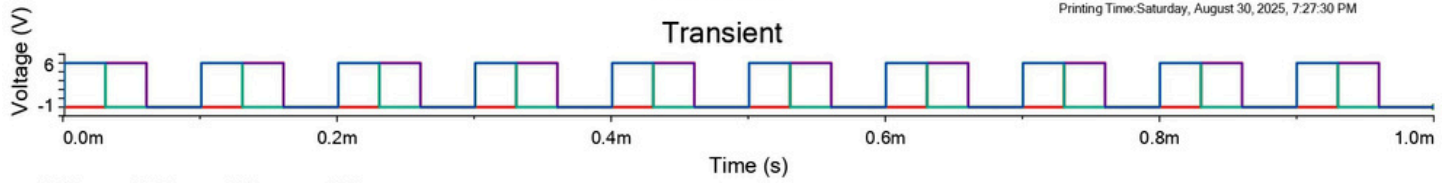


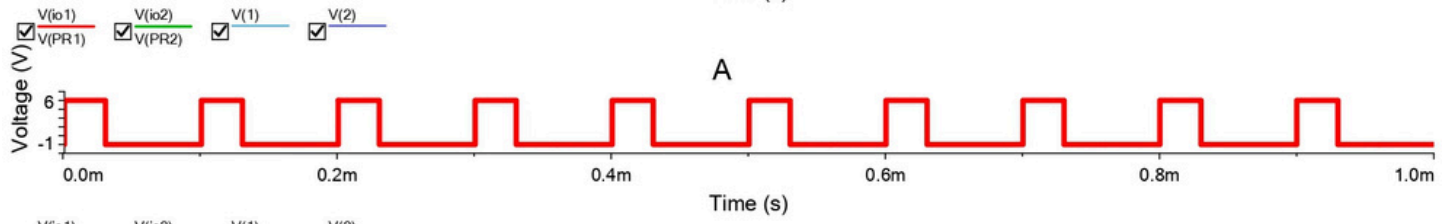
Half Adder

Printing Time: Saturday, August 30, 2025, 7:27:30 PM

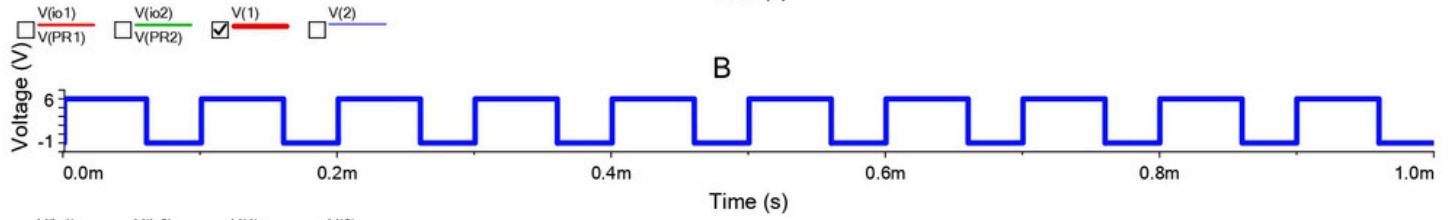
Transient



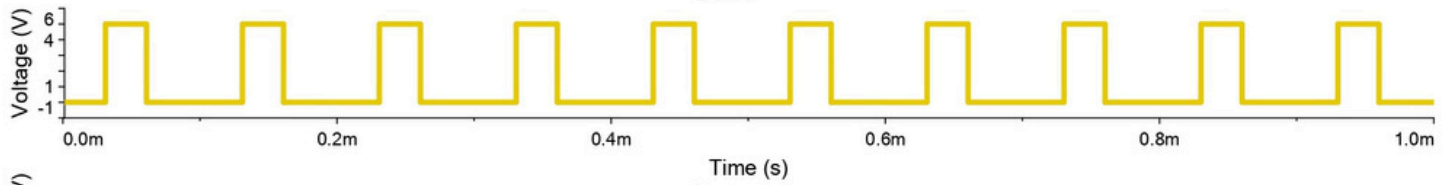
A



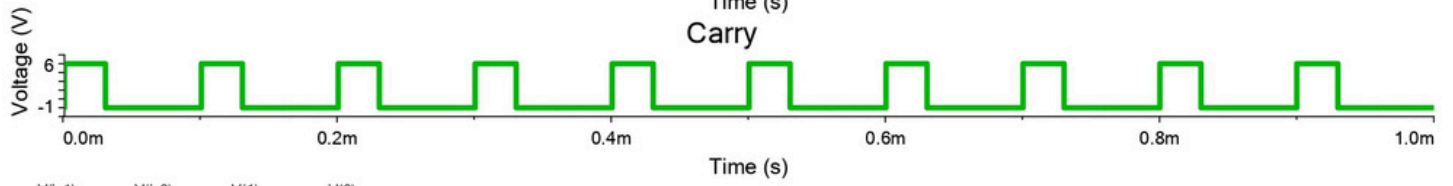
B



Sum



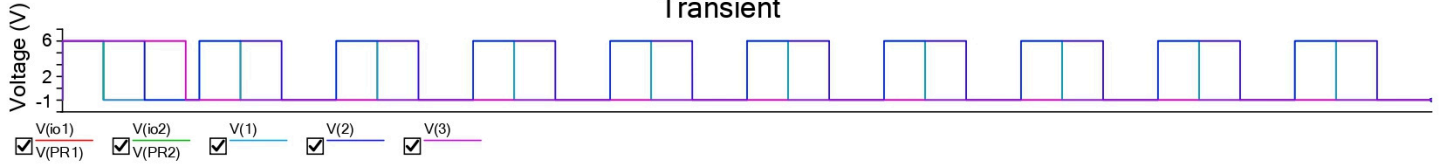
Carry



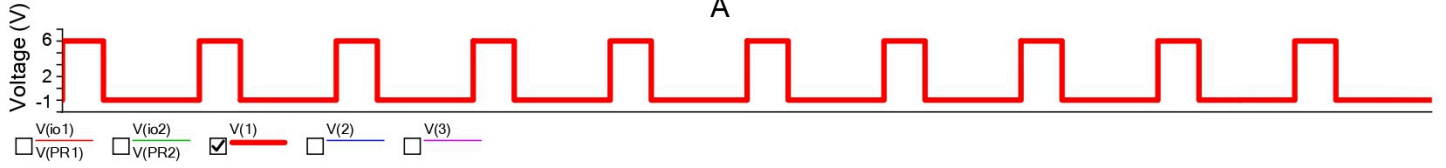
Full Adder

Printing Time: Saturday, August 30, 2025, 7:44:19 PM

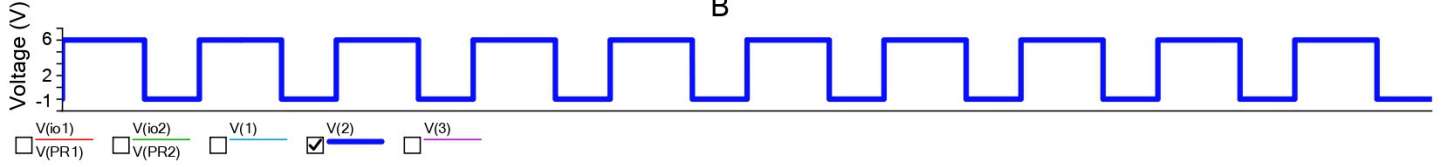
Transient



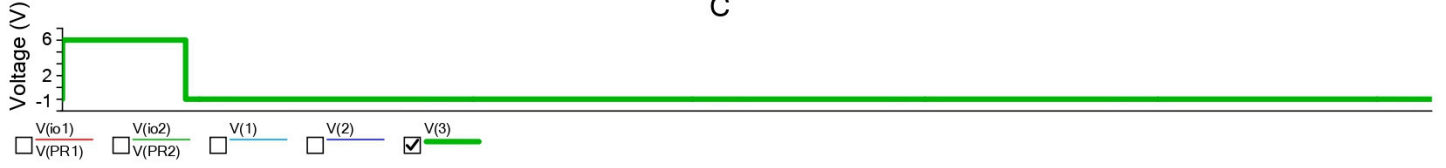
A



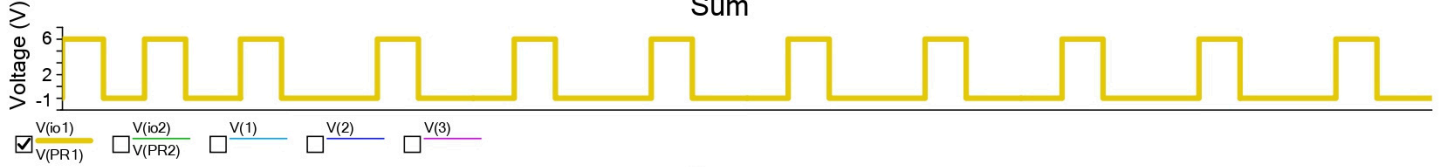
B



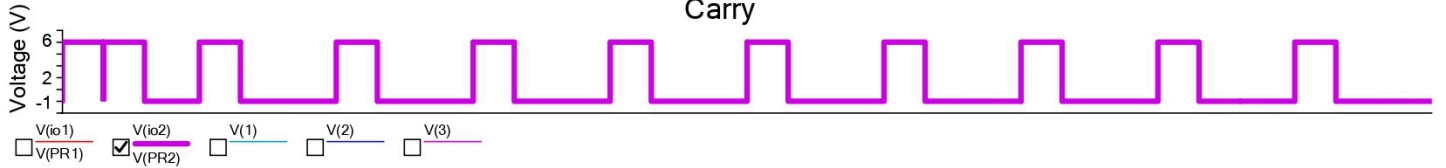
C



Sum

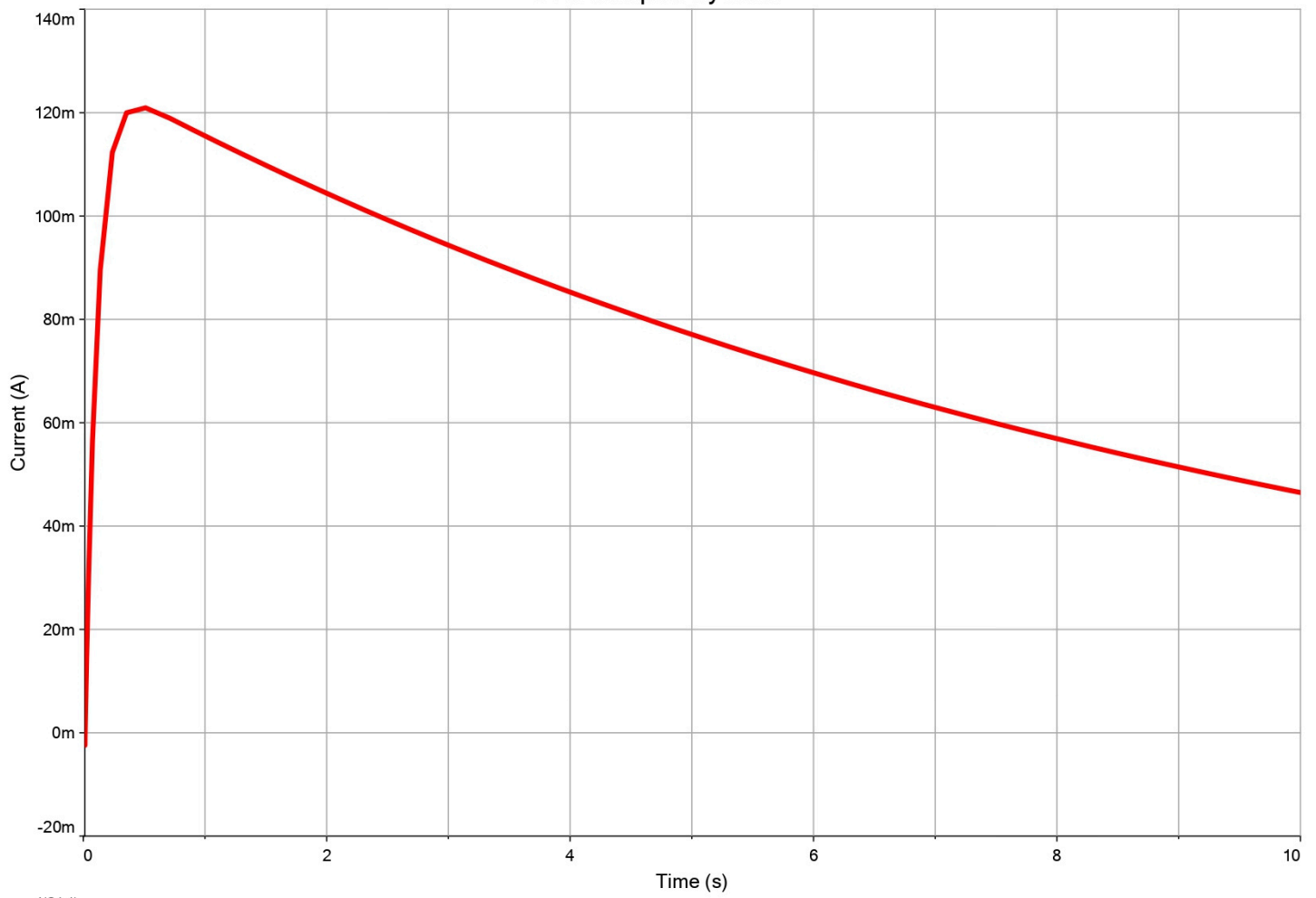


Carry



Source Free RLC Circuit Overdamped System

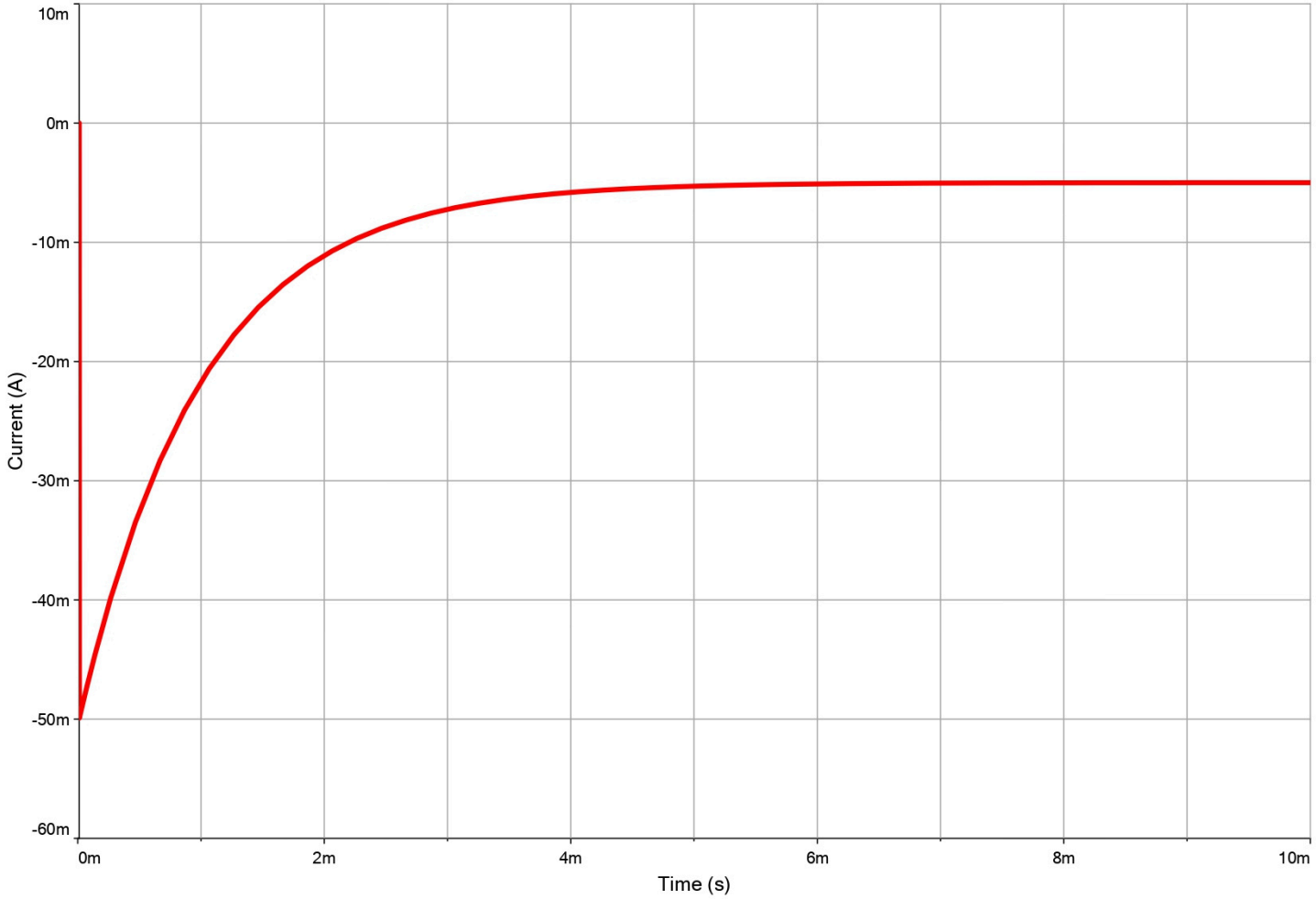
Printing Time: Monday, August 25, 2025, 12:57:30 PM



☒ $-I(C1:1)$
☐ $I(PR1)$

Sourced RL Circuit
Transient

Printing Time: Sunday, August 31, 2025, 1:12:07 AM

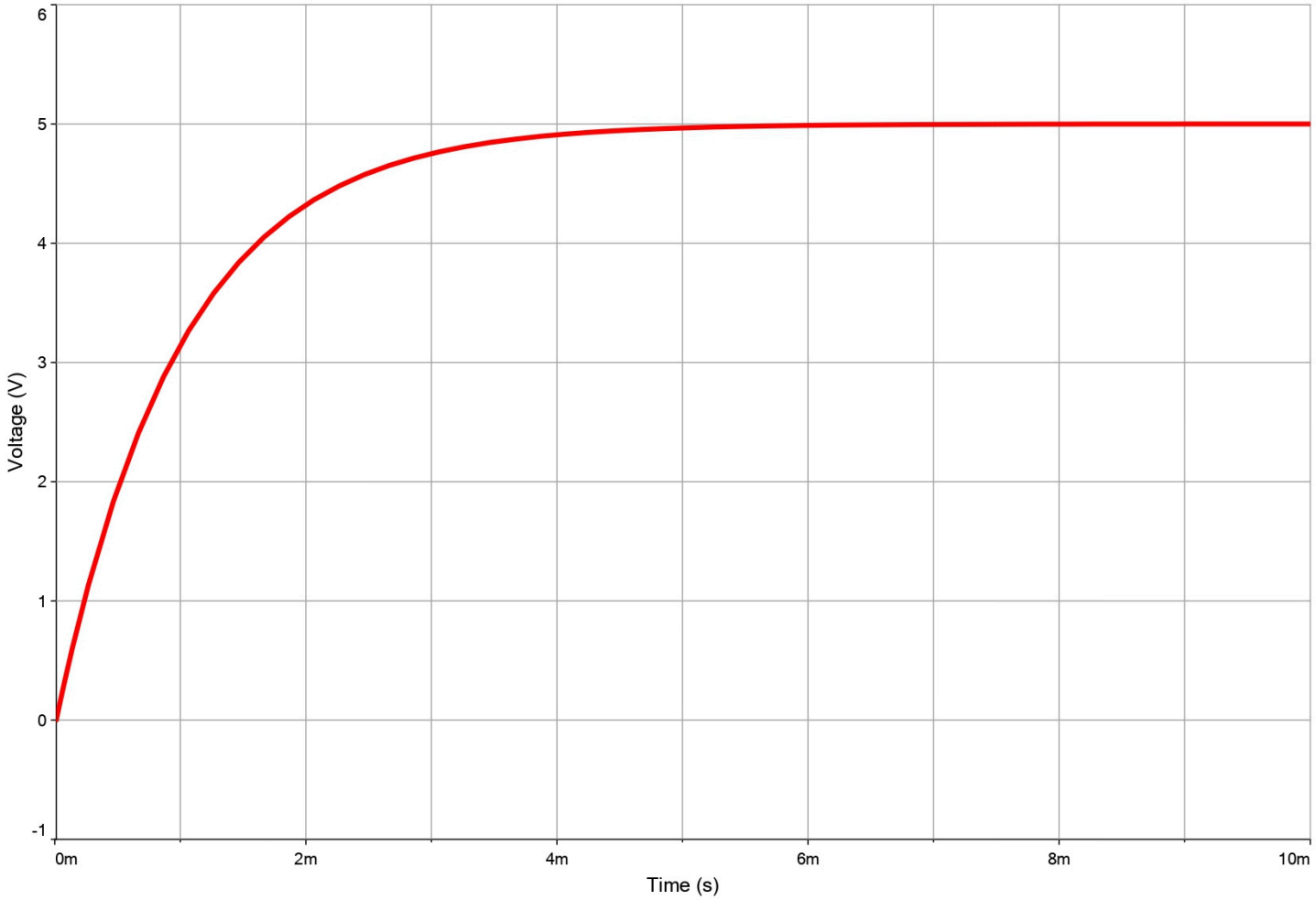


☒ I(V1:1)
☐ I(PR1)

Sourced RC Circuit

Printing Time: Tuesday, August 19, 2025, 12:29:40 PM

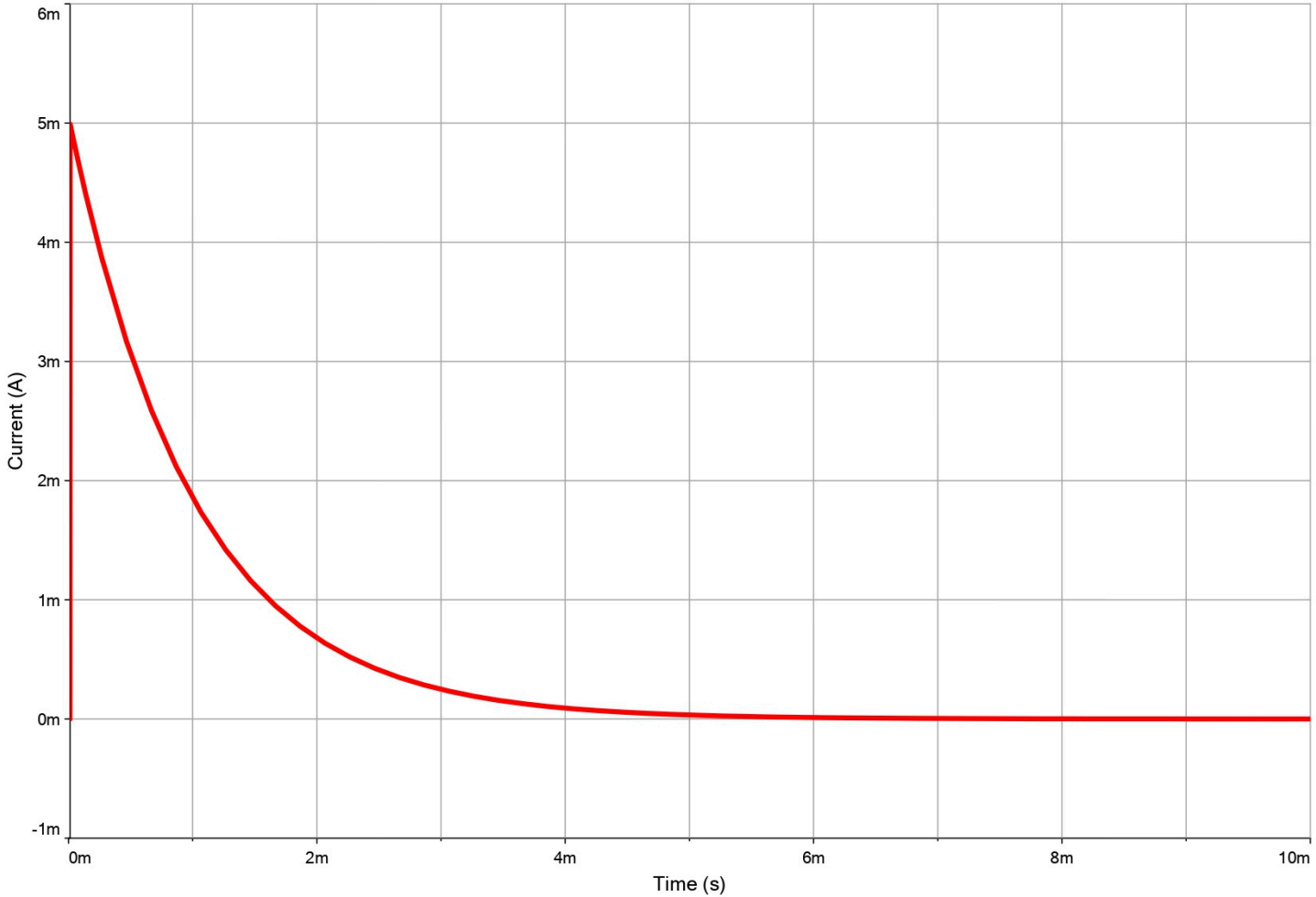
Transient



☒ V(1)

Source Free RL Circuit
Transient

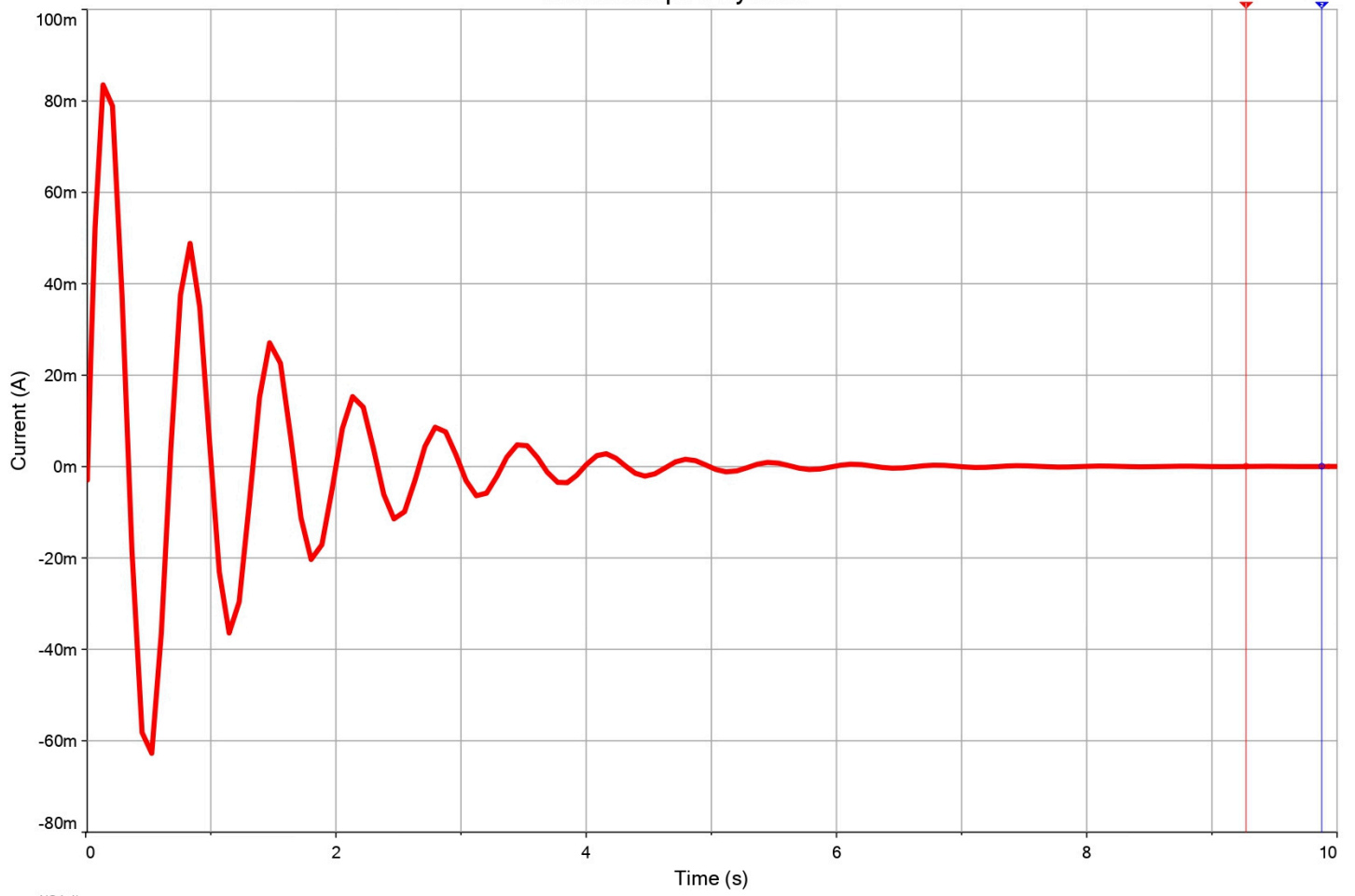
Printing Time: Tuesday, August 19, 2025, 12:28:00 PM



☒ I(L 1:1)
☐ I(PR1)

Source Free RLC Circuit Underdamped System

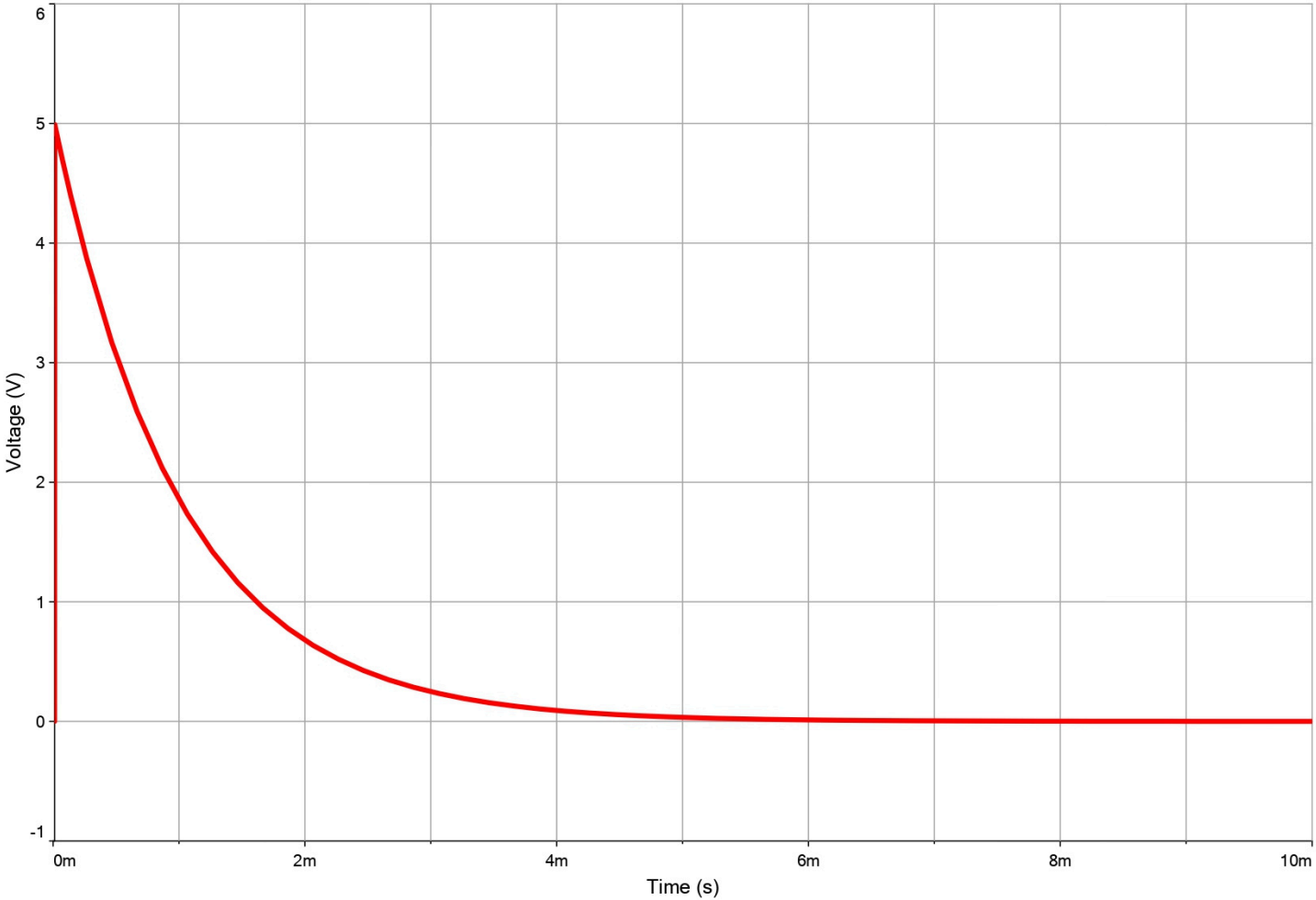
Printing Time: Monday, August 25, 2025, 12:59:53 PM



<input checked="" type="checkbox"/>	-(C1:1)								
<input type="checkbox"/>	I(PR1)								
-(C1:1) I(PR1):	x1	y1	x2	y2	dx	dy	dy/dx	1/dx	
	9.2702	-6.9347μ	9.8758	-13.3697μ	605.5901m	-6.4350μ	-10.6260μ	1.6513	

Source Free RC Circuit
Transient

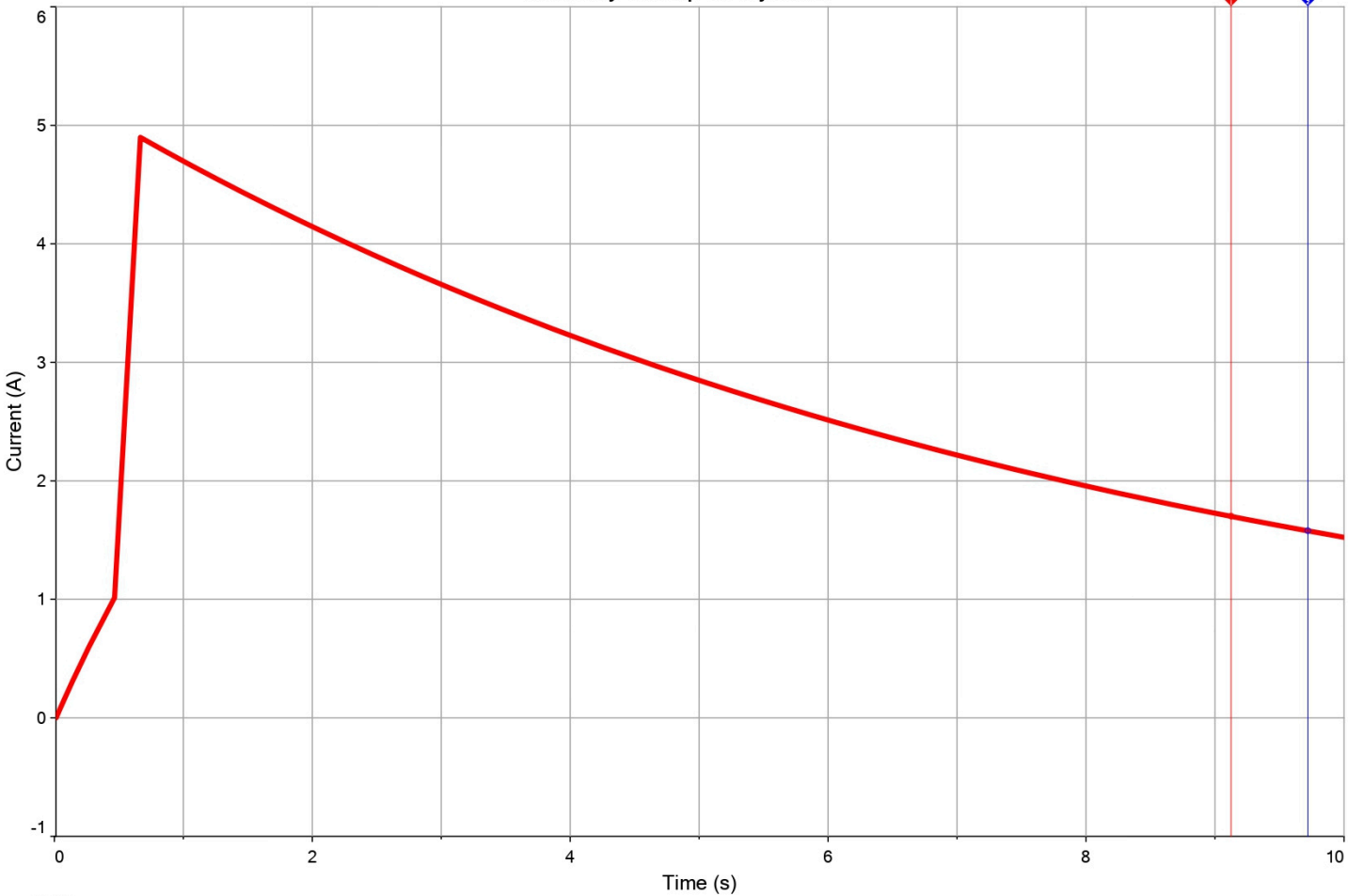
Printing Time: Tuesday, August 19, 2025, 12:30:45 PM



☒ V(1)

Source Free RLC Circuit
Critically Damped System

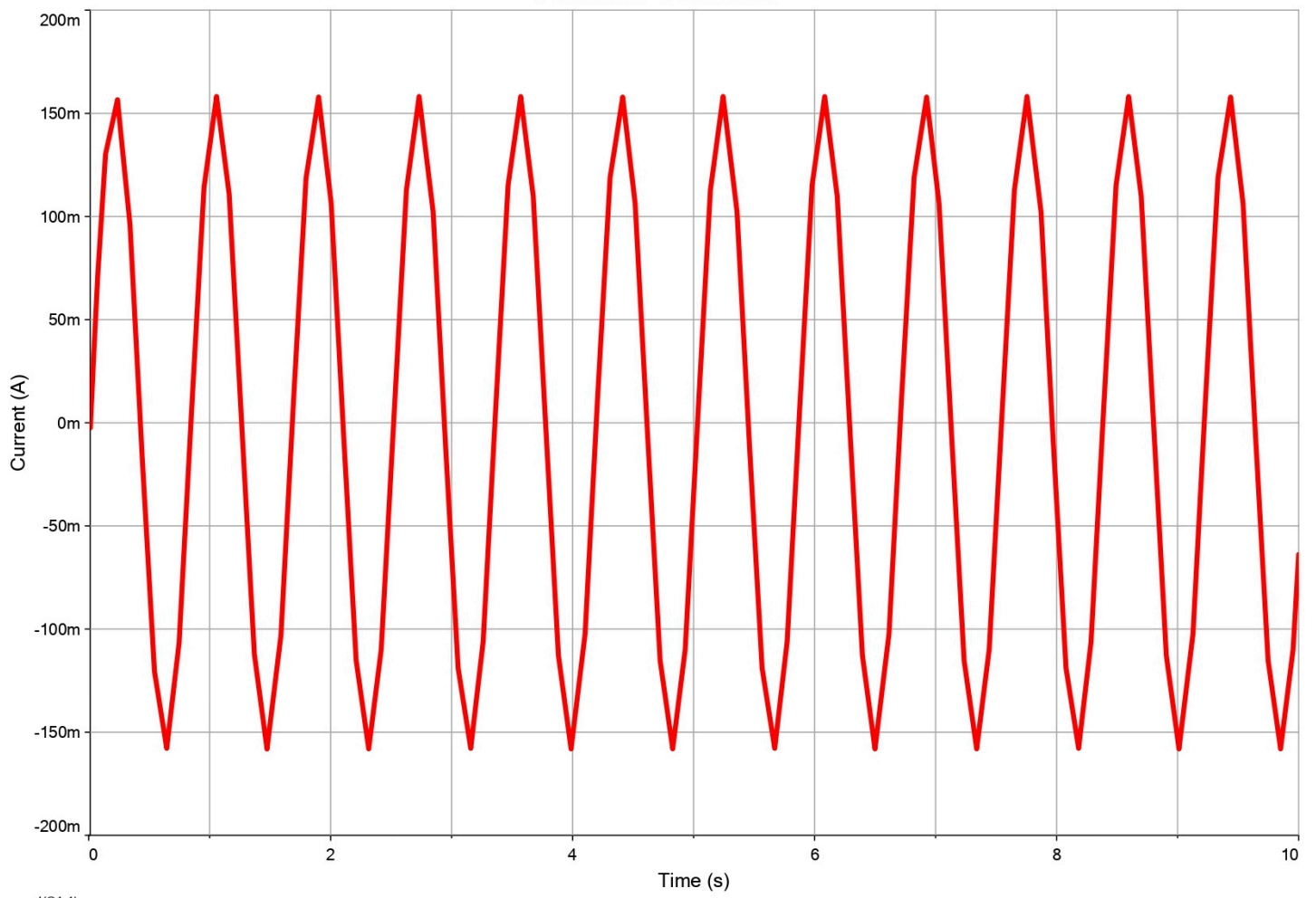
Printing Time: Monday, August 25, 2025, 1:01:04 PM



<input checked="" type="checkbox"/>	-I(C1:1)								
	I(PR1)								
-I(C1:1) I(PR1):	x1	y1	x2	y2	dx	dy	dy/dx	1/dx	
	9.1215	1.6995	9.7173	1.5775	595.8747m	-122.0087m	-204.7556m	1.6782	

Source Free RLC Circuit Sustained Oscillation

Printing Time: Monday, August 25, 2025, 1:02:27 PM



☒ $-i(C1:1)$
☐ $i(PR1)$