

1) If two processors have different instruction sets, how can you figure out which one is faster?

2) Performance Processors: The table below describes the performance of two processors, the rAlpha and the c86, and two compilers on a common 'benchmark' program.

-	GHZ	Cost	Compiler A		Compiler B	
			Instructions	Average CPI	Instructions	Average CPI
rAlpha	3.4	\$100	7000	1.2	5000	1.5
c86	2.6	\$100	1500	2.2	1000	4

a. Which is the best compiler-machine combination?

Both hardware vendors release quad-core versions of the processor for 1.7x the cost of the single core version. The results on the two new systems:

-	GHZ	Cost	Compiler A		Compiler B	
			Instructions	Average CPI	Instructions	Average CPI
rAlphaX	3.4	\$170	2250/core	1.5	1750/core	1.8
c86x4	2.6	\$170	625/core	3.2	500	5

b. Assume the benchmarks used are perfectly parallelizable i.e. the cores process equal parts of the program independently and at the same time. Now which combination performs best?

c. Using the metric $(\text{Cycles/s}) * \text{Cores} / (\text{Dollars}^2)$, which is the best machine? Is this metric useful?

3) Consider two different implementations, P1 and P2, of the same instruction set. There are five classes of instructions (A, B, C, D, and E) in the instruction set. The clock rate and CPI of each class is given below.

Machine	Clock rate	CPI A	CPI B	CPI C	CPI D	CPI E
P1	1.0 GHZ	1	1	2	3	2
P2	1.5 GHZ	1	2	3	4	3

a. Assume that peak performance is defined as the fastest rate that a computer can execute any instruction sequence. What are the peak performances of P1 and P2 expressed in instructions per second?

b. If the number of instructions executed in a certain program is divided equally among the classes of instructions except for class A, which occurs twice as often as each of the others: Which computer is faster? How much faster is it?

c. If the number of instructions executed in a certain program is divided equally among the classes of instructions except for class E, which occurs twice as often as each of the others: Which computer is faster? How much faster is it?

4) The table below shows instruction-type breakdown for different programs. Using this data, you will be exploring the performance tradeoffs with different changes made to a MIPS processor.

# Instrcutions					
Program	Compute	Load	Store	Branch	Total
P	1500	300	100	100	1750

a. Assuming that computes take 1 cycle, loads and store instructions take 10 cycles, and branches take 3 cycles, find the execution time of this program on a 3 GHz MIPS processor.

b. Assuming that computes take 1 cycle, loads and store instructions take 2 cycles, and branches take 3 cycles, find the execution time of this program on a 3 GHz MIPS processor.

c. Assuming that computes take 1 cycle, loads and store instructions take 2 cycles, and branches take 3 cycles, what is the speed-up of a program if the number of compute instructions can be reduced by one-half?

5) Amdahl's Law: The following table shows the execution time of five routines of a program running on different numbers of processors.

# Processors	Routine A (ms)	Routine B (ms)	Routine C (ms)	Routine D (ms)	Routine E (ms)
16	4	14	2	12	2

a. Find the total execution time, and how much it is reduced if the time of routines A, C, & E is improved by 15%.

b. By how much is the total time reduced if routine B is improved by 10%?

c. By how much is the total time reduced if routine D is improved by 10%?

d. Execution time in a multiprocessor system can be split into computing time for the routines plus routing time spent sending data from one processor to another. Consider the execution time and routing time given in the following table.

# Processors	Routine A (ms)	Routine B (ms)	Routine C (ms)	Routine D (ms)	Routine E (ms)	Routing (ms)
2	20	78	9	65	4	11
4	12	44	4	34	2	13
8	1	23	3	19	3	17
16	4	13	1	10	2	22

For each doubling of the number of processors, determine the ratio of new to old computing time and the ratio of new to old routing time.

6) The hypothetical machine has two I/O instructions:

0011 Load AC from I/O

0111 Store AC to I/O

add instruccion:

0101 ADD

In these cases, the 12-bit address identifies a particular I/O device. Show the program execution for the following program:

1. Load AC from device 5.
2. Add contents of memory location 940.
3. Store AC to device 6.

Assume that the next value retrieved from device 5 is 3 and that location 940 contains a value of 2.