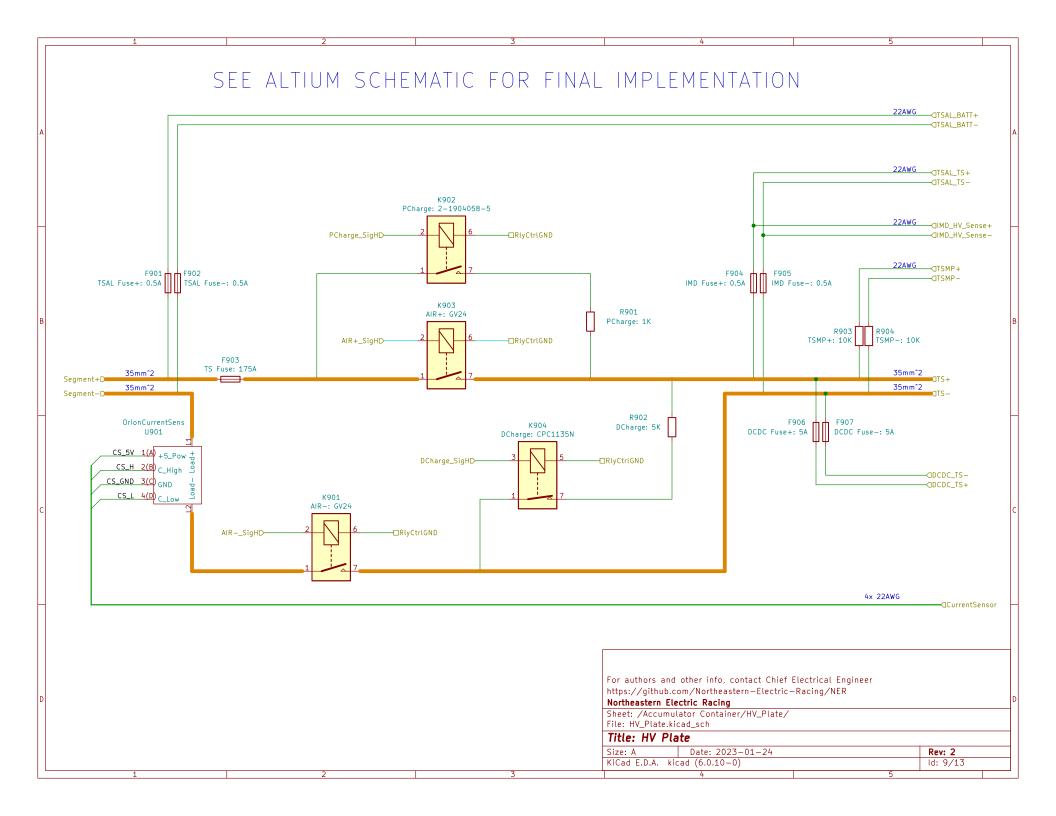
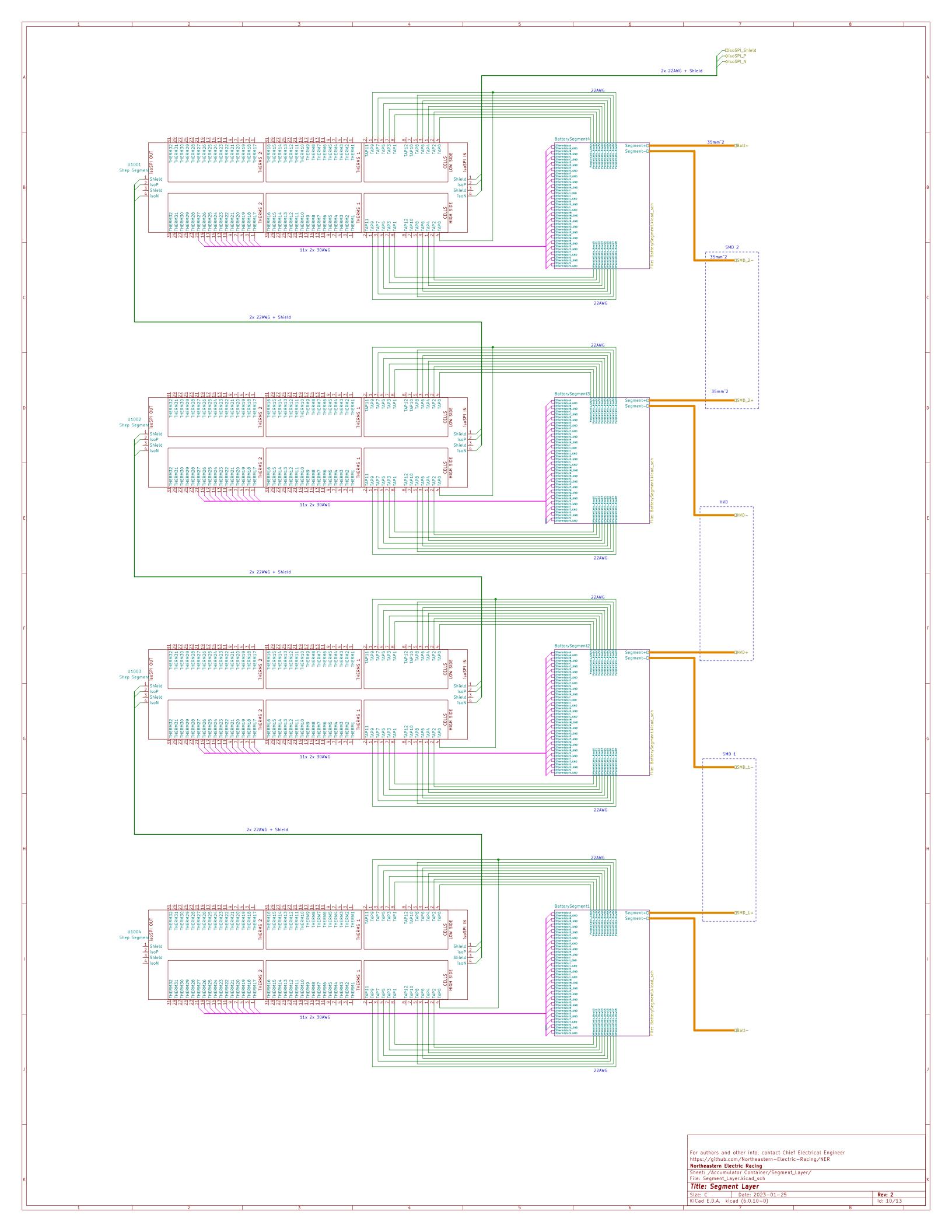
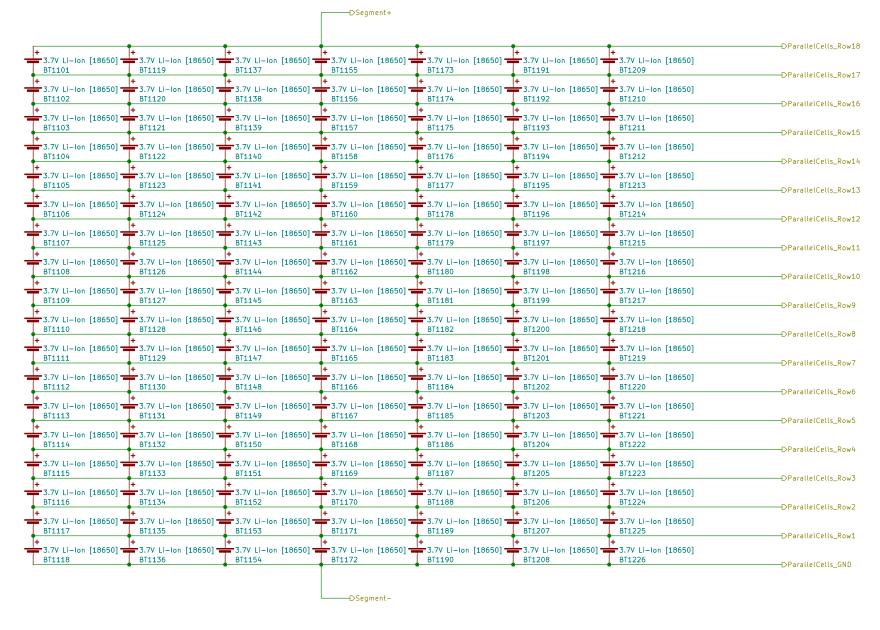


### ALL WIRES 22AWG EXCEPT WHERE LABELED To HV Plate area —GACCUM\_30V\_LED+ GLV Connector ACTIVE LED A —⊲TSAL\_ACTIVE U604 TSAL17 HV Side GNDD-Shep\_Controller LVPlate\_GLV+D-BATT-BATT+ Accumulator 10 TS-TS+ 1 GND 2 GND 3 CAN1\_L 4 CAN1\_H 5 GND 6 GND From HV Plate below GLV\_CANHD— GLV\_CANLD— FAN\_PWM 3 BattFan Hole/Cutout —dTSAL\_TS− 3x 22AWG U601 ─dtsal\_ts+ Bender\_Isometer\_IR155-3204 —⊲TSAL\_BATT-FAN\_PWM —⊲BattFanPWM BMS\_FaultD− 7 CAN2\_L 8 CAN2\_H 9 GND 10 GND 11 Fault\_Rly 13 Analog5 14 Analog5 14 Analog5 16 Analog3 16 Analog4 17 GND 19 Digital5 20 Digital6 21 Digital5 22 Digital6 21 Digital3 12V 7 GND 8 ──☐TSAL\_BATT+ STATUS\_OK +12V\_Supply FAN\_PWM 9 GLV\_Chassis1 CHASSIS\_GND2 HV\_Sense-D-HV\_Sense+ 12V 10 GND 11 HV\_Sense+D— HV\_Sense- CHASSIS\_GND1 —∕JIMD\_Fault FAN\_PWM 12 12V 13 GND 14 FAN\_PWM 15 4x 22AWG CurrentSense —⊲Fused\_TS+ To HV Plate control connector TS-AIR+\_SigH⟨<del>----</del> —⊲MC\_AIR+ AIR+\_SigHQ AIR-\_SigHQ DCharge\_SigHQ PCharge\_SigHQ HVRelays\_GND —⊲Shutdown\_GLV+ Analog1 Analog2 HV Side —⊲MC\_Precharge+ U603 TS-GLV17 isoSPI ——□isoSPI\_Shield ——◇isoSPI\_P LV Side isoSPI\_P 2 —DDCDC\_13V Shield 3 isoSPI\_M 4 — ⇒isoSPI\_N Charger Connector 3x 22AWG CHRG\_12VD→X CHRG\_CANHD-CHRG\_CANLD CHRG\_CANSD— CHRG\_SAFETYD— CHRG\_DETECT2D-CHRG\_GNDD CHRG\_DETECT1< For authors and other info, contact Chief Electrical Engineer https://github.com/Northeastern-Electric-Racing/NER Northeastern Electric Racing Sheet: /Accumulator Container/LV\_Plate/ File: LV\_Plate.kicad\_sch Title: LV Plate Size: B Date: 2023-01-25 KiCad E.D.A. kicad (6.0.10-0) Rev: 2 ld: 6/13





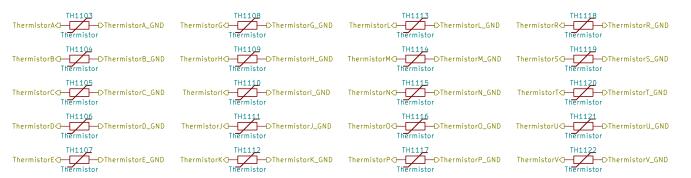


Thermistors F & Q are critical, and are therefore independently grounded to the BMS. They are located in the center of the cell pack.

TH1101
ThermistorF\_GND
Thermistor

TH1102
ThermistorQ DThermistorQ GND

\*Thermistors are distributed evenly throughout the segment. Each group of 5 has a common ground on the thermistor expansion.



For authors and other info, contact Chief Electrical Engineer https://github.com/Northeastern-Electric-Racing/NER

Northeastern Electric Racing

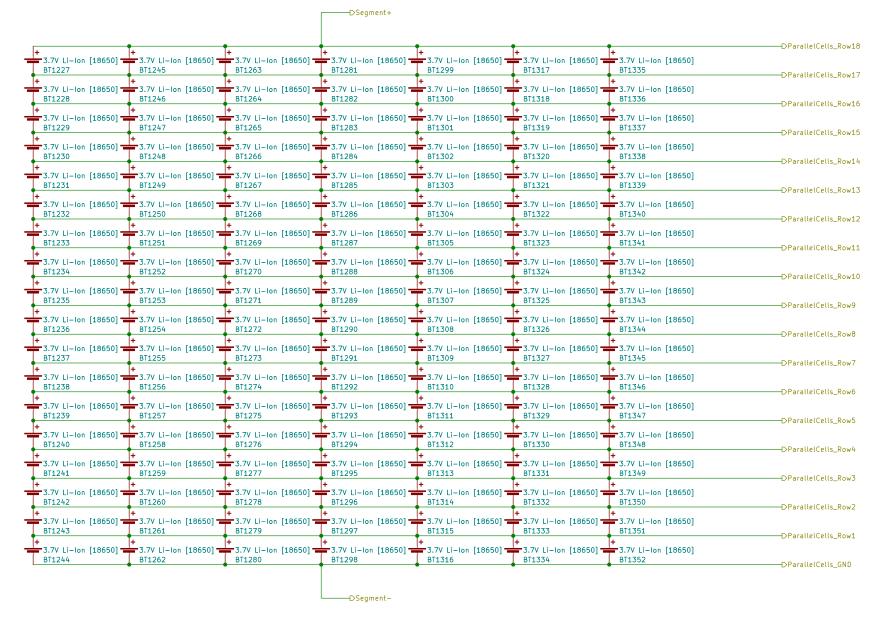
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Title: Accumulator Segment

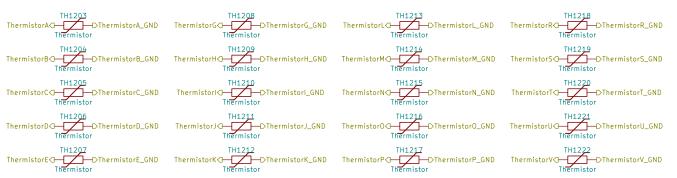
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 Date: 2020-11-11
 Rev: 3

 KiCad E.D.A. kicad (6.0.10-0)
 Id: 11/13



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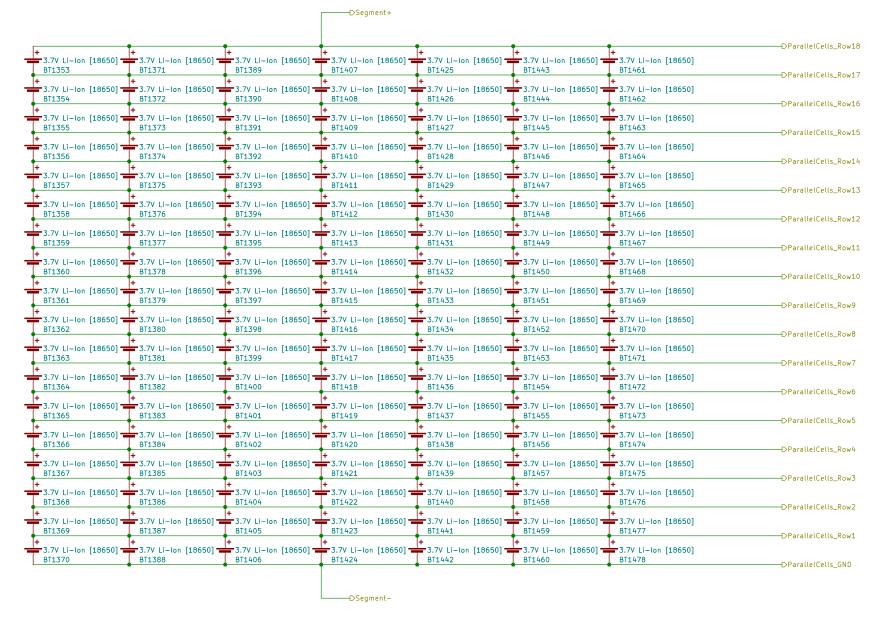
## Northeastern Electric Racing

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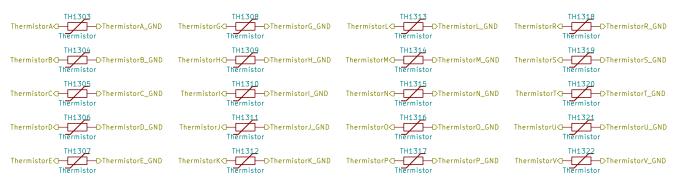
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Size: B	Date: 2020-11-11	Rev: 3
KiCad E.D.A.	kicad (6.0.10-0)	ld: 12/13



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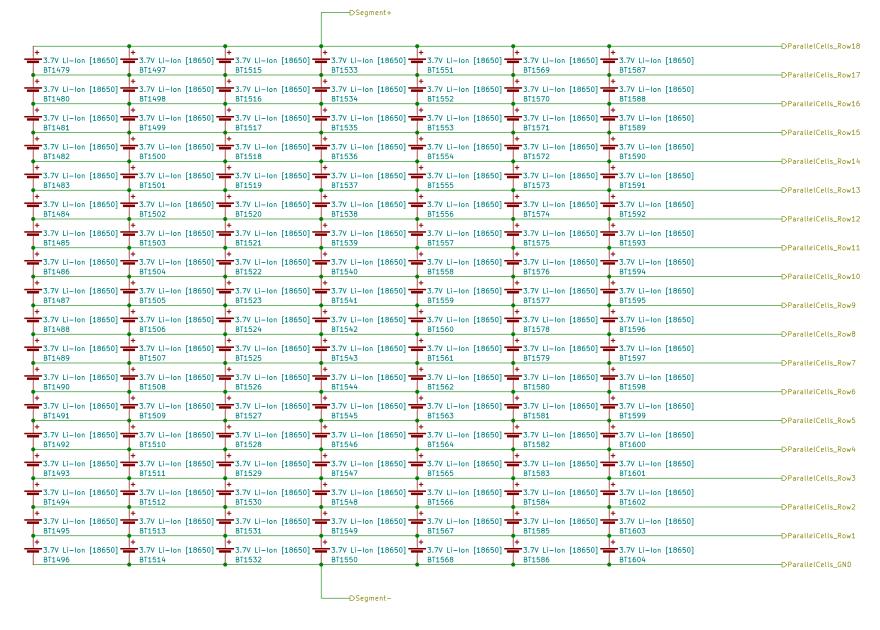
For authors and other info, contact Chief Electrical Engineer https://github.com/Northeastern-Electric-Racing/NER

Northeastern Electric Racing

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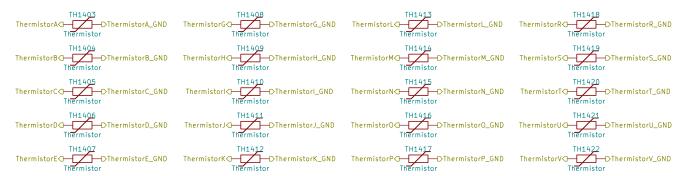
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Size: B	Date: 2020-11-11	Rev: 3
KiCad E.D.A.	kicad (6.0.10-0)	ld: 13/13



Thermistors F & Q are critical, and are therefore independently grounded to the BMS. They are located in the center of the cell pack.

\*Thermistors are distributed evenly throughout the segment. Each group of 5 has a common ground on the thermistor expansion.



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Northeastern Electric Racing

Sheet: /Accumulator Container/Segment\_Layer/BatterySegment4/

ile: BatterySegment.kicad\_sch Title: Accumulator Segment

Size: B	Date: 2020-11-11	Rev: 3
KiCad E.D.A. ki	cad (6.0.10-0)	ld: 14/13

