

		0	1	0	0	16
		0	1	0	1	32
		0	1	1	0	64
		0	1	1	1	128
		1	0	0	0	256
		Other values				Retention
[4]	-	Are reserved				
[5]	CKOEN0 Set the system clock is in PB0 Pin output					
[6]	CKOEN1 Set the system clock is in PE5 Pin output					
[7]	WCE	<p>Change clock prescaler clock changing control CLKPR Before the other bits of the register, you must first be provided separately</p> <p>CKWEN for 1 , Then in four cycles after the system, to set the other bits. After four cycles, CKWEN Automatically cleared.</p>				