## Features

High performance with low power consumption options

Advanced RISC Architecture

131 Instructions over 80% execute in a single cycle

32 x 8 General purpose working registers

Up to 32MHz with 32 MIPS

Internal single cycle multiplier (8x8)

Non-Volitile program and data memory

32Kbytes On-chip programmable Flash Memory

2Kbytes SRAM

Programmable EEPROM supports byte access

Program encryption

Two independently prescaled 8 Bit timers

Input capture and output compare modes

Internal 32KHz oscillator for Real Time Clock function

Up to 9 PWM outputs, Programmable dead-band control

12 Bit High Speed ADC with up to 12 channels

Optional internal or external voltage reference

Programmable Gain Amplifier (X 1/8/16/32)

Differential input channels

Automatic threshold voltage monitoring mode

Internal 1.024V / 2.048V / 4.096V Reference +-1%

8 Bit programmable DAC

Watchdog timer

Synchronous and Asynchronous serial Interface

SPI with programmable master/slave TWI compatible

12C with Master/Slave mode

16 Bit arithmetic accelerator unit (DSC)

SWD debug interface

POR built in power on reset circuit

LVD Low voltage detection circuit



## 8-bit LGT8XM

RISC Microcontroller with In-System Programmable FLASH Memory

## *LGT8F88P LGT8F168P LGT8F328P*

Data book Version 1.0.4

Applications

Motor-driven

automation and

control home appliances

Packaging QFP48/32L, SSOP20L

Power

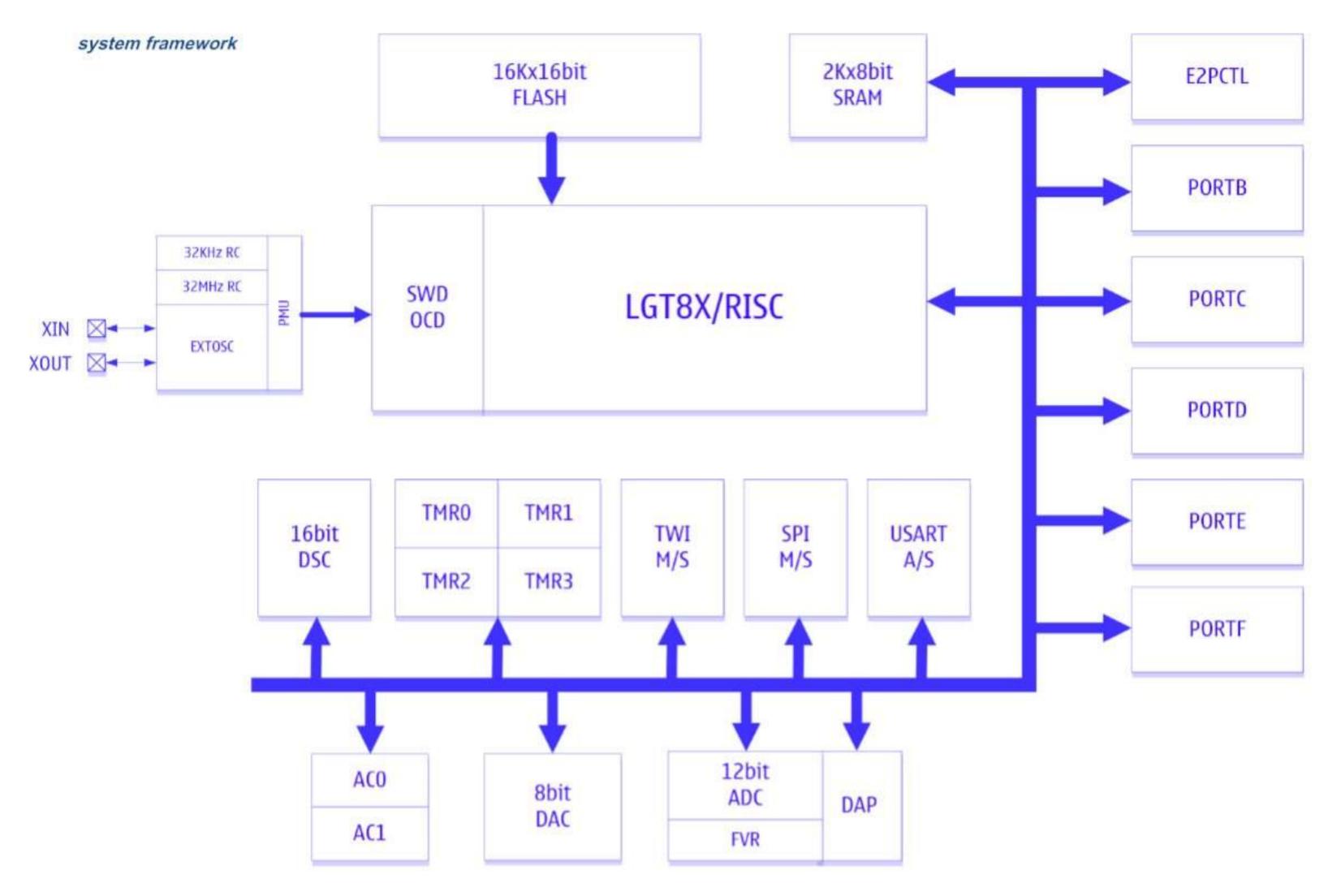
1uA@3.3V

Features

Voltage 1.8V ~ 5.5V

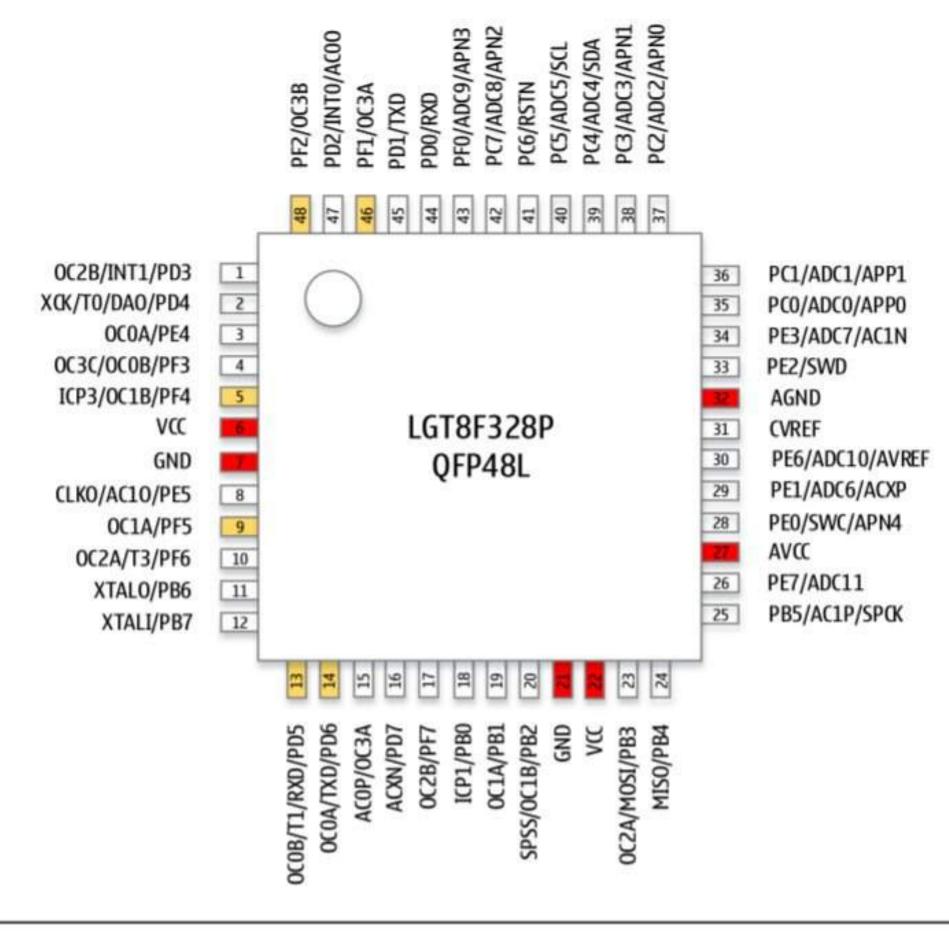
Frequency  $0 \sim 32MHz$ Temperature  $-40C \sim +85C$ 

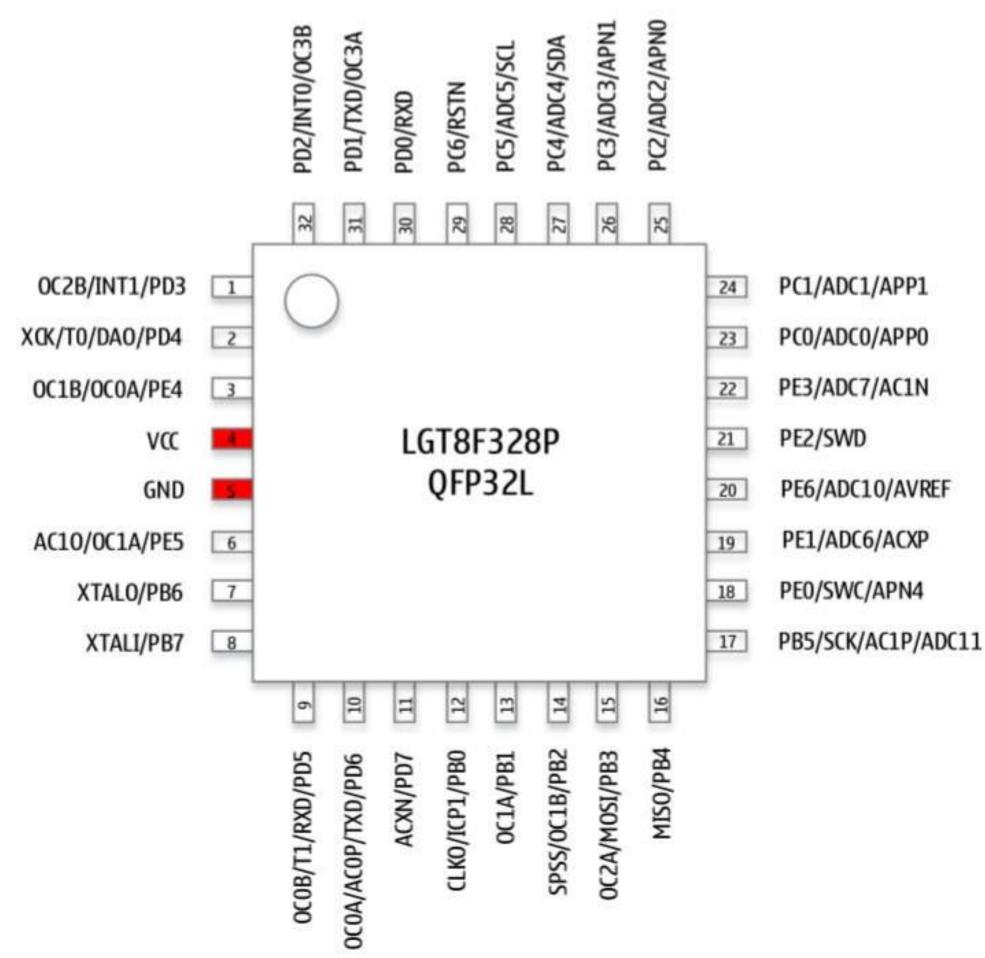
HMB ESD > 4KV

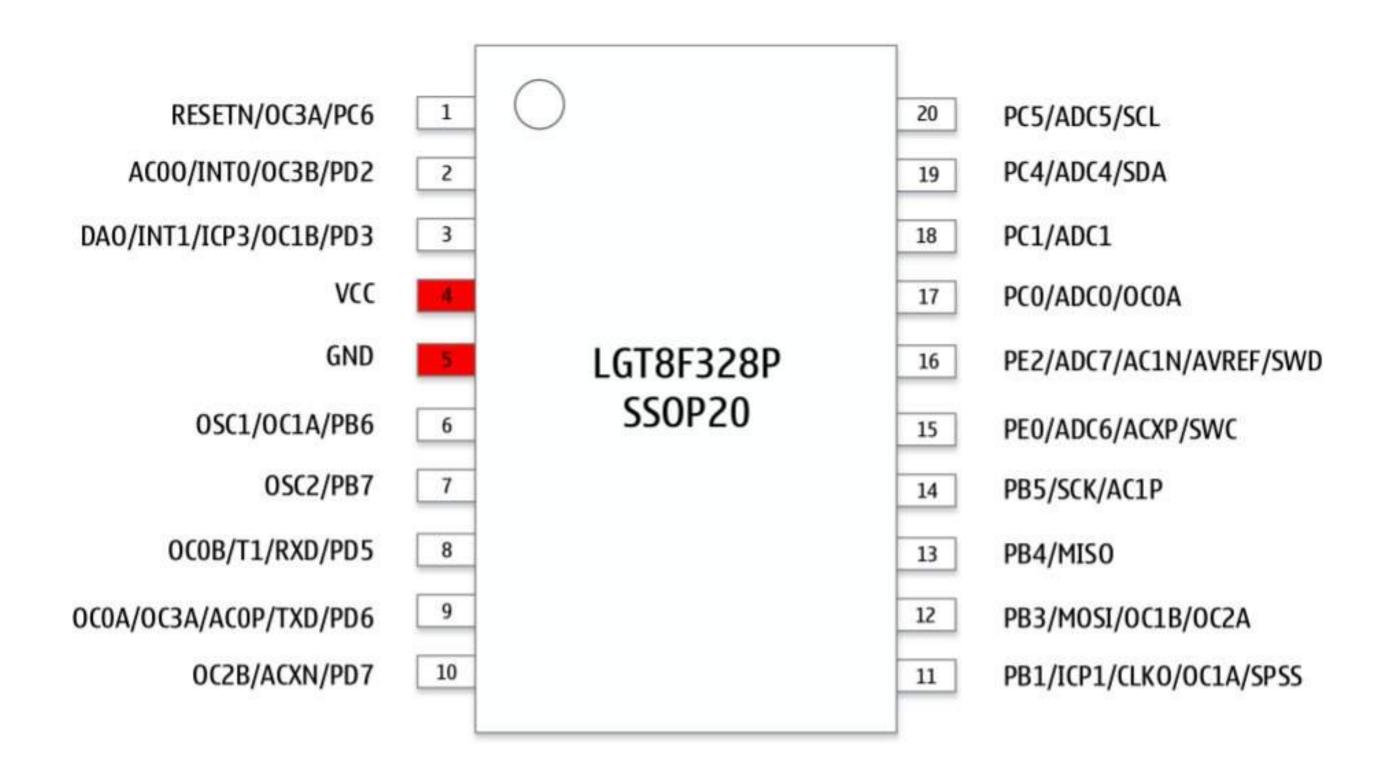


Module Name	Module features		
SWD	Debug module, debugging and ISP Features		
LGT8X	8Bit high performance RISC Kernel		
E2PCTL	FLASH Data Access Interface Controller		
PMU	Power management module		
PORTB/C/D/E/F	O/E/F General-purpose programmable input and output ports		
DSC	16 Digit arithmetic acceleration unit		
ADC	8 Channel 12 Bit ADC with programmable gain		
DAP	Differential amplifier		
IVREF	1.024V / 2.048V / 4.096V Internal Reference		
ACO/1	Analog comparator		
TMRO/ 1/2/3	8/16 Bit timer / counter, PWM Controller		
WDT	Reset Watchdog module		
SPIM/S	Master-slave SPI Controller		
TWIM/S	Master-Slave two-wire interface controller, compatible I2C protocol		
USART	Synchronous / Asynchronous Serial Transceiver		
DAC	8 Bit DAC		

## Package defined







QFP48	QFP32	SSOP20	
	08	07	PB7/XTALI
12			PB7: I/O Pin B7
			XTALI: Crystal Oscillator Input
13			PD5/RXD*/T1/0C0B
	09	08	PD5: I/O Pin D5
			RXD: USART Receive Data
			T1: Timer 1 External Clock Input
			OCOB: Timer/Counter 0 Output Compare Match B
	10	09	PD6/TXD*/OCOA
14			PD6: I/O Pin D6
			TXD: USART Transmit Data
			OCOA: Timer/Counter 0 Output Compare Match A
			ACOP/OC3A
15			ACOP: Analog Comparator 0 Positive Input
			OC3A: Timer/Counter 3 Output Compare Match A
127.42			PD7/ACXN
16	11		PD7: I/O Pin D7
		10	ACXN: Analog Comparator 0/1 Inverting Input
			PF7/0C2B
17	-		PF7: I/O Pin F7
			OC2B: Timer/Counter 2 Output Compare Match B
18	12	11	PBO/ICP1
			PB0: I/O Pin B0
			ICP1: Timer 1 Capture Input
10	13		PB1/OC1A
19			PB1: I/O Pin B1
			OC1A: Timer/Counter 1 Output Compare Match A
	14	12	PB2/OC1B/SPSS
20			PB2: I/O Pin B2  OCI P: Times/Counter 1 Output Compare Match P
Wilders			OC1B: Timer/Counter 1 Output Compare Match B SPSS: SPI Slave Select
21	_	_	GND
22	-	-	VCC
			PB3/MOSI/OC2A
	15	12	PB3: I/O Pin B3
23			MOSI: SPI Master Output Slave Input
			OC2A: Timer/Counter 2 Output Compare Match A
	16	13	PB4/MISO
24			PB4: I/O Pin B4
			MISO: SPI Master Input Slave Output
	17	14	PB5/SPCK/AC1P
25			PB5: I/O Pin B5
25			SPCK: SPI Clock
			AC1P: Analog Comparator 1 Noninverting Input
QFP48	QFP32	SSOP20	
			-6-

QFP48	QFP32	SSOP20	
		-	PE7/ADC11
26	•		PE7: I/O Pin E7
			ADC11: ADC Input Channel 11
27	-	-	AVCC: Internal Analog Circuit Positive Power Supply
		15	PEO/SWC/APN4
28	18		PEO: I/O Pin E0
			SWC: SWD Debug Interface Clock
			APN4: Differential Amplifier Inverting Input Channel 4
29	19		PE1/ADC6/ACXP
			PE1: I/O Pin E1
			ADC6: ADC Input Channel 6
			ACXP: Analog Comparator 0/1 Noninverting Input
			PE6/ADC10/AVREF
30	20	16	PE6: I/O Pin E6
			ADC10: ADC Input Channel 10
21			AVREF: ADC External Reference Voltage Input
31	-	-	CVREF: ADC Reference Voltage External Filter Capacitor (0.1uF)
32	-	-	AGND: Internal Analog Circuit Power Supply Ground
22	21		PE2/SWD
33	21	16	PE2: I/O Pin E2
	22		SWD: SWD Debug Interface Data
			PE3/ADC7/AC1N PE3: I/O Pin E3
34			ADC7: ADC Input Channel 7
			AC1N: Analog Comparator 1 Inverting Input
	23	17	PCO/ADCO/APPO
			PCO: I/O Pin CO
35			ADCO: ADC Input Channel 0
			APPO: Differential Amplifier Channel 0 Positive Input
	24	18	PC1/ADC1/APP1
26			PC1: I/O Pin C1
36			ADC1: ADC Input Channel 1
			APP1: Differential Amplifier Channel 1 Positive Input
	25	•	PC2/ADC2/APN0
37			PC2: I/O Pin C2
31			ADC2: ADC Input Channel 2
			APNO: Differential Amplifier Channel 0 Inverting Input
	26		PC3/ADC3/APN1
38			PC3: I/O Pin C3
			ADC3: ADC Input Channel 3
			APN1: Differential Amplifier Channel 1 Inverting Input
QFP48	QFP32	SSOP20	

QFP48	QFP32	SSOP20	
	27	19	PC4/ADC4/SDA  PC4- 1/O Din C4
39			PC4: I/O Pin C4 ADC4: ADC Input Channel 4
			SDA: I2C Data
		20	PC5/ADC5/SCL
40	28		PC5: I/O Pin C5
			ADC5: ADC Input Channel 5
			SCL: 12C Clock
41	29	1	PC6/RESETN PC6: I/O Pin C6
			RESETN: External Reset Input
	-	-	PC7/ADC8/APN2
			PC7: I/O Pin C7
42			ADC8: ADC Input Channel 8
			APN2: Differential Amplifier Channel 2 Inverting Input
	:=	·-	PFO/ADC9/APN3
43			PF0: I/O Pin F0
			ADC9: ADC Input Channel 9
			APN3: Differential Amplifier Channel 3 Inverting Input
44	30	•	PDO/RXD
44			PD0: I/O Pin D0  RXD: USART Receive
			PD1/TXD
45		•	PD1: I/O Pin D1
43			TXD: USART Transmit
	31	1	PF1/0C3A
46			PF1: I/O Pin F1
			OC3A: Timer/Counter 3 Output Compare Match A
	32	2	PD2/INT0/AC00
47			PD2: I/O Pin D2
****			INTO: External Interupt 0 Input
			ACOO: Analog Comparator 0 Output
48			PF2/0C3B
			PF2: I/O Pin F2  OC3B: Timer/Counter 3 Output Compare Match B
QFP48	QFP32	SSOP20	ocsb. Timely counter's output compare materia
QI F40	VI POZ	330FZ0	