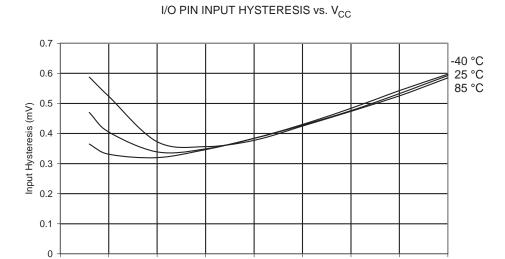
Figure 27-165.I/O Pin Input Hysteresis vs. V<sub>CC</sub>.

1.5



3.5

 $V_{CC}(V)$ 

4.5

5.5

## Figure 27-166. Reset Input Threshold Voltage vs. $V_{CC}$ ( $V_{\emph{IH}}$ , I/O Pin read as '1').

3

2.5

