port C Alternate Function

Pin	Multiplexing Function Description
PC7	ADC8 (ADC Input channel 8) APN2 (DAP Inverting input 2) PCINT15 (Pin Change input 15)
PC6	RESETN (External reset input) PCINT14 (Pin Change input 14)
PC5	ADC5 (ADC Input channel 5) SCL (TWI Clock line) PCINT13 (Pin Change input 13)
PC4	ADC4 (ADC Input channel 4) SDA (TWI Data line) PCINT12 (Pin Change input 12)
PC3	ADC3 (ADC Input channel 3) PCINT11 (Pin Change input 11)
PC2	ADC2 (ADC Input channel 2) PCINT10 (Pin Change input 10)
PC1	ADC1 (ADC Input channel 1) PCINT9 (Pin Change input 9)
PC0	ADC0 (ADC Input channel 0) PCINT8 (Pin Change input 8)

ADC8 / APN2 / PCINT15- port C Pin 6

ADC8: ADC External input channels 8

APN2: Reverse input port of the differential amplifier 2

PCINT15: Pin change interrupt. Close this pin after the external reset input function, PC7 It can be used as an external interrupt source.

RESETN / PCINT14- port C Pin 6

RESETN: An external reset pin. After the power-on reset, this pin defaults to an external reset function. able to pass IOCR Close register external reset function. After closing the external reset function, this pin as a general I / O use. But note that, in the power-on reset and other processes, this pin defaults to a reset input, so if you need to use this common pin I / O Function, can not affect the external circuit of the chip and the power reset process, it proposed that this pin is configured as an output function I / O And adding a suitable external pull-up resistor.

PCINT14: Pin change interrupt. Close this pin after the external reset input function, PC6 It can be used as an external interrupt source.

SCL / ADC5 / PCINT13- port C Pin 5

SCL: TWI Interface clock signal. TWCR Register TWEN position 1 After enabling TWI interface, PC5 will be TWI Control, become TWI Clock signal interface.

ADC5: ADC Input channel 5 . DIDR Close register number of multiplexed analog I / O The digital function to avoid the digital unit