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|---|-------|---|
| | | <p>When, that is, TIFR3 in OCF3A When the bit is set, an interrupt is generated. when OCIE3A Bit "0" Time, TC3 Output Compare A Match interrupts are disabled.</p> |
| 0 | TOIE3 | <p>TC3 Overflow interrupt enable bit.</p> <p>when TOIE3 Bit "1" And Global Interrupt Set, TC3 Overflow interrupt is enabled. when TC3 Overflow occurs, that is, TIFR3 in of TOV3 When the bit is set, an interrupt is generated. when TOIE3 Bit "0" Time, TC3 Overflow interrupts are disabled.</p> |

TIFR3-TC3 Interrupt Flag Register

| TIFR3 - TC3 Interrupt Flag Register | | | | | | | | |
|-------------------------------------|-------|--|-------|---|----------------|-------|-------|-------|
| address: 0x38 | | | | | Defaults: 0x00 | | | |
| Bit | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| Name | - | - | ICF3 | - | - | OCF3B | OCF3A | TOV3 |
| R / W | - | - | R / W | - | - | R / W | R / W | R / W |
| Bit | Name | description | | | | | | |
| 7: 6 | - | Reservations. | | | | | | |
| 5 | ICF3 | <p>Input capture flag. When the input capture event occurs, ICF3 Flag is set. when ICR3 It is counted as TOP Value, and the count value reaches TOP Value, ICF3 Flag is set. If the ICIE1 for "1" And the Global interrupt flag is set, it will generate an interrupt input capture. ICF3 Flag will not be cleared automatically, you need software ICF3 Write bit "1" Cleared.</p> | | | | | | |
| 4 | - | Reservations. | | | | | | |
| 3 | OCF3C | <p>Output Compare C Matching flag. when TCNT3 equal OCR3C , The comparison unit signals a match, the comparison flag is set and OCF3C . If then the output compare interrupt enable OCIE3C for "1" And the Global interrupt flag is set, it will generate an output compare interrupt. OCF3C Flag will not be cleared automatically, you need software OCF3C Write bit "1" This bit is cleared.</p> | | | | | | |
| 2 | OCF3B | <p>Output Compare B Matching flag. when TCNT3 equal OCR3B , The comparison unit signals a match, the comparison flag is set and OCF3B . If then the output compare interrupt enable OCIE3B for "1" And the Global interrupt flag is set, it will generate an output compare interrupt. OCF3B Flag will not be cleared automatically, you need software OCF3B Write bit "1" Cleared.</p> | | | | | | |
| 1 | OCF3A | <p>Output Compare A Matching flag. when TCNT3 equal OCR3A , The comparison unit signals a match, the comparison flag is set and OCF3A . If then the output compare interrupt enable OCIE3A for "1" And the Global interrupt flag is set, it will generate an output compare interrupt. OCF3A Flag will not be cleared automatically, you need software OCF3A Write bit "1" Cleared.</p> | | | | | | |
| 0 | TOV3 | <p>Overflow flag.</p> <p>When the counter overflows, the overflow flag is set TOV3 . If this time overflow interrupt enable TOIE3 for "1" And the Global interrupt flag is set, it will generate an overflow interrupt.</p> <p>TOV3 Flag will not be cleared automatically, you need software TOV3 Write bit "1" Cleared.</p> | | | | | | |