

The storage unit

Outline

This chapter describes LGT8FX8P Series of different internal storage units. LGT8XM Architecture supports two main types of internal storage space, namely data storage and program memory space. LGT8FX8P Interior also contains data FLASH May be implemented inside the controller EEPROM Data storage interfaces. In addition, LGT8FX8P The system also includes a special memory unit for storing system configuration information and the global chip device number (GUID) .

LGT8FX8P Series chip contains LGT8F88P / 168P / 328P Four different models; four models of peripherals, and is fully compatible packaging, the difference is FLASH Internal program memory space and data SRAM The following table compares the clear description of the LGT8FX8P Different series chip memory configurations:

DEVICE	FLASH	SRAM	E2PROM	Interrupt vector
LGT8F88P	8KB	1KB	2KB	1 Instruction words
LGT8F168P	16KB	1KB	4KB	2 Instruction words
LGT8F328P	32KB	2KB	It can be configured to 0K / 1K / 2K / 4K / 8K (versus FLASH shared)	2 Instruction words

LGT8F328P No separate internal analog for E2PROM Interface FLASH Space; means for simulation E2PROM The storage space program FLASH Sharing, the user depending on the application needs, to select the appropriate configuration.

Since the analog E2PROM Interface uses unique realization, the system requires twice the program FLASH Space simulation E2PROM Storage space, such as for LGT8F328P , When you configure a 1KB of E2PROM Space, there will be 2KB Bytes of program space is reserved, the rest 30KB of FLASH Space for storing programs.

LGT8F328P program FLASH versus E2PROM Shared configuration table:

DEVICE	FLASH	E2PROM
LGT8F328P	32KB	0KB
	30KB	1KB
	28KB	2KB
	24KB	4KB
	16KB	8KB

System Programmable FLASH Program storage unit

LGT8FX8P Internal microcontrollers include 8K / 16K / 32K Byte on-chip programmable line FLASH A program storage unit.

program FLASH To ensure that at least 100,000 Or more of erase cycles. LGT8FX8P Internal integration FLASH Interface controller can be implemented in system programming (ISP) And since the upgrade functions of the program. Specific implementation details, please refer to the chapter on FLASH Description of the controller interface portion.

You can also program space LPM Instructions direct access (read), this feature can be achieved constant find application-related