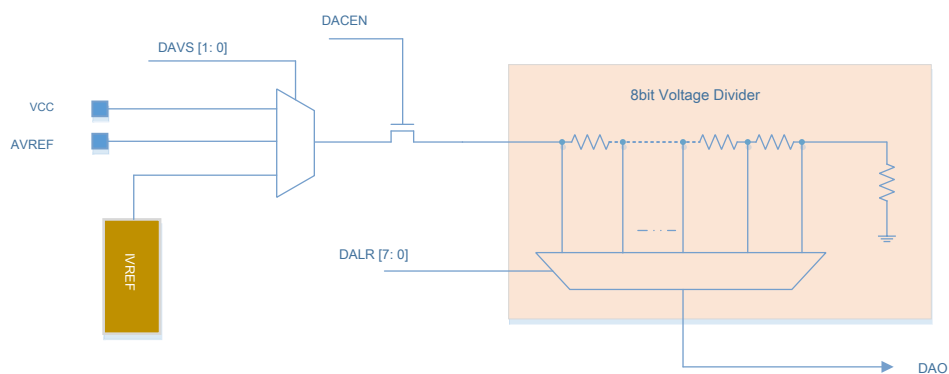


DAC (DAC)

- 8 Bit analog conversion output
- DAC Analog comparator output can be used as a reference input
- stand by DAC Output to an external port (DAO)
- Optional VCC / AVREF / IVREF Splitting power

Overview

LGT8FX8P The internal integration of a 8 Bit programmable DAC (DAC) . DAC Reference may be selected as the power input, the internal reference voltage source from the system operating power from the chip or external port AVREF Input. DAC Selected as the output of internal comparator AC0 / 1 The input source may be directly output to the external pins on the chip as an external reference. when DAC Output to external pins, not directly for driving a load, required by the voltage follower circuit, or other similar drive. DAC An internal configuration as shown below:



Register Definition

DACON - DAC Control register

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address: 0xA0					0000_0000			
Bit	7	6	5	4	3	2	1	0
	-	-	-	-	DACEN DAOE		DAVS1 DAVS0	
R / W	-	-	-	-	R / WW / R		R / WW / R	
Bit	Name description							
7: 4	-	Retention						
3	DACEN	DAC Enable Control bit 1 = Enable DAC Module 0 = Disable DAC Module						
2	DAOE	DAC Output enable control port to the outside 1 = Enable DAC Output to an external terminal PD4 0 = Ban DAC Output to an external port						
1	DAVS1	DAC Select bit reference voltage source 10						
	DAVS0	DAC Select bit reference voltage source 0 . [DVS1, DVS0] =						