ATmega48P/88P/168P/328P

After a repeated START condition (state 0x10) the 2-wire Serial Interface can access the same Slave again, or a new Slave without transmitting a STOP condition. Repeated START enables the Master to switch between Slaves, Master Transmitter mode and Master Receiver mode without losing control of the bus.

Table 19-2. Status codes for Master Transmitter Mode

| Status Code | Status of the 2-wire Serial Bus | Application Software Response | | | | | |
|--------------------------|--|-------------------------------|---------|-----|-----------|----------|--|
| (TWSR) Prescaler Bits | | To/from TWDR | To TWCR | | | | |
| are 0 | and 2-wire Serial Interface Hardware | | STA | STO | TWIN T | TWE A | Next Action Taken by TWI Hardware |
| 0x08 | A START condition has been transmitted | Load SLA+W | 0 | 0 | 1 | Х | SLA+W will be transmitted; ACK or NOT ACK will be received |
| 0x10 | A repeated START condition has been transmitted | Load SLA+W or | 0 | 0 | 1 | Х | SLA+W will be transmitted; ACK or NOT ACK will be received |
| | | Load SLA+R | 0 | 0 | 1 | Х | SLA+R will be transmitted; Logic will switch to Master Receiver mode |
| 0x18 | SLA+W has been transmitted; ACK has been received | Load data byte or | 0 | 0 | 1 | Х | Data byte will be transmitted and ACK or NOT ACK will be received |
| | | No TWDR action or | 1 | 0 | 1 | Х | Repeated START will be transmitted |
| | | No TWDR action or | 0 | 1 | 1 | Х | STOP condition will be transmitted and TWSTO Flag will be reset |
| | | No TWDR action | 1 | 1 | 1 | Х | STOP condition followed by a START condition will be transmitted and TWSTO Flag will be reset |
| 0x20 | SLA+W has been transmitted; NOT ACK has been received | Load data byte or | 0 | 0 | 1 | Х | Data byte will be transmitted and ACK or NOT ACK will be received |
| | | No TWDR action or | 1 | 0 | 1 | X | Repeated START will be transmitted |
| | | No TWDR action or | 0 | 1 | 1 | Х | STOP condition will be transmitted and |
| | | No TWDR action | 1 | 1 | 1 | Х | TWSTO Flag will be reset STOP condition followed by a START condition will be transmitted and TWSTO Flag will be reset |
| 0x28 | Data byte has been transmit- ted: | Load data byte or | 0 | 0 | 1 | Х | Data byte will be transmitted and ACK or NOT ACK will be received |
| | ACK has been received | No TWDR action or | 1 | 0 | 1 | Х | Repeated START will be transmitted |
| | | No TWDR action or | 0 | 1 | 1 | Х | STOP condition will be transmitted and TWSTO Flag will be reset |
| | | No TWDR action | 1 | 1 | 1 | Х | STOP condition followed by a START condition will be transmitted and TWSTO Flag will be reset |
| 0x30 | Data byte has been transmit- ted; | Load data byte or | 0 | 0 | 1 | Х | Data byte will be transmitted and ACK or NOT ACK will be received |
| | NOT ACK has been received | No TWDR action or | 1 | 0 | 1 | Х | Repeated START will be transmitted |
| | | No TWDR action or | 0 | 1 | 1 | Х | STOP condition will be transmitted and TWSTO Flag will be reset |
| | | No TWDR action | 1 | 1 | 1 | Х | STOP condition followed by a START condition will be transmitted and TWSTO Flag will be reset |
| 0x38 | Arbitration lost in SLA+W or data bytes | No TWDR action or | 0 | 0 | 1 | Х | 2-wire Serial Bus will be released and not addressed Slave mode entered |
| | | No TWDR action | 1 | 0 | 1 | Х | A START condition will be transmitted when the bus becomes free |

