port B Input Data Register - PINB

PINB - port B Input data register								
PINB: 0x03 (0x23)				Defaults: 0x00				
PINB	PINB7	PINB6	PINB5	PINB4	PINB3	PINB2	PINB1	PINB0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	PINB	B Port status register group. read PINB Direct access to the current state of the port; write PINBn Place 1 The flip PORTBn The output state						

port C Output Data Register - PORTC

PORTC - port C Output data register								
PORTC: 0x08 (0x28)				Defaults: 0x00				
PORTC	PC7	PC6	PC5	PC4	PC3	PC2	PC1	PC0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	PORTC	C Port output register group						

port C Direction Register - DDRC

DDRC - port C Direction Register								
DDRC: 0x07 (0x27)				Defaults: 0x00				
DDRC	DDC7	DDC6	DDC5	DDC4	DDC3	DDC2	DDC1	DDC0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	DDC	C Group port direction control bit; 1 = Output, 0 = Entry						

port C Input Data Register - PINC

PINC - port C Input data register								
PINB: 0x06 (0x26)				Defaults: 0x00				
PINC	PINC7	PINC6	PINC5	PINC4	PINC3	PINC2	PINC1	PINC0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	PINC	C Port status register group; read PINC Get the current status of the write port PINC Will flip the current output port						

port D Output Data Register - PORTD

PORTD - port D Output data register								
PORTD: 0x0B (0x2B)				Defaults: 0x00				
Bits	PD7	PD6	PD5	PD4	PD3	PD2	PD1	PD0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								