

6.6	Calibrated Internal RC Oscillator .....	33
6.7	128 kHz Internal Oscillator .....	33
6.8	External Clock .....	34
6.9	Clock Output Buffer .....	35
6.10	Timer/Counter Oscillator .....	35
6.11	System Clock Prescaler .....	35
6.12	Register Description .....	37
<b>7</b>	<b>Power Management and Sleep Modes .....</b>	<b>39</b>
7.1	Sleep Modes .....	39
7.2	BOD Disable .....	40
7.3	Idle Mode .....	40
7.4	ADC Noise Reduction Mode .....	40
7.5	Power-down Mode .....	41
7.6	Power-save Mode .....	41
7.7	Standby Mode .....	41
7.8	Extended Standby Mode .....	41
7.9	Power Reduction Register .....	42
7.10	Minimizing Power Consumption .....	42
7.11	Register Description .....	44
<b>8</b>	<b>System Control and Reset .....</b>	<b>46</b>
8.1	Resetting the AVR .....	46
8.2	Reset Sources .....	46
8.3	Power-on Reset .....	47
8.4	External Reset .....	48
8.5	Brown-out Detection .....	48
8.6	Watchdog System Reset .....	49
8.7	Internal Voltage Reference .....	49
8.8	Watchdog Timer .....	50
8.9	Register Description .....	54
<b>9</b>	<b>Interrupts .....</b>	<b>57</b>
9.1	Interrupt Vectors in ATmega48P .....	57
9.2	Interrupt Vectors in ATmega88P .....	59
9.3	Interrupt Vectors in ATmega168P .....	62
9.4	Interrupt Vectors in ATmega328P .....	65
9.5	Register Description .....	68