

		<p>When set PCINT18 Bit "1" Time, PD2 Pin change interrupt is enabled. PD2 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have</p> <p>PCI2 Interrupted. When set PCINT18 Bit "0" Time, PD2 Pin change interrupts are disabled.</p>
1	PCINT17 Pin change enable mask 17 .	<p>When set PCINT17 Bit "1" Time, PD1 Pin change interrupt is enabled. PD1 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have</p> <p>PCI2 Interrupted. When set PCINT17 Bit "0" Time, PD1 Pin change interrupts are disabled.</p>
0	PCINT16 Pin change enable mask 16 .	<p>When set PCINT16 Bit "1" Time, PD0 Pin change interrupt is enabled. PD0 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have</p> <p>PCI2 Interrupted. When set PCINT16 Bit "0" Time, PD0 Pin change interrupts are disabled.</p>

PCMSK3 - Pin change interrupt mask register 3

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address: 0x73					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	PCINT31	PCINT30	PCINT29	PCINT28	PCINT27	PCINT26	PCINT25	PCINT24
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit	Name description							
7	<p>PCINT31 Pin change enable mask 31 .</p> <p>When set PCINT31 Bit "1" Time, PE7 Pin change interrupt is enabled. PE7 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have</p> <p>PCI3 Interrupted. When set PCINT31 Bit "0" Time, PE7 Pin change interrupts are disabled.</p>							
6	<p>PCINT30 Pin change enable mask 30 .</p> <p>When set PCINT30 Bit "1" Time, PE6 Pin change interrupt is enabled. PE6 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have</p> <p>PCI3 Interrupted. When set PCINT30 Bit "0" Time, PE6 Pin change interrupts are disabled.</p>							
5	<p>PCINT29 Pin change enable mask 39 .</p> <p>When set PCINT29 Bit "1" Time, PE5 Pin change interrupt is enabled. PE5 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have</p> <p>PCI3 Interrupted. When set PCINT29 Bit "0" Time, PE5 Pin change interrupts are disabled.</p>							
4	<p>PCINT28 Pin change enable mask 28 .</p> <p>When set PCINT28 Bit "1" Time, PE4 Pin change interrupt is enabled. PE4 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have</p> <p>PCI3 Interrupted. When set PCINT28 Bit "0" Time, PE4 Pin change interrupts are disabled.</p>							
3	PCINT27 Pin change enable mask 27 .							