OC3C / OC0B / PCINT35- port F Pin 3

OC0B: Timer / Counter 0 of B Group match output. Output selection by PMX0 Register control

OC3C: Timer / Counter 3 of C Group match output

PCINT35: Pin Change Interrupt 35

OC3B / PCINT34- port F Pin 2

OC3B: Timer / Counter 3 of B Group match output

PCINT34: Pin Change Interrupt 34

OC3A / PCINT33- port F Pin 1

OC3A: Timer / Counter 3 of B Group match output. Output selection by PMX1 Register control

PCINT33: Pin Change Interrupt 33

ADC9 / APN3 / PCINT32- port F Pin 0

ADC9: ADC External input channel mode 9

APN3: Inverting input of the differential amplifier 3

PCINT32: Pin Change Interrupt 32

Register Definition

port B Output Data Register - PORTB

PORTB - port B Output data register								
PORTB: 0x05 (0x25)				Defaults: 0x00				
Bits	PB7	PB6	PB5	PB4	PB3	PB2	PB1	PB0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	PORTB	B Port output register group						

port B Direction Register - DDRB

DDRB - port B Direction Register								
DDRB: 0x04 (0x24)				Defaults: 0x00				
DDRB	DDB7	DDB6	DDB5	DDB4	DDB3	DDB2	DDB1	DDB0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	DDB	port B Group direction control bit; 1 = Output, 0 = Entry						