

Register Definition**TC0 Register List**

register	address	Defaults	description
TCCR0A *	0x44	0x00	TC0 Control register A
TCCR0B *	0x45	0x00	TC0 Control register B
TCNT0 *	0x46	0x00	TC0 Count value register
OCR0A *	0x47	0x00	TC0 Output Compare Register A
OCR0B *	0x48	0x00	TC0 Output Compare Register B
DSX0 *	0x49	0x00	TC0 Trigger source control register
DTR0 *	0x4F	0x00	TC0 Dead time register
TIMSK0	0x6E	0x00	Timing counter 0 Interrupt mask register
TIFR0	0x35	0x00	Timing counter 0 Interrupt Flag Register
TCKCSR	0xEC	0x00	TC Clock control and status register

[Note] with "" The register operation at high speed clock and a system clock domains, not with "" The working register only at the system clock domain.

TC0 Control register A- TCCR0A

TCCR0A - TC0 Control register A								
address: 0x44						Defaults: 0x00		
Bit	7	6	5	4	3	2	1	0
	COM0A1	COM0A0	COM0B1	COM0B0	DOC0B	DOC0A	WGM01	WGM00
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit	Name description							
7	COM0A1	TC0 Compare match A High-output mode control. COM0A1 with COM0A0 Together comprise comparison output mode control COM0A [1: 0] To control OC0A The output waveform. in case COM0A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC0A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM0A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.						
6	COM0A0	TC0 Compare match A Low output mode control. COM0A0 with COM0A1 Together comprise comparison output mode control COM0A [1: 0] To control OC0A The output waveform. in case COM0A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC0A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM0A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.						
5	COM0B1	TC0 Compare match B High-output mode control. COM0B1 with COM0B0 Together comprise comparison output mode control COM0B [1: 0] To control OC0B The output waveform. in case COM0B of 1 Position or 2 Bits are set,						