TCCR2B -TC2 Control register B

TCCR2B - TC2 Control register B								
address: 0xB1						Defaults: 0x00		
Bit	7	6	5	4	3	2	1	0
	FOC2A	FOC2B	-	- WGM22		CS22	CS21	CS20
R / W	W	W	-	- R / W		R / W	R / W	R / W

Bit	Name	description
7	FOC2A	**TC2 Force Output Compare A Control bit. In non PWM Mode, the force output by comparing bits FOC2A write "1"** The way to compare match. Forcing compare match will not set OCF2A Flag or reload or clear the timer, but the output pin OC2A Will be in accordance with COM2A It sets the appropriate update, just compare match had really happened. Read FOC2A The return value is always zero.
6	FOC2B	**TC2 Force Output Compare B Control bit. In non PWM Mode, the force output by comparing bits FOC2B write "1"** The way to compare match. Forcing compare match will not set OCF2B Flag or reload or clear the timer, but the output pin OC2B Will be in accordance with COM2B It sets the appropriate update, just compare match had really happened. Read FOC2B The return value is always zero.
5: 4	-	Reservations.
3	WGM22	TC2 Waveform generation mode control high. **WGM22 with WGM20 , WGM21 Together form waveform generation mode control WGM2 [2: 0] , Control and counting of** the counter waveform generation mode, see the specific waveform generation pattern table is described.
2	CS22	**TC2 Clock control high. For selecting a timing counter 2** The clock source.
1	CS21	**TC2 Clock selection control bits. For selecting a timing counter 2 The clock source.**
0	CS20	**TC2 Clock control low. For selecting a timing counter 2** The clock source.

CS2 [2: 0]	description
0	No clock source, stops counting
1	clk t2s
2	clk t2s / 8 From prescaler
3	clk t2s / 32 From prescaler
4	clk t2s / 64 From prescaler
5	clk t2s / 128 From prescaler
6	clk t2s / 256 From prescaler
7	clk t2s / 1024 From prescaler

The following table non PWM Mode (ie, normal mode and CTC Mode), the comparison output of the comparator mode control output waveform.