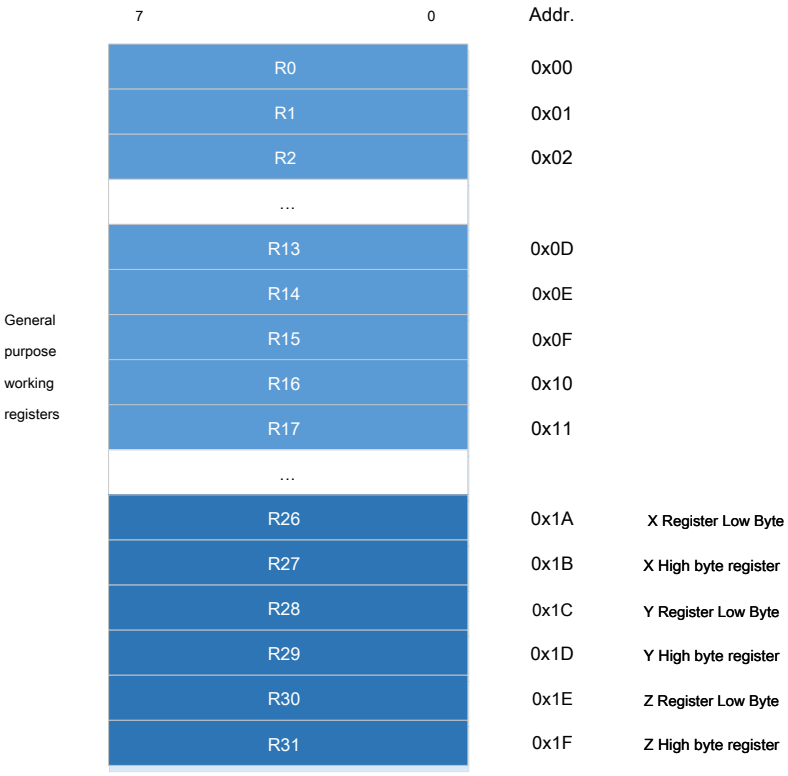
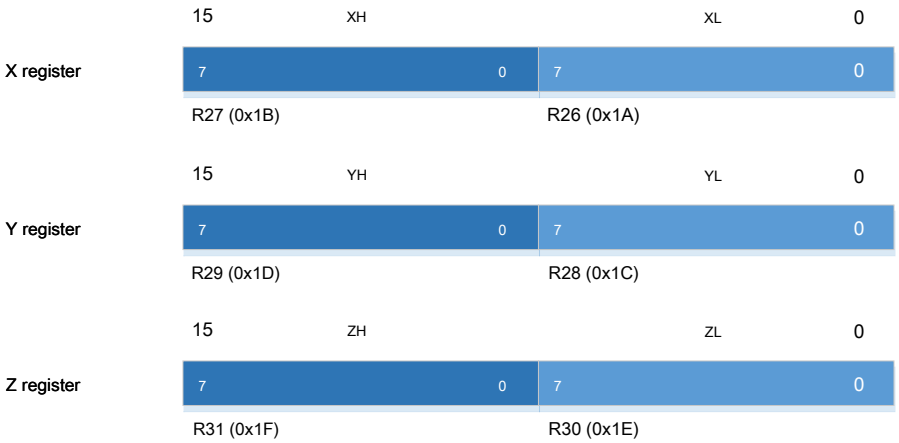
LGT8XM General purpose working registers



Most instructions can directly access to all of the general-purpose working registers, they are also the most single-cycle instruction. As shown above, each register address corresponds to a data memory space, these general purpose registers are mapped into the data storage space. As soon as they do not really exist in SRAM But such storage unified organization mapped to visit them a lot of flexibility. X / Y / Z Index register pointer can be used as any general purpose registers.

X / Y / Z register

register R26 ... R31 It can be combinations of two, three configuration 16 Bit registers. These three 16 Bit register used primarily to access the address pointer indirection, X / Y / Z Register structure as follows:



In the different addressing modes, These registers are used as a fixed offset, the auto-increment and auto-decrement of the address pointer described details, refer to the instruction portion.