3: 0 UBRR [11: 8]	USART High byte portion of register baud rate. **USART Baud rate register comprising UBRRL with UBRRH Two parts, joined together to set the** baud rate. UBRR = {UBRR [11: 8], UBRRL}	
	Operating mode	The baud rate is calculated
	Asynchronous Normal mode	**BAUD = f_{sys} / (16 * (UBRR + 1))**
	Asynchronous speed mode	**BAUD = f_{sys} / (8 * (UBRR + 1))**
	Synchronous Master Mode	**BAUD = f_{sys} / (2 * (UBRR + 1))**

UDR - USART Data register

***UDR* - USART Data register**								
address: 0xC6					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
Name UDR7		UDR6	UDR5	UDR4	UDR3	UDR2	UDR1	UDR0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W

Bit Name	description
7: 0 UDR	USART Transmission and reception of data. **USART Transmission data and reception data buffer shared buffer USART Data register UDR . The data is written UDR That is written to the transmit data buffer, from UDR I.e., the read data read received data buffer. in 5 To 8 Lower frame mode data, the unused 9 Bits are ignored transmitter and the receiver they are set to 0 . Only when UCSRA Register UDRE Flag "1"** When the transmit buffer to write, otherwise the operation of the transmitter to be wrong. When the transmit shift register is empty, the transmitter will transmit data in the buffer is loaded into the transmit shift register, and then serially from the data TxD Pin output. **A receive buffer contains two FIFO Once the receive buffer is read, FIFO It will change its state.**