

Saving Control Register - PRR1

PRR1 - Power control register 1								
PRR1: 0x65					Defaults: 0x00			
PRR1			PRWDT	-	PRTIM3	PREFL	PRPCI	-
R / W			R / W	-	R / W	R / W	R / W	-
Bit Definitions								
[0]	-	Are reserved						
[1]	PRPCI	Set as 1 , Off the external pin interrupt module and the external clock change						
[2]	PREFL	Set as 1 ,shut down FLASH Clock Interface Controller						
[3]	PRTIM3 Set as 1 ,shut down TMR3 Clock controller							
[4]	-	Are reserved						
[5]	PRWDT Set as 1 ,shut down WDT Counter Clock							
[7: 6]	-	Are reserved						

MCU Control Register - MCUCR

MCUCR - MCU Control register									
MCUCR: 0x35 (0x55)					Defaults: 0x00				
MCUCR	FWKEN	FPDEN	EXRFD		PUD	IRLD	IFAIL	IVSEL	WCE
R / W	R / W	R / W	R / W		R / WW / O		R / O	R / W	R / W
Bit Definitions									
[0]	WCE	MCUCR Update Enable bit, update MCUCR Before, you first need to set this bit, and then 6 Completed within a period MCUCR Update registers							
[1]	IVSEL	Interrupt Vector Select bit, this location 1 After the interrupt vector address will be based IVBASE Mapped to the new value of the register address							
[2]	IFAIL	The system failed to load configuration bits flag, 0 = By checking the configuration information 1 = Failed to load configuration information							
[3]	IRLD	write 1 The reload the system configuration information							
[4]	PUD	Pull on the global disable bit 0 = Pull-up control to enable global 1 = Close all IO The pull-up resistor							
[5]	EXRFD	External reset filtering disable bit 0 = Enable External reset (190us) The digital filter 1 = Disable external reset of the digital filter circuit							
[6]	FPDEN	Flash Power / down Enable Control 0 :system SLEEP Rear FLASH Remain powered 1 :system SLEEP Rear FLASH Power outage							
[7]	FWKEN	Fast wake-up mode enable control, only Power / Off Mode is active 0 : 260us Filter delay 1 : 32us Filter delay							