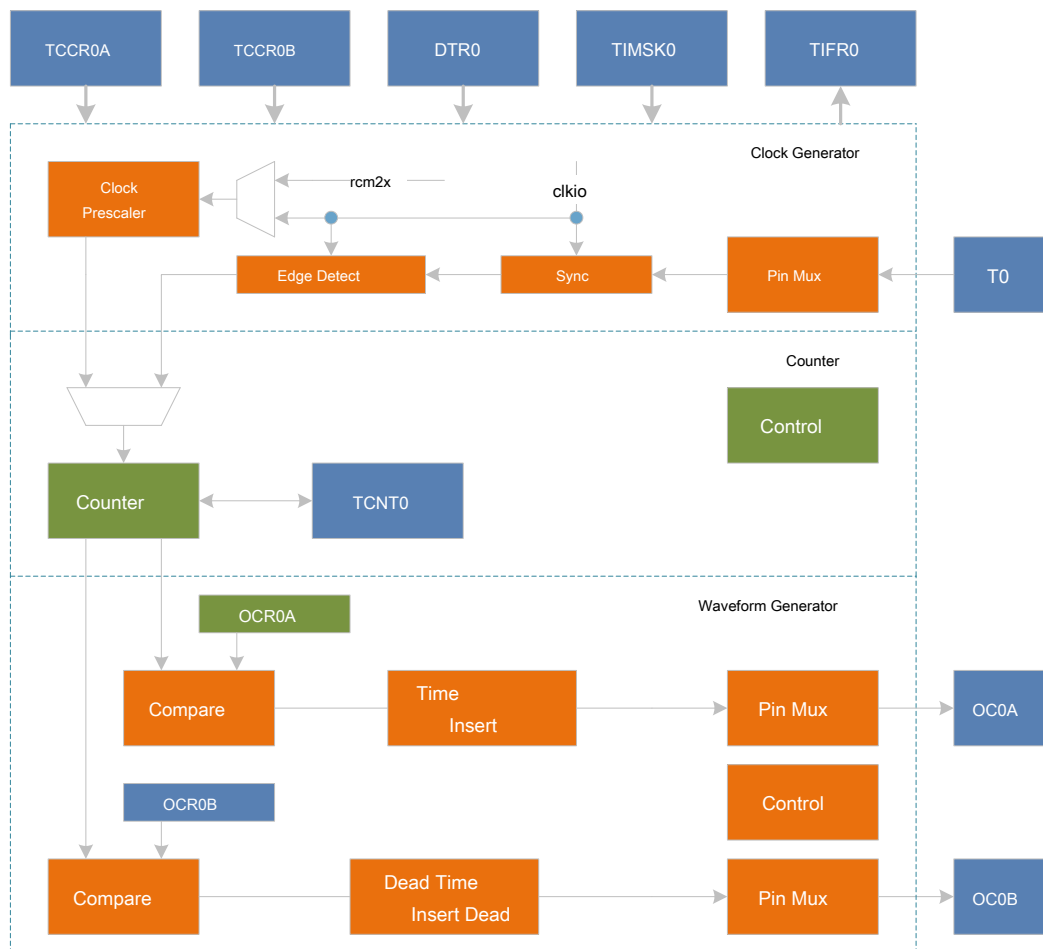


CTC Mode, double buffering function failure. When the count reaches maximum or minimum value of the buffer register is updated simultaneously comparing register OCR0A with OCR0B. See section describes the operating modes.

Waveform generator and comparator generates a mode control output waveform control pattern matching and Comparative counter overflow signal to generate an output waveform comparison OC0A with OC0B. DETAILED generation mode and the operation mode register, see section below. We should compare the output signal waveform OC0A with OC0B Corresponding to the output pin, the data direction register must be set to the output pin.

The figure below shows TC0 The internal structure of FIG. TC0 contain 1 Count clock generation unit, 1 More 8 Bit counter, 2 And output the comparison unit 2 Waveform generation control unit.



TC0 Structure chart

Operating mode

Timing counter 0 There are four different operating modes, including normal mode (Normal), Cleared on compare match (CTC) Mode, fast pulse width modulation (FPWM) Mode and a phase correction pulse width modulation (PCPWM) Mode, the mode control bits generated by the waveform WGM0 [2: 0] To choose. The following four modes will be described specifically. Since there are two separate output of the comparison unit, respectively "A" with "B" Represented by lowercase "x" To represent the two channel outputs the comparison unit.