General Description multiplexing function control signals:

signal Full name	Functional Description
PUOE The multiplexing of the enable pull	This bit is 1 , Enabled by the pull PVOV Control; If this bit is 0 Pull-up enabled by DDxn, PORTxn as well as PUD Joint control
PUOV Pull multiplexing value	in case PUOE for 1 This bit is 1 Enable pin pull-up resistor, otherwise it will prohibit a pullup
DDOE Port Direction enable multiplexing	Second place is 1 , By the output enable pin DDOE Control, or by the DDxn control
DDOV Multiplexing port direction value	in case DDOE for 1 , Sub-bit 1 Will enable output enable pin, otherwise closed pin output
PVOE Data multiplexing port enable	If the second bit is 1 And an output enable pin, pin input value by PVOV Control, or by PORTxn control
PVOV Multiplexing port data value	reference PVOE Functional Description
PTOE Flip enable multiplexing port	Second place is 1 , PORTxn Bit flips
DIEOE Digital Input Enable enable reuse If the second bit is 1	, Can make a digital input port DIEOV control System; otherwise there will be MCU Running state control
DIEOV Digital Input Enable multiplexing value	in case DIEOE for 1 , Digital input port by the second position control, and MCU Run regardless of the state
DI Digital input	This is the digital input signal is input to replace the function of the module. From I / O Wait for the next circuit diagram can be seen, this value after the Schmitt trigger, but I / O Input before the synchronizer. This signal is connected to the peripheral modules, the peripheral modules will be synchronized as required
AIO Analog Input	Analog input / output signal, this signal directly I / O of PAD Is connected, it may be used as a bidirectional analog signal. This signal is directly related to the internal ADC Port, a comparator connected to the analog module etc.

The following section will be a brief description of each pin multiplexing functions and related control signals.

port B Alternate Function

Pin Multiplexing Function Description
PB7 XTALI / TOSC2 (External main crystal pins XI) PCINT7 (Pin Change Interrupt 7)
PB6 XTALO / TOSC1 (External main crystal pins XO) PCINT6 (Pin Change Interrupt 6)
PB5 SCK (SPI Bus master clock input) PCINT5 (Pin Change Interrupt 5)
PB4 MISO (SPI Bus master input / output) PCINT4 (Pin Change Interrupt 4)