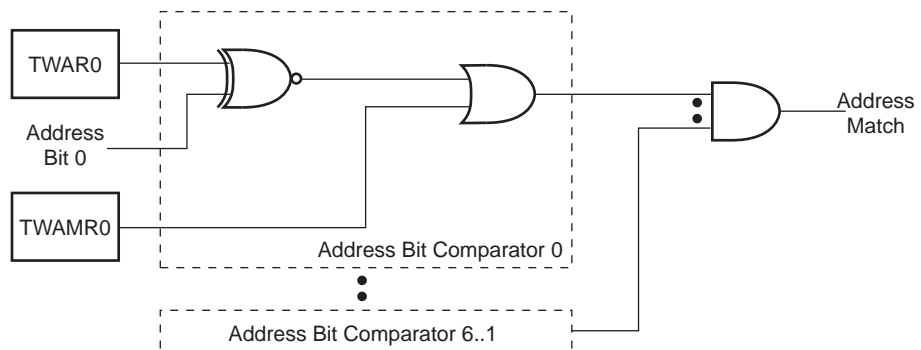


Figure 19-22. TWI Address Match Logic, Block Diagram



- **Bit 0 – Res: Reserved Bit**

This bit is an unused bit in the ATmega48P/88P/168P/328P, and will always read as zero.