| | | |
|---|---|---|
| | | COM3C of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC3C Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM3C The control waveform output of the comparator is different, the comparison output mode control specifically see table below. |
| | 1 WGM31 | Waveform generation mode control times lower. WGM31 with WGM33, WGM32, WGM30 Together form waveform generation mode control WGM3 [3: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described. |
| | 0 WGM30 | Waveform generation mode control lowest bit. WGM30 with WGM33, WGM32, WGM31 Together form waveform generation mode control WGM3 [3: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described. |

The following table non PWM Mode (ie, normal mode and CTC Mode), the comparison output of the comparator mode control output waveform.

non- PWM Compare output mode control mode

| COM3x [1: 0] | description |
|---|---|
| 0 | OC3x Disconnect, GM IO Port operations |
| 1 | Flip compare match OC3x signal |
| 2 | Clear compare match OC3x signal |
| 3 | When set compare match OC3x signal |

The following table fast PWM Mode mode control comparator output waveform of the output comparator.

fast PWM Compare output mode control mode

| COM3x [1: 0] | description |
|---|---|
| 0 | OC3x Disconnect, GM IO Port operations |
| 1 | WGM3 for 15 When: Flip compare match OC3A signal, OC3B disconnect WGM3 When other values: OC3x Disconnect, GM IO Port operations |
| 2 | Clear compare match OC3x Signal is set to match the maximum value OC3x signal |
| 3 | When set compare match OC3x Signal is cleared when the maximum matching OC3x signal |

The following table shows the comparison output of the phase correction mode the mode control output of the comparator waveform.

Phase correction and frequency correction phase PWM Compare output mode control mode

| COM3x [1: 0] | description |
|---|---|
| 0 | OC3x Disconnect, GM IO Port operations |
| 1 | WGM3 for 9 or 11 When: Flip compare match OC3A signal, OC3B disconnect WGM3 When other values: OC3x Disconnect, GM IO Port operations |
| 2 | Match clears the count comparator ascending OC3x Signal, the match count comparator arranged in descending order bits OC3x signal |
| 3 | Comparison of the configuration bit match count ascending OC3x Down signal, in descending count comparator match clears OC3x signal |