

To the DDB4 control. When this pin is SPI The controller is forced to enter, it can still pull-up resistor
PROTB4 control.

PCINT4: Pin change interrupt. PB4 External interrupt sources.

MOSI / OC2A / PCINT3- port B Pin 3

MOSI: SPI Master device data output controller, from data input device. when SPI Is configured as a slave, this pin will be forced to enter, not subject to DDB3 control. when SPI The controller is configured as a master, this pin by the method of DDB3 control. When this pin is SPI Forced to control the input, you can still pass PORTB3 Control its pull-up resistor.

OC2A: Timer / Counter 2 of A Group match output. PB3 As timer / counter 2 Compare match outside. At this point must DDB3
The output pin is set. Simultaneously, OC2A Also timer 2 of PWM Mode output pin.

PCINT3: Pin change interrupt. PB3 External interrupt sources.

SSN / OC1B / PCINT2- port B Pin 2

SSN: SPI Sheet from the selected input device. when SPI The controller is configured as a slave, this pin will be forced to enter, it is not subject to DDB2 control. As a slave device, SPI Controller SSN It is driven low to be effective. when SPI Configured as a master controller, by the direction of this pin DDB2 control. When this pin is SPI The controller is forced to enter, can still PORTB2 Control the pull-up resistor.

OC1B: Timer / Counter 1 of B Group match output. PB2 As timer / counter 1 Compare match outside. At this point must DDB2
The output pin is set. Simultaneously, OC1B Also timer 1 of PWM Mode output pin.

PCINT2: Pin change interrupt. PB2 External interrupt sources.

OC1A / PCINT1- port B Pin 1

OC1A: Timer / Counter 1 of A Group match output. PB1 As timer / counter 1 Compare match outside. At this point must DDB1
The output pin is set. Simultaneously, OC1A Also timer 1 of PWM Mode output pin.

PCINT1: Pin change interrupt. PB1 External interrupt sources.

ICP1 / CLKO / PCINT0- port B Pin 0

ICP1: Timer / Counter 1 The capture input pin

CLKO: The system clock output work, when CLKPR Register CLKOE Bit 1 This pin will be forced to output, from DDB0 control. Output frequency of the current system clock frequency.

PCINT0: Pin change interrupt. PB0 External interrupt sources.