

4	WGM13	Waveform generation mode control high.  <b>WGM13 with WGM12, WGM11, WGM10 Together form waveform generation mode control</b>  WGM1 [3: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described.																		
3	WGM12	Second uppermost waveform generation mode control.  <b>WGM12 with WGM13, WGM11, WGM10 Together form waveform generation mode control</b>  WGM1 [3: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described.																		
2	CS12 Clock control high.	For selecting a timing counter 1 The clock source.																		
1	CS11 Clock selection control bits.	For selecting a timing counter 1 The clock source.																		
0	CS10 Clock control low.	For selecting a timing counter 1 The clock source.																		
		<table><tr><th>CS1 [2: 0]</th><th>description</th></tr><tr><td>0</td><td>No clock source, stops counting</td></tr><tr><td>1</td><td>clk<sub>sys</sub></td></tr><tr><td>2</td><td>clk<sub>sys</sub> / 8 From prescaler</td></tr><tr><td>3</td><td>clk<sub>sys</sub> / 64 From prescaler</td></tr><tr><td>4</td><td>clk<sub>sys</sub> / 256 From prescaler</td></tr><tr><td>5</td><td>clk<sub>sys</sub> / 1024 From prescaler</td></tr><tr><td>6</td><td>External Clock T1 Pin, falling edge</td></tr><tr><td>7</td><td>External Clock T1 Pin on the rising edge</td></tr></table>	CS1 [2: 0]	description	0	No clock source, stops counting	1	clk <sub>sys</sub>	2	clk <sub>sys</sub> / 8 From prescaler	3	clk <sub>sys</sub> / 64 From prescaler	4	clk <sub>sys</sub> / 256 From prescaler	5	clk <sub>sys</sub> / 1024 From prescaler	6	External Clock T1 Pin, falling edge	7	External Clock T1 Pin on the rising edge
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The following table is a waveform generation mode control.

WGM1 [3: 0] Operating mode	TOP value	Update OCR0 Time set	TOV0 time	
0	Normal	0xFFFF	immediately	MAX
1	8 Place PCPWM	0x00FF	TOP	BOTTOM
2	9 Place PCPWM	0x01FF	TOP	BOTTOM
3	10 Place PCPWM 0x03FF		TOP	BOTTOM
4	CTC	OCR1A	immediately	MAX
5	8 Place FPWM	0x00FF	BOTTOM	TOP
6	9 Place FPWM	0x01FF	BOTTOM	TOP
7	10 Place FPWM	0x03FF	BOTTOM	TOP
8	PFCPWM	ICR1	BOTTOM	BOTTOM
9	PFCPWM	OCR1A	BOTTOM	BOTTOM
10	PCPWM	ICR1	TOP	BOTTOM
11	PCPWM	OCR1A	TOP	BOTTOM
12	CTC	ICR1	immediately	MAX
13	Retention	-	-	-
14	FPWM	ICR1	TOP	TOP
15	FPWM	OCR1A	TOP	TOP