

27.3.3 Supply Current of IO Modules

The tables and formulas below can be used to calculate the additional current consumption for the different I/O modules in Active and Idle mode. The enabling or disabling of the I/O modules are controlled by the Power Reduction Register. See ["Power Reduction Register" on page 42](#) for details.

Table 27-5. Additional Current Consumption for the different I/O modules (absolute values)

PRR bit	Typical numbers		
	V _{CC} = 2V, F = 1MHz	V _{CC} = 3V, F = 4MHz	V _{CC} = 5V, F = 8MHz
PRUSART0	5.54 µA	34.8 µA	133.9 µA
PRTWI	10.1 µA	63.1 µA	250.6 µA
PRTIM2	10.4 µA	68.5 µA	269.4 µA
PRTIM1	9.66 µA	62.9 µA	248.1 µA
PRTIM0	2.45 µA	14.1 µA	60.8 µA
PRSPI	9.55 µA	63.8 µA	275.6 µA
PRADC	9.04 µA	58.6 µA	243.9 µA

Table 27-6. Additional Current Consumption (percentage) in Active and Idle mode

PRR bit	Additional Current consumption compared to Active with external clock (see Figure 27-93 on page 377 and Figure 27-94 on page 377)	Additional Current consumption compared to Idle with external clock (see Figure 27-98 on page 379 and Figure 27-99 on page 380)
PRUSART0	1.9%	8.5%
PRTWI	3.4%	15.6%
PRTIM2	3.6%	16.5%
PRTIM1	3.4%	15.2%
PRTIM0	0.8%	3.7%
PRSPI	3.5%	15.8%
PRADC	3.2%	14.5%

It is possible to calculate the typical current consumption based on the numbers from [Table 27-6 on page 382](#) for other V_{CC} and frequency settings than listed in [Table 27-5 on page 382](#).

Example

Calculate the expected current consumption in idle mode with TIMER1, ADC, and SPI enabled at V_{CC} = 2.0V and F = 1MHz. From [Table 27-6 on page 382](#), third column, we see that we need to add 15.2% for the TIMER1, 14.5% for the ADC, and 15.8% for the SPI module. Reading from [Figure 27-98 on page 379](#), we find that the idle current consumption is ~0.055 mA at V_{CC} = 2.0V and F = 1MHz. The total current consumption in idle mode with TIMER1, ADC, and SPI enabled, gives:

$$I_{CC\text{total}} \approx 0.065 \text{ mA} \cdot (1 + 0.152 + 0.145 + 0.158) \approx 0.095 \text{ mA}$$