

		"1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC1A / OC1B The waveform output. When set DSX14 Bit "0" When the external interrupt 1 As a comparison output signal waveform is off OC1A / OC1B The trigger source is prohibited.
3: 2	-	Retention
1	DSX11	TC1 Select the trigger source control enables the first 1 Bit. When set DSX11 Bit "1" When, analog comparator 1 As a comparison output signal waveform is off OC1A / OC1B The trigger source is enabled. when DOC1A / DOC1B Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC1A / OC1B The waveform output. When set DSX11 Bit "0" When, analog comparator 1 As a comparison output signal waveform is off OC1A / OC1B The trigger source is prohibited.
0	DSX10	TC1 Select the trigger source control enables the first 0 Bit. When set DSX10 Bit "1" When, analog comparator 0 As a comparison output signal waveform is off OC1A / OC1B The trigger source is enabled. when DOC1A / DOC1B Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC1A / OC1B The waveform output. When set DSX10 Bit "0" When, analog comparator 0 As a comparison output signal waveform is off OC1A / OC1B The trigger source is prohibited.

The following table shows the selection control trigger source waveform output.

shut down OC1A / OC1B Trigger source waveform output from the selection control

DOC1x DSX1n = 1	Trigger source	description
0	-	DOC1x Bit "0", Trigger source waveform output off function is disabled
1	0	Analog comparator 0 ACIF0 The rising edge will be closed OC1x Waveform output
1	1	Analog comparator 1 ACIF1 The rising edge will be closed OC1x Waveform output
1	4	External Interrupt 1 INTF1 The rising edge will be closed OC1x Waveform output
1	5	Pin Change 1 PCIF1 The rising edge will be closed OC1x Waveform output
1	6	TC2 overflow TOV2 The rising edge will be closed OC1x Waveform output
1	7	TC0 overflow TOV0 The rising edge will be closed OC1x Waveform output

【note】:

DSX1n = 1 Show DSX1 The first register n Bit 1 When each register bit may be set simultaneously.

TCNT1L - TC1 Low byte count value register

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address: 0x84					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	TCNT1L7	TCNT1L6	TCNT1L5	TCNT1L4	TCNT1L3	TCNT1L2	TCNT1L1	TCNT1L0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit	Name description							
7: 0	TCNT1 TC1 Low byte count value.							