

DSIR - Arithmetic instruction register

<i>DSIR</i> - uDSC Arithmetic instruction register								
address: 0x21 (0x01)						Defaults: 0000_0000		
Bit	7	6	5	4	3	2	1	0
Name	DSIR [7: 0]							
R / W	R / W							
Bit	Name description							
7: 0	IR	uDSC Operation instruction. See " Operation instruction defines " Section describes						

DSDX - Operand register DSDX

DSDX - uDSC Operand register DX																								
address: 0x30 (0x10)														Defaults: 0000_0000										
Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Name							
	DSDX [15: 0]																							
R / W	R / W																							
Bit	Name description																							
15: 0	DSDX		16 Bit operand registers DSDX																					

DSDY - Operand register DSDY

DSDY - uDSC Operand register DY																							
address: 0x31 (0x11)																	Defaults: 0000_0000						
Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Name						
	DSDY [15: 0]																						
R / W	R / W																						
Bit	Name description																						
15: 0	DSDY		16 Bit operand registers DSDY																				

DSAL - 32 Bit accumulator DA Low 16 Place

DSAL - uDSC Operand register DSA Low 16 Place																								
address: 0x58 (0x38)															Defaults: 0000_0000									
Bit	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	Name							
	DSA [15: 0]																							
R / W	R / W																							
Bit	Name description																							
15: 0	DSAL		32 Bit accumulator DSA Low 16 Place																					