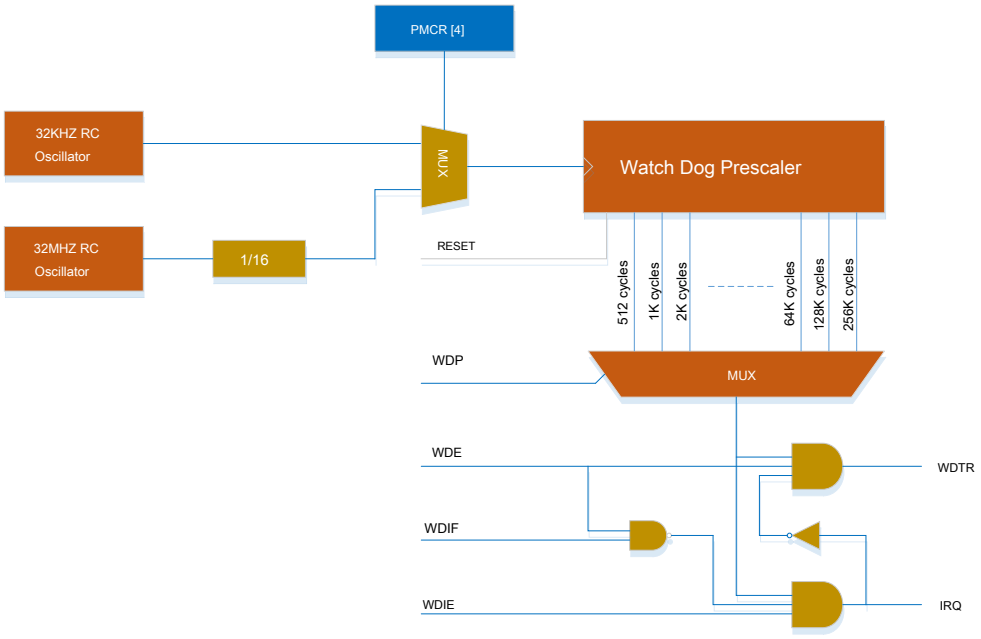
Watchdog Timer

- **Optional internal clock 32KHz RC Or internal 32MHz RC of 16 Divider (2MHz)**
- Supports interrupt mode, reset mode and reset interrupt mode
- **Timer expires maximum to 8 second**

LGT8FX8P Interior contains an enhanced watchdog timer (WDT) Module. WDT Timer operation clock can be internal 32KHz RC Oscillator, or internal 32MHz RC Oscillator 16 Divider. WDT After the counter overflows, an interrupt may be output or a system reset signal. In normal use, the software needed to perform a

WDR - Watchdog Timer Reset instruction restart until the counter overflows. If the system does not even execution WDR instruction, WDT It will generate an interrupt or system reset.

A configuration diagram of the watchdog timer as shown below:



In interrupt mode, WDT Generates an interrupt request signal overflow. You can use this as a wake-up signal interrupt sleep mode, it can be used as a general system timer uses. For example, you can use this interrupt an operation execution time limit, terminate one of the tasks in the **current overflow. In the reset mode, WDT Generating a system reset signal immediately after the counter overflows. The most typical use is to prevent system crashes or running out. The third mode is reset interrupt mode, interrupt and reset combines two functions. First, the system will respond WDT**

Interrupt function exits WDT After the interrupt routine, and immediately switched to reset mode. This feature can save support some of the more critical parameter information before reset.

to prevent WDT Was accidentally disabled, shut down WDT The operation must be carried out in accordance with the timing of a strictly defined. The following code describes how to disable the watchdog timer. The following example assumes that interrupt has been disabled, so that the entire operation process will not be interrupted.