		10 : 4KB E2PROM, 24KB program FLASH 11 : 8KB E2PROM, 16KB program FLASH

FLASH Access Control Register - EECR

EECR - FLASH / E2PROM Control register								
EECR: 0x1F (0x3F)				Defaults: 0x00				
bits	EEPM3	EEPM2	EEPM1	EEPM0	EERIE	EEMPE	EEPE	EERE
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
The initial value	0	0	0	0	0	0	0	0

Bit Definitions

[7: 4]	EEPM [3: 0]	EFLASH / EPROM Access mode control bits				
		[3]	[2]	[1]	[0]	Mode Description
		0	0	0	x	8 Bit mode read / write E2PROM (default)
		0	0	1	x	16 Bit mode read / write E2PROM
		0	1	0	x	32 Bit mode read / write E2PROM
		1	x	0	0	E2PROM Erase (optional)
		1	x	0	1	program FLASH Erased (page erase)
		1	x	1	0	program FLASH program
		1	x	1	1	Reset FLASH / E2PROM Controller
[3]	EERIE	FLASH / E2PROM Ready interrupt enable control. write 1 Enable write 0 Prohibited. when EEPE After the hardware is automatically cleared, E2PROM Ready interrupt valid. in EPROM During operation, this will not generate an interrupt				
[2]	EEMPE	FLASH / E2PROM Programming operation enable control bit EEMPE For control EEPE Is valid, when at the same time set EEMPE for 1 , EEPE for 0 After, in four cycles after setting EEPE for 1 Will start the programming operation. Otherwise invalid programming operation. After four cycles, EEMPE It is automatically cleared				
[1]	EEPE	FLASH / E2PROM Programming operation enable bit				
[0]	EERE	E2PROM Read enable bit, data valid after two periodic				

Common I / O register- GPIOR2

GPIOR2 - Common I / O register 2		
GPIOR2: 0x2B (0x4B)		Defaults: 0x00
Bits	GPIOR2 [7: 0]	
R / W	R / W	
The initial value	0x00	

Bit Definitions

[7: 0]	GPIOR2	Common I / O register 2 For storing a user-defined data