

**TCCR2A-TC2 Control register A**

<b>TCCR2 A - TC2 Control register A</b>								
address: 0xB0					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	COM2A1	COM2A0	COM2B1	COM2B0	-	-	WGM21	WGM20
R / W	W	R / W	R / W	R / W	-	-	R / W	R / W
Bit	Name	description						
7	COM2A1	<p>TC2 Compare Match Output A Mode control high.</p> <p>COM2A1 with COM2A0 Together form the output compare mode control COM2A [1: 0] ,control</p> <p>OC2A The output waveform. in case COM2A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC2A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM2A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
6	COM2A0	<p>TC2 Compare Match Output A Mode control low.</p> <p>COM2A0 with COM2A1 Together form the output compare mode control COM2A [1: 0] ,control</p> <p>OC2A The output waveform. in case COM2A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC2A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM2A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
5	COM2B1	<p>TC2 Compare Match Output B Mode control high.</p> <p>COM2B1 with COM2B0 Together form the output compare mode control COM2B [1: 0] ,control</p> <p>OC2B The output waveform. in case COM2B of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC2B Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM2B The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
4	COM2B0	<p>TC2 Compare Match Output B Mode control low.</p> <p>COM2B0 with COM2B1 Together form the output compare mode control COM2B [1: 0] ,control</p> <p>OC2B The output waveform. in case COM2B of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC2B Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM2B The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
3: 2	-	Reservations.						
1	WGM21	<p>TC2 Waveform generation mode control high.</p> <p>WGM20 with WGM21 , WGM22 Together form waveform generation mode control</p> <p>WGM2 [2: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described.</p>						
0	WGM20	<p>TC2 Waveform generation mode control low.</p> <p>WGM21 with WGM20 , WGM22 Together form waveform generation mode control</p> <p>WGM2 [2: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described.</p>						