PD Group level change wakeup control register - IOCWK

IOCWK - PD Group level change wakeup control register							
IOCWK: 0xAE				Defaults: 0x00			
Bits	IOCD7	IOCD6	IOCD5	IOCD4	IOCD3	IOCD2	IOCD1	IOCD0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions							
[7: 0]	IOCWK Put 1	Corresponding bit, enabling PD group IO The pin change wake-up function					

DPS2 Mode Control Register - DPS2R

DPS2R - DPS2 Mode control register							
DPS2R: 0xAF				Defaults: 0x00			
Bits	-	-	-	- DPS2E		LPRCE	TOS1	TOS0
R / W	-	-	-	- R / W		R / W	R / W	R / W
Bit Definitions							
[1: 0]	TOS	LPRC Timed wake-up settings: 00 = 128ms 01 = 256ms 10 = 512ms 11 = 1s					
[2]	LPRCE	LPRC Enable Control 0 = Disable LPRC Timer 1 = Enable LPRC Timer					
[3]	DPS2E	DPS2 Mode enable control bit 0 = Disable DPS2 mode 1 = Enable DPS2 mode					
[7: 4]	-	Retention					