or series – L			VICW V1.0.7	Logicureen Technologies Co
QFP48	QFP32	SSOP20		
12	08	07	PB7/XTALI	
			PB7: I/O Pin B7	
			XTALI: Crystal Oscillator Input	
13 14 15	10	08	PD5/RXD*/T1/OC0B	
			PD5: I/O Pin D5	
			RXD: USART Receive Data	
			T1: Timer 1 External Clock Input	
			OCOB: Timer/Counter 0 Output Compare Match B	
			PD6/TXD*/OCOA	
			PD6: I/O Pin D6	
			TXD: USART Transmit Data	
			OCOA: Timer/Counter 0 Output Compare Match A	
			ACOP/OC3A	
			ACOP: Analog Comparator 0 Positive Input	
			OC3A: Timer/Counter 3 Output Compare Match A	
16	11	10	PD7/ACXN	
			PD7: I/O Pin D7	
			ACXN: Analog Comparator 0/1 Inverting Input	
17 18	12		PF7/OC2B	
			PF7: I/O Pin F7	
			OC2B: Timer/Counter 2 Output Compare Match B	
			PBO/ICP1	
			PB0: I/O Pin B0	
			ICP1: Timer 1 Capture Input	
19	13		PB1/OC1A	
			PB1: I/O Pin B1	
			OC1A: Timer/Counter 1 Output Compare Match A	
20	14	12	PB2/OC1B/SPSS	
			PB2: I/O Pin B2	
			OC1B: Timer/Counter 1 Output Compare Match B	
			SPSS: SPI Slave Select	
21	-	-	GND	
22	-	-	VCC	
23	15	12	PB3/MOSI/OC2A	
			PB3: I/O Pin B3	
			MOSI: SPI Master Output Slave Input	
			OC2A: Timer/Counter 2 Output Compare Match A	
24	16	13	PB4/MISO	
			PB4: I/O Pin B4	
			MISO: SPI Master Input Slave Output	
25	17	14	PB5/SPCK/AC1P	
			PB5: I/O Pin B5	
			SPCK: SPI Clock	
			AC1P: Analog Comparator 1 Noninverting Input	
QFP48	QFP32	SSOP20		
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