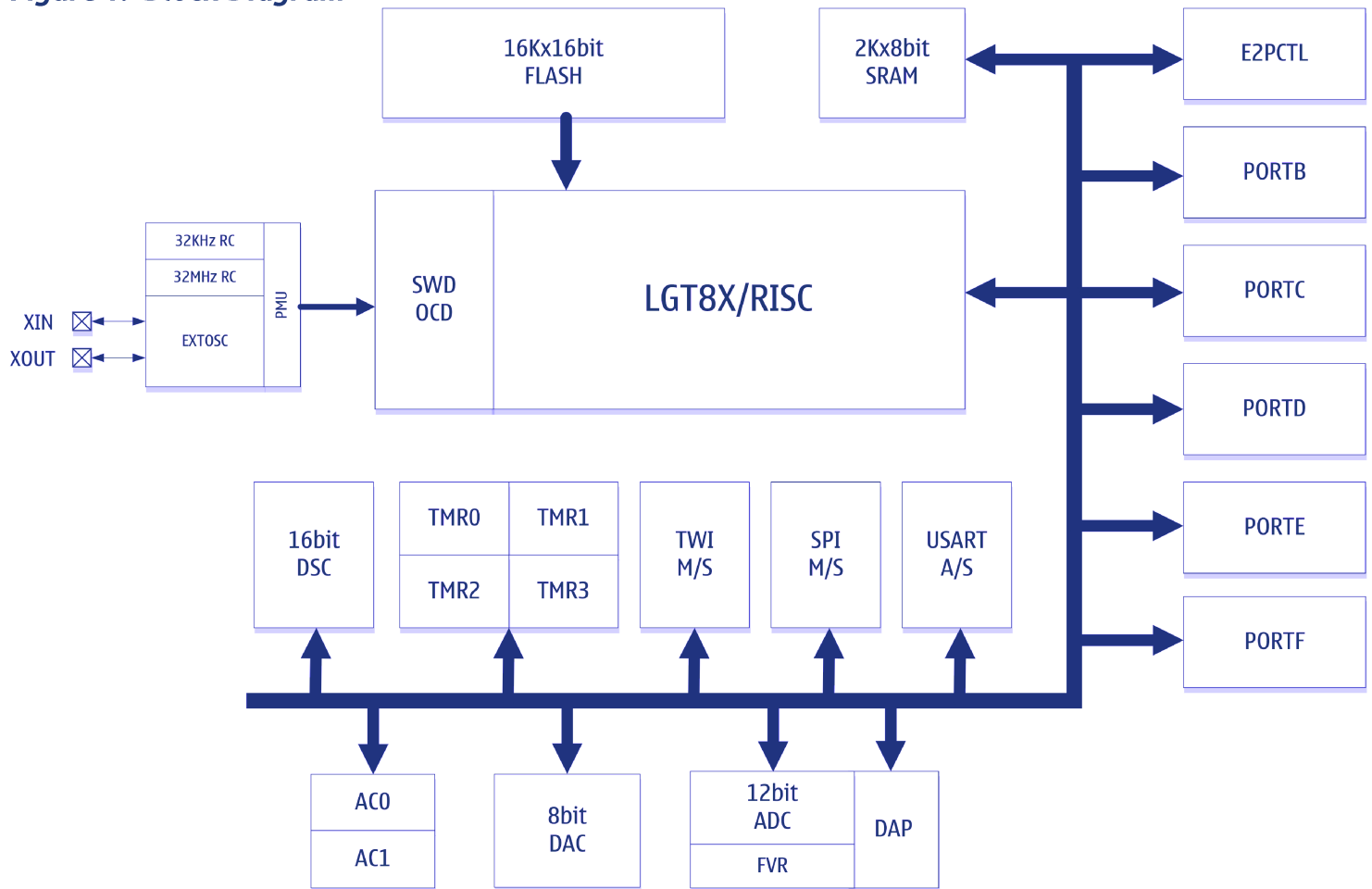


SECTION 1

LGT8XM MCU OVERVIEW

Figure 1. Block Diagram



NAME	DESCRIPTION
SWD	Debug Module and ISP Features
LGT8X	8 Bit CPU
E2PCTL	MemCon FLASH Memory Controller
PMU	Power Management Module
PORTB/C/D/E/F	General-Purpose Input and Output Ports
DSC	16 Bit Arithmetic Acceleration Unit
ADC	8 Channel 12 Bit ADC with Programmable Gain
DAP	Differential Amplifier
IVREF	1.024V / 2.048V / 4.096V Internal Reference
ACO/1	Analog Comparator
TMRO/ 1/2/3	8/16 Bit Timer / Counter, PWM Controller
WDT	Watchdog Reset Module
SPI (M/S)	SPI Controller (Master or Slave)
TWI (M/S)	Two-Wire Controller (Master or Slave)
USART	Synchronous / Asynchronous Serial Transceiver
DAC	8 Bit DAC