System reset control

Outline

After a reset, all I / O Register will be set to their initial values, the program begins at the reset vector. LGT8FX8P The interrupt vector address, you must use a RJMP - Relative jump instruction to jump to the reset handler. If the program is useless to use interrupt is not enabled interrupt source, interrupt vector will not be used, the interrupt vector area can be used to store the user's program code.

After the reset is effected, all I / O Port immediately into their initial state. most I / O Initialization state is entered and close off the internal pullup. Analog input function I / O Also initialized to digital I / O Features.

When the reset becomes inactive, LGT8FX8P Internal timer counter started for broadening reset. Broadening the width of the reset signal used to ensure the system power supply and clock modules into a stable state.

Reset sources

LGT8FX8P Total supports six sources of reset:

- POR: internal low pressure system when the operating voltage POR Module reset threshold, the reset valid.

- External reset: low pulse on the external reset pin of the chip constant width, the external reset is asserted.

- Reset Watchdog: Watchdog module after, if the watchdog timer expires, the system will reset.

- Low voltage reset: LGT8FX8P It has an internal low-voltage detection module (LVD) , When the system power is below LVD Setting the reset threshold, MCU It will also be reset.

- Software Reset: LGT8FX8P Internal reset register trigger a dedicated software, the user can be reset at any time by the register MCU .

- OCD Reset: OCD Reset is issued debugger module for direct reset MCU Kernel.

Reset System structure: