

		Interrupt is enabled. When a compare match occurs, i.e., TIFR in OCF1A. When the bit is set, an interrupt is generated. when OCIE1A Bit "0" Time, TC1 Output Compare A Match interrupts are disabled.
0	TOIE1	TC1 Overflow interrupt enable bit. when TOIE1 Bit "1" And Global Interrupt Set, TC1 Overflow interrupt is enabled. when TC1 Overflow occurs, that is, TIFR middle TOV1 When the bit is set, an interrupt is generated. when TOIE1 Bit "0" Time, TC1 Overflow interrupts are disabled.

TIFR1 - TC1 Interrupt Flag Register

TIFR1 - TC1 Interrupt Flag Register								
address: 0x36					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	-	-	ICF1	-	-	OCF1B OCF1A		TOV1
R / W	-	- R / W		-	- R / W		R / W	R / W
Bit Name		description						
7: 6	-	Reservations.						
5	ICF1	Input capture flag. When the input capture event occurs, ICF1 Flag is set. when ICR1 It is counted as TOP Value, and the count value reaches TOP Value, ICF1 Flag is set. If the ICIE1 for "1" And the Global interrupt flag is set, it will generate an interrupt input capture. When you do this the interrupt service routine ICF1 Will be automatically cleared or ICF1 Write bit "1" Also clears the bit.						
4: 3	-	Reservations.						
2	OCF1B	Output Compare B Matching flag. when TCNT1 equal OCR1B , The comparison unit signals a match, the comparison flag is set and OCF1B . If then the output compare interrupt enable OCIE1B for "1" And the Global interrupt flag is set, it will generate an output compare interrupt. When you do this the interrupt service routine OCF1B Will be automatically cleared or OCF1B Write bit "1" Also clears the bit.						
1	OCF1A	Output Compare A Matching flag. when TCNT1 equal OCR1A , The comparison unit signals a match, the comparison flag is set and OCF1A . If then the output compare interrupt enable OCIE1A for "1" And the Global interrupt flag is set, it will generate an output compare interrupt. When you do this the interrupt service routine OCF1A Will be automatically cleared or OCF1A Write bit "1" Also clears the bit.						
0	TOV1	Overflow flag. When the counter overflows, the overflow flag is set TOV1 . If this time overflow interrupt enable TOIE1 for "1" And the Global interrupt flag is set, it will generate an overflow interrupt. When you do this the interrupt service routine TOV1 Will be automatically cleared or TOV1 Write bit "1" Also clears the bit.						

DTR1L -TC1 Dead Time Register Low Byte

DTR1 - TC1 Dead time register								
address: 0x8C					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0