

LGT8F168P / 328P Interrupt vector list

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Numbering	Vector address	Interrupt source signals	Interrupt Source Description
1	0x0000	RESET	External reset, power-on reset, a watchdog reset, SWD Debug reset, low voltage reset
2	0x0002	INT0	External Interrupt Request 0
3	0x0004	INT1	External Interrupt Request 1
4	0x0006	PCI0	Interrupt pin level 0
5	0x0008	PCI1	Interrupt pin level 1
6	0x000A	PCI2	Interrupt pin level 2
7	0x000C	WDT	Watchdog overflow interrupt
8	0x000E	TC2 COMPA	Timer 2 Compare match A Interrupt
9	0x0010	TC2 COMPB	Timer 2 Compare match B Interrupt
10	0x0012	TC2 OVF	Timer 2 Overflow
11	0x0014	TC1 CAPT	Timer 1 Input Capture interrupt
12	0x0016	TC1 COMPA	Timer 1 Compare match A Interrupt
13	0x0018	TC1 COMPB	Timer 1 Compare match B Interrupt
14	0x001A	TC1 OVF	Timer 1 Overflow
15	0x001C	TC0 COMPA	Timer 0 Compare match A Interrupt
16	0x001E	TC0 COMPB	Timer 0 Compare match B Interrupt
17	0x0020	TC0 OVF	Timer 0 Overflow
18	0x0022	SPI STC	SPI Serial transfer end interrupt
19	0x0024	USART RXC	USART RX Complete
20	0x0026	USART UDRE	USART Data Register Empty
twenty one	0x0028	USART TXC	USART End of Transmit Interrupt
twenty two	0x002A	ADC	ADC Conversion end interrupt
twenty three	0x002C	EE_RDY	EEPROM Ready interrupt
twenty four	0x002E	ANA_COMP	Analog Comparator Interrupt
25	0x0030	TWI	Two-wire serial interface interrupt
26	0x0032	ANA_COMP1	Analog comparator 1 Interrupt
27	0x0034	-	Retention
28	0x0036	PCI3	Interrupt pin level 3
29	0x0038	PCI4	Interrupt pin level 4
30	0x003A	TC3_INT	Timer 3 Interrupt

LGT8FX8P The reset vector address from 0x0000 Begin execution. In addition to the reset vector, the other by the vector address can be MCUCR Register IVSEL as well as IVBASE Register redirected to 512 Byte-aligned start address.