

5	DSX35 TC3	<p>Select the trigger source control enables the first 5 Bit.</p> <p>When set DSX35 Bit "1" When, pin change 1 As a comparison output signal waveform is off OC3x The trigger source is enabled. when DOC3x Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC3x The waveform output. When set DSX35 Bit "0" When, pin change 1 As a comparison output signal waveform is off OC3x The trigger source is prohibited.</p>
4	DSX34 TC3	<p>Select the trigger source control enables the first 4 Bit.</p> <p>When set DSX34 Bit "1" When the external interrupt 1 As a comparison output signal waveform is off OC3x The trigger source is enabled. when DOC3x Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC3x The waveform output. When set DSX34 Bit "0" When the external interrupt 1 As a comparison output signal waveform is off OC3x The trigger source is prohibited.</p>
3: 2	-	Reservations.
1	DSX31 TC3	<p>Select the trigger source control enables the first 1 Bit.</p> <p>When set DSX31 Bit "1" When, analog comparator 1 As a comparison output signal waveform is off OC3x The trigger source is enabled. when DOC3x Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC3x The waveform output. When set DSX31 Bit "0" When, analog comparator 1 As a comparison output signal waveform is off OC3x The trigger source is prohibited.</p>
0	DSX30 TC3	<p>Select the trigger source control enables the first 0 Bit.</p> <p>When set DSX30 Bit "1" When, analog comparator 0 As a comparison output signal waveform is off OC3x The trigger source is enabled. when DOC3x Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC3x The waveform output. When set DSX30 Bit "0" When, analog comparator 0 As a comparison output signal waveform is off OC3x The trigger source is prohibited.</p>

The following table shows the selection control trigger source waveform output.

shut down OC3x Trigger source waveform output from the selection control

DOC3x DSX3n = 1 Trigger source	description		
0	-	-	DOC3x Bit "0", Trigger source waveform output off function is disabled
1	0	Analog comparator 0	ACIF0 The rising edge will be closed OC3x Waveform output
1	1	Analog comparator 1	ACIF1 The rising edge will be closed OC3x Waveform output
1	4	External Interrupt 1	INTF1 The rising edge will be closed OC3x Waveform output
1	5	Pin Change 1	PCIF1 The rising edge will be closed OC3x Waveform output
1	6	TC2 overflow	TOV2 The rising edge will be closed OC3x Waveform output
1	7	TC0 overflow	TOV0 The rising edge will be closed OC3x Waveform output

note:

2) DSX3n = 1 Show TCCR1D The first register n Bit 1 When each register bit may be set simultaneously.

TCNT3L-TC3 Low Byte Counter Register

TCNT3L - TC3 Low byte count value register								
address: 0x94					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0