		stop.
1	PCINT9 Pin change enable mask 1 . When set PCINT9 Bit "1" Time, PC1 Pin change interrupt is enabled. PC1 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT9 Bit "0" Time, PC1 Pin change interrupts are disabled.	
0	PCINT8 Pin change enable mask 0 . When set PCINT8 Bit "1" Time, PC0 Pin change interrupt is enabled. PC0 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT8 Bit "0" Time, PC0 Pin change interrupts are disabled.	

PCMSK2 - Pin change interrupt mask register 2

PCMSK2 - Pin change mask register 2							
address: 0x6D				Defaults: 0x00			
Bits	7	6	5	4	3	2	1	0
	PCINT23	PCINT22	PCINT21	PCINT20	PCINT19	PCINT18	PCINT17	PCINT16
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W

Bit	Name description
7	PCINT23 Pin change enable mask twenty three . When set PCINT23 Bit "1" Time, PD7 Pin change interrupt is enabled. PD7 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have PCI2 Interrupted. When set PCINT23 Bit "0" Time, PD7 Pin change interrupts are disabled.
6	PCINT22 Pin change enable mask 6 . When set PCINT22 Bit "1" Time, PD6 Pin change interrupt is enabled. PD6 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have PCI2 Interrupted. When set PCINT22 Bit "0" Time, PD6 Pin change interrupts are disabled.
5	PCINT21 Pin change enable mask twenty one . When set PCINT21 Bit "1" Time, PD5 Pin change interrupt is enabled. PD5 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have PCI2 Interrupted. When set PCINT21 Bit "0" Time, PD5 Pin change interrupts are disabled.
4	PCINT20 Pin change enable mask 20 . When set PCINT20 Bit "1" Time, PD4 Pin change interrupt is enabled. PD4 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have PCI2 Interrupted. When set PCINT20 Bit "0" Time, PD4 Pin change interrupts are disabled.
3	PCINT19 Pin change enable mask 19 . When set PCINT19 Bit "1" Time, PD3 Pin change interrupt is enabled. PD3 Level change on the pin will be set PCIF2 If the PCIE2 And a global interrupt bit set, will have PCI2 Interrupted. When set PCINT19 Bit "0" Time, PD3 Pin change interrupts are disabled.
2	PCINT18 Pin change enable mask 18 .