		Level trigger, then the low level must be held until the completion of the current instruction execution will trigger an interrupt.
1	ISC01	INT0 Pin interrupt trigger control bit high.
0	ISC00	INTO Pin interrupt trigger control bit low. When the global interrupt set and GICR The respective control bit interrupt mask register is set when the external interrupt 0 by INTO Pin excited. See table describes interrupt trigger mode. Prior to edge detection MCU First sampling INTO Level on the pin. If the selected edge trigger or level trigger changes in the way, that last longer than 1 A system clock cycle pulse will trigger an interrupt. Shorter pulses are not guaranteed to trigger an interrupt. If you select a low level interrupt level must be held until the completion of the current instruction execution will trigger an interrupt.

External Interrupt 1 The table below trigger.

External Interrupt 1 Trigger control

ISC1 [1: 0]	description					
0	External pin INT1 Low trigger					
1	External pin INT1 Rising or falling edge trigger					
2	External pin INT1 Falling edge					
3	External pin INT1 Rising edge triggered					

External Interrupt 0 The table below trigger.

External Interrupt 0 Trigger control

ISC0 [1: 0]	description				
0	External pin INT0 Low trigger				
1	External pin INT0 Rising or falling edge trigger				
2	External pin INT0 Falling edge				
3	External pin INT0 Rising edge triggered				

External Interrupt Mask Register - EIMSK

EIMSK - External interrupt mask register											
address: 0x3D				Defaults	Defaults: 0x00						
Bit	7	6	5	4	3	2	1	0			
Name	-	-	-	-	-	-	INT1	INT0			
R/W	-	-	-	-	-	- R / W		R/W			
Bit	Name descrip	ption									
7: 2	-	Retention									
1	INT1 Extern	external pin 1 Interrupt enable control bit.									
		When set INT1 Bit "1" When, and Global Interrupt set, external pin 1 Interrupts are enabled, wake-up function is									
		enabled. even if INT1 Pin is configured as an output pin corresponding change in level occurs, an interrupt will									
		be generated. When set INT1 Bit "0" When the external pin 1 Interrupts are disabled, wake-up function is also									
		disabled.									