

Version History

V1.0.4 2017/11/15	correct SSOP20 PIN8 / 11 Definition
V1.0.3 2017/6/23	increase SSOP20 Package definition updates TMR3 Interrupt flag Operating instructions
V1.0.2 2017/5/15	Update TMR0 / TRM1 / TMR3 Regarding automatic PWM Shut down and restart the Notes Update SPI Section of SPI Interrupt processing instructions And update SPFR Register descriptions
V1.0.1 2017/2/13	delete I2C1 Part, this feature is not used to improve the definition section of the register
V1.0.0 2016/12/29	initial version