

PB3	MOSI (SPI Bus master output / input) OC2A (Timer / Counter 2 Compare Match Output A) PCINT3 (Pin Change Interrupt 3)
PB2	SSN (SPI Bus Slave Select Input) OC1B (Timer / Counter 1 Compare Match Output B) PCINT2 (Pin Change Interrupt 2)
PB1	OC1A (Timer / Counter 1 Compare Match Output A) PCINT1 (Pin Change Interrupt 1)
PB0	ICP1 (Timer / Counter 1 Capture input) CLKO (System clock output) PCINT0 (Pin Change Interrupt 0)

XTALI / TOSC2 / PCINT7 - port B Pin 7

XTALI: External crystal pins XI . When the clock signal is used as the crystal, this pin can not be used as I / O use.

TOSC2: Timer external crystal pins 2 . When the internal RC Is configured as master chip clock and asynchronous timer function is enabled (ASSR Configuration register), this pin as an external oscillator pins timer. when ASSR Register AS2 set as 1 , EXCLK Is set to 0 , Will enable the timer / counter 2 Using an external asynchronous crystal clock function, PB7 The internal I / O Disconnected from the port, becoming the internal oscillator amplifier inverting output pin. In this mode, an external crystal is connected to the pin.

PCINT7: Pin Change Interrupt 7 . PB7 External interrupt sources. in case PB7 Crystal pins are used, DDB7, PORTB7 with PINB7

The value will not make any sense.

XTALO / TOSC1 / PCINT6- port B Pin 6

XTALO: External crystal pins XO .

TOSC1: Timer external crystal pins 1 . When the internal RC Is configured as master chip clock and asynchronous timer function is enabled (ASSR Configuration register), this pin as an external oscillator pins timer. when ASSR Register AS2 set as 1 , EXCLK Is set to 0 , Will enable the timer / counter 2 Using an external asynchronous crystal clock function, PB6 The internal I / O Port port pin as an input internal oscillator amplifier. In this mode, an external crystal is connected to the pin.

PCINT6: Pin Change Interrupt 6 . PB6 External interrupt sources. in case PB6 Crystal pins are used, DDB6, PORTB6 with PINB6

The value will not make any sense.

SCK / PCINT5- port B Pin 5

SCK: SPI The controller master clock output from the clock input device. when SPI The controller is configured as a device, this pin is configured as an input from the pin, from DDB5 control. when SPI The controller is configured as a master, in this direction by the pin DDB5 control. When this pin is SPI Forced to enter, can still PORTB5

Pull-up resistor control bits.

PCINT5: Pin change interrupt. PB5 External interrupt sources.

MISO / PCINT4- port B Pin 4

MISO: SPI Master control device data input, data output from the device. when SPI Configured as a master, this pin will be forced to enter, not subject to DDB4 control. when SPI As a slave device, the data side pin