

*Interrupt vector processing*

The following code is only LGT8F88P For example, reset and interrupt vectors for explaining the programming, for reference only:

Examples of assembly code - LGT8F88P		
address	Code	Explanation
0x000	RJMP RESET RJMP	Reset Vector
0x001	EXT_INT0 RJMP	External Interrupt 0
0x002	EXT_INT1 RJMP PCINT0	External Interrupt 1
0x003	RJMP PCINT1 RJMP	Pin Change Interrupt 0
0x004	PCINT2 RJMP WDT RJMP	Pin Change Interrupt 1
0x005	TIM2_COMPA RJMP	Pin Change Interrupt 2
0x006	TIM2_COMPB RJMP	Watchdog timer interrupt timer 2 Compare
0x007	TIM2_OVF RJMP	match A Group interrupt timer 2 Compare match
0x008	TIM1_CAPT RJMP	B Group interrupt timer 2 Overflow interrupt
0x009	TIM1_COMPA RJMP	timer 1 Capture interrupt timer 1 Compare
0x00A	TIM1_COMPB RJMP	match A Group interrupt timer 1 Compare match
0x00B	TIM1_OVFR RJMP	B Group interrupt timer 1 Overflow interrupt
0x00C	TIM0_COMPA RJMP	timer 0 Compare match A Group interrupt timer 0
0x00D	TIM0_COMPB RJMP	Compare match B Group interrupt timer 0 Overflow
0x00E	TIM0_OVF RJMP SPI_STC	
0x00F	RJMP USART_RXC RJMP	
0x010	USART_UDRE RJMP	
0x011	USART_TXC RJMP ADC	SPI Transmission complete interrupt
0x012	RJMP EE_RDY RJMP	USART Reception completion interrupt
0x013	ANA_COMP RJMP TWI	USART Data Register Empty
0x014	NOP NOP RJMP PC13	USART Send complete interrupt
0x015		ADC Conversion Complete Interrupt
0x016		EEPROM Ready Interrupt Controller
0x017		Comparator Interrupt
0x018		TWI Reserved interrupt controller
0x019		addresses are reserved address pin
0x01A		change interrupt 3
0x01B;		
0x01C ( RESET :)	LDI r16, high (RAMEND)	Set the stack pointer to start the main program RAM The
0x01D	OUT SPH, r16 LDI	top address
0x01E	r16, low (RAMEND) OUT	
0x01F	SPL, r16 SEI .....	
0x020		Enable global interrupt
0x021		