

		Operating mode	The baud rate is calculated
		Slave mode	The baud rate is determined by the external host
		Host mode	$BAUD = f_{sys} / (2 * (UBRR + 1))$

UDR - USPI Data register

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address: 0xC6					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
Name	UDR7	UDR6	UDR5	UDR4	UDR3	UDR2	UDR1	UDR0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Name		description						
7: 0	UDR	<p>USPI Transmission and reception of data.</p> <p>USPI Transmission data and reception data buffer shared buffer USPI Data register UDR . The data is written UDR That is written to the transmit data buffer, from UDR I.e., the read data read received data buffer. in 5 To 8 Lower frame mode data, the unused 9 Bits are ignored transmitter and the receiver they are set to 0 . Only when UCSRA Register UDRE Flag "1" When the transmit buffer to write, otherwise the operation of the transmitter to be wrong. When the transmit shift register is empty, the transmitter will transmit data in the buffer is loaded into the transmit shift register, and then serially from the data TxD Pin output. A receive buffer contains two FIFO Once the receive buffer is read, FIFO It will change its state.</p>						