		When set PCINT36 Bit "1" Time, PF4 Pin change interrupt is enabled. PF4 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT36 Bit "0" Time, PF4 Pin change interrupts are disabled.
3		PCINT35 Pin change enable mask 35 . When set PCINT35 Bit "1" Time, PF3 Pin change interrupt is enabled. PF3 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT35 Bit "0" Time, PF3 Pin change interrupts are disabled.
2		PCINT34 Pin change enable mask 34 . When set PCINT34 Bit "1" Time, PF2 Pin change interrupt is enabled. PF2 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT34 Bit "0" Time, PF2 Pin change interrupts are disabled.
1		PCINT33 Pin change enable mask 33 . When set PCINT33 Bit "1" Time, PF1 Pin change interrupt is enabled. PF1 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT33 Bit "0" Time, PF1 Pin change interrupts are disabled.
0		PCINT32 Pin change enable mask 32 . When set PCINT31 Bit "1" Time, PF0 Pin change interrupt is enabled. PF0 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT32 Bit "0" Time, PF0 Pin change interrupts are disabled.