

23	<i>Self-Programming the Flash, ATmega48P</i>	269
23.1	Overview	269
23.2	Addressing the Flash During Self-Programming	270
23.3	Register Description	275
24	<i>Boot Loader Support – Read-While-Write Self-Programming, ATmega88P, ATmega168P and ATmega328P</i>	277
24.1	Features	277
24.2	Overview	277
24.3	Application and Boot Loader Flash Sections	277
24.4	Read-While-Write and No Read-While-Write Flash Sections	278
24.5	Boot Loader Lock Bits	280
24.6	Entering the Boot Loader Program	281
24.7	Addressing the Flash During Self-Programming	282
24.8	Self-Programming the Flash	282
24.9	Register Description	292
25	<i>Memory Programming</i>	294
25.1	Program And Data Memory Lock Bits	294
25.2	Fuse Bits	295
25.3	Signature Bytes	298
25.4	Calibration Byte	299
25.5	Page Size	299
25.6	Parallel Programming Parameters, Pin Mapping, and Commands	299
25.7	Parallel Programming	301
25.8	Serial Downloading	308
26	<i>Electrical Characteristics</i>	313
26.1	Absolute Maximum Ratings*	313
26.2	DC Characteristics	313
26.3	Speed Grades	316
26.4	Clock Characteristics	319
26.5	System and Reset Characteristics	320
26.6	SPI Timing Characteristics	321
26.7	2-wire Serial Interface Characteristics	323
26.8	ADC Characteristics – Preliminary Data	325
26.9	Parallel Programming Characteristics	326
27	<i>Typical Characteristics</i>	328