OC0A / PCINT28- port E Pin 4

OC0A: Timer / Counter 0 of A Group match output. PE4 As timer / counter 0 Compare match outside. At this point must DDE4

The output pin is set. Simultaneously, OC0A Also timer 0 of PWM Mode output pin.

PCINT28: Pin Change Interrupt 28

ADC7 / AC1N / PCINT27- port E Pin 3

ADC7: ADC Input channel 7 . DIDR Close register number of multiplexed analog I / O The digital functions, to avoid interference with the digital part of

the analog circuit. For details, see ADC Related Sections

AC1N: Analog comparator 1 Negative input

PCINT27: Pin Change Interrupt 27

SWD / PCINT26- port E Pin 2

SWD: SWD Commissioning data lines. PE2 The default is SWD Features. Users can be MCUSR register SWDD

position 1 shut down SWD Debugger function. SWD After being closed, the debugging features will not be used.

PCINT26: Pin Change Interrupt 26

ADC6 / ACXP / PCINT25- port E Pin 1

ADC6: ADC Input channel 6 . DIDR Close register number of multiplexed analog I / O The digital functions, to avoid interference with the digital part of

the analog circuit. For details, see ADC Related Sections

ACXP: Analog comparator 0/1 Public input positive terminal

PCINT25: Pin Change Interrupt 25

SWC / APN4 / PCINT24- port E Pin 0

SWC: SWD The debugger clock line. PE0 The default is SWC Features. Users can be MCUSR register SWDD

position 1 shut down SWD Debugger function. SWD After being closed, the debugging features will not be used

APN4: Inverting input of the differential amplifier 4

PCINT24: Pin Change Interrupt twenty four