

OCR3CH	0x9F	0x00	TC3 Output Compare Register C High Byte
TIMSK3	0x71	0x00	Timer counter interrupt mask register
TIFR3	0x38	0x00	Timer counter Interrupt Flag Register

TCCR3A-TC3 Control register A

TCCR3A - TC3 Control register A								
address: 0x90					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
Name	COM3A1 COM3A0 COM3B1			COM3B0 COM3C1 COM3C0 WGM31 WGM30				
R / W	R / W	R / W	R / W	R / W	W	W	R / W	R / W
Bit	Name description							
7	COM3A1	<p>Compare Match Output A Mode control high.</p> <p>COM3A1 with COM3A0 composition COM3A [1: 0] To control the output waveform of comparator OC3A . in case COM3A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC3A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM3A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
6	COM3A0	<p>Compare Match Output A Mode control low.</p> <p>COM3A1 with COM3A0 composition COM3A [1: 0] To control the output waveform of comparator OC3A . in case COM3A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC3A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM3A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
5	COM3B1	<p>Compare Match Output B Mode control high.</p> <p>COM3B1 with COM3B0 composition COM3B [1: 0] To control the output waveform of comparator OC3B . in case COM3B of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC3B Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM3B The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
4	COM3B0	<p>Compare Match Output B Mode control low.</p> <p>COM3B1 with COM3B0 composition COM3B [1: 0] To control the output waveform of comparator OC3B . in case COM3B of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC3B Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM3B The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
3	COM3C1	<p>Compare Match Output C Mode control high.</p> <p>COM3C1 with COM3C0 composition COM3C [1: 0] To control the output waveform of comparator OC3C . in case COM3C of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC3C Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM3C The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
2	COM3C0	<p>Compare Match Output C Mode control low.</p> <p>COM3C1 with COM3C0 composition COM3C [1: 0] To control the output waveform of comparator OC3C . in case</p>						