		When set PCINT27 Bit "1" Time, PE3 Pin change interrupt is enabled. PE3 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have PCI3 Interrupted. When set PCINT27 Bit "0" Time, PE3 Pin change interrupts are disabled.
2	PCINT26 Pin change enable mask 26 .	When set PCINT26 Bit "1" Time, PE2 Pin change interrupt is enabled. PE2 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have PCI3 Interrupted. When set PCINT26 Bit "0" Time, PE2 Pin change interrupts are disabled.
1	PCINT25 Pin change enable mask 25 .	When set PCINT25 Bit "1" Time, PE1 Pin change interrupt is enabled. PE1 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have PCI3 Interrupted. When set PCINT25 Bit "0" Time, PE1 Pin change interrupts are disabled.
0	PCINT24 Pin change enable mask twenty four .	When set PCINT24 Bit "1" Time, PE0 Pin change interrupt is enabled. PE0 Level change on the pin will be set PCIF3 If the PCIE3 And a global interrupt bit set, will have PCI3 Interrupted. When set PCINT24 Bit "0" Time, PE0 Pin change interrupts are disabled.

PCMSK4 - Pin change interrupt mask register 4

PCMSK4 - Pin change mask register 4							
address: 0x74				Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	PCINT39	PCINT38	PCINT37	PCINT36	PCINT35	PCINT34	PCINT33	PCINT32
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W

Bit	Name description
7	PCINT39 Pin change enable mask 39 . When set PCINT39 Bit "1" Time, PF7 Pin change interrupt is enabled. PF7 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT39 Bit "0" Time, PF7 Pin change interrupts are disabled.
6	PCINT38 Pin change enable mask 38 . When set PCINT38 Bit "1" Time, PF6 Pin change interrupt is enabled. PF6 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT38 Bit "0" Time, PF6 Pin change interrupts are disabled.
5	PCINT37 Pin change enable mask 37 . When set PCINT37 Bit "1" Time, PF5 Pin change interrupt is enabled. PF5 Level change on the pin will be set PCIF4 If the PCIE4 And a global interrupt bit set, will have PCI4 Interrupted. When set PCINT37 Bit "0" Time, PF5 Pin change interrupts are disabled.
4	PCINT36 Pin change enable mask 36 .