

```

Address  Labels Code          Comments
;
.org 0x1C00
0x1C00      jmp    RESET      ; Reset handler
0x1C02      jmp    EXT_INT0    ; IRQ0 Handler
0x1C04      jmp    EXT_INT1    ; IRQ1 Handler
...          ...      ...      ;
0x1C32      jmp    SPM_RDY     ; Store Program Memory Ready Handler
;
0x1C33  RESET: ldi    r16,high(RAMEND); Main program start
0x1C34      out    SPH,r16     ; Set Stack Pointer to top of RAM
0x1C35      ldi    r16,low(RAMEND)
0x1C36      out    SPL,r16
0x1C37      sei                      ; Enable interrupts
0x1C38      <instr>  xxx

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9.4 Interrupt Vectors in ATmega328P

Table 9-6. Reset and Interrupt Vectors in ATmega328P

VectorNo.	Program Address ⁽²⁾	Source	Interrupt Definition
1	0x0000 ⁽¹⁾	RESET	External Pin, Power-on Reset, Brown-out Reset and Watchdog System Reset
2	0x0002	INT0	External Interrupt Request 0
3	0x0004	INT1	External Interrupt Request 1
4	0x0006	PCINT0	Pin Change Interrupt Request 0
5	0x0008	PCINT1	Pin Change Interrupt Request 1
6	0x000A	PCINT2	Pin Change Interrupt Request 2
7	0x000C	WDT	Watchdog Time-out Interrupt
8	0x000E	TIMER2 COMPA	Timer/Counter2 Compare Match A
9	0x0010	TIMER2 COMPB	Timer/Counter2 Compare Match B
10	0x0012	TIMER2 OVF	Timer/Counter2 Overflow
11	0x0014	TIMER1 CAPT	Timer/Counter1 Capture Event
12	0x0016	TIMER1 COMPA	Timer/Counter1 Compare Match A
13	0x0018	TIMER1 COMPB	Timer/Counter1 Compare Match B
14	0x001A	TIMER1 OVF	Timer/Counter1 Overflow
15	0x001C	TIMER0 COMPA	Timer/Counter0 Compare Match A
16	0x001E	TIMER0 COMPB	Timer/Counter0 Compare Match B
17	0x0020	TIMER0 OVF	Timer/Counter0 Overflow
18	0x0022	SPI, STC	SPI Serial Transfer Complete
19	0x0024	USART, RX	USART Rx Complete
20	0x0026	USART, UDRE	USART, Data Register Empty
21	0x0028	USART, TX	USART, Tx Complete