

32. Errata

32.1 Errata ATmega48P

The revision letter in this section refers to the revision of the ATmega48P device.

32.1.1 Rev. C

No known errata.

32.1.2 Rev. B

No known errata.

32.1.3 Rev. A

Not Sampled.

32.2 Errata ATmega88P

The revision letter in this section refers to the revision of the ATmega88P device.

32.2.1 Rev. C

Not sampled.

32.2.2 Rev. B

No known errata.

32.2.3 Rev. A

No known errata.

32.3 Errata ATmega168P

The revision letter in this section refers to the revision of the ATmega168P device.

32.3.1 Rev B

No known errata.

32.3.2 Rev A

No known errata.

32.4 Errata ATmega328P

The revision letter in this section refers to the revision of the ATmega328P device.

32.4.1 Rev C

No known errata.

32.4.2 Rev B

- **Unstable 32 kHz Oscillator**

1. **Unstable 32 kHz Oscillator**

The 32 kHz oscillator does not work as system clock.

The 32 kHz oscillator used as asynchronous timer is inaccurate.