port D Alternate Function

Pin	Multiplexing Function Description
PD7	ACXN (Analog comparator 0/1 Common negative input) PCINT23 (Pin Change Interrupt twenty three)
PD6	AC0P (QFP32: Analog comparator 0 Positive input) OC0A (Timer / Counter 0 Compare Match Output A) OC3A (QFP32: Timer / Counter 3 Compare Match Output A) PCINT22 (Pin Change Interrupt twenty two)
PD5	T1 (Timer / Counter 1 External count clock input) OC0B (Timer / Counter 0 Compare Match Output B) PCINT21 (Pin Change Interrupt twenty one)
PD4	XCK (USART External Clock Input / Output) DAO (internal 8bit DAC Analog Output) T0 (Timer / Counter 0 External count clock input) PCINT20 (Pin Change Interrupt 20)
PD3	INT1 (External interrupt input 1) OC2B (Timer / Counter 2 Compare Match Output B) PCINT19 (Pin Change Interrupt 19)
PD2	INT0 (External interrupt input 0) AC0O (Comparators 0 Output) OC3B (QFP32: Timer / Counter 3 Compare Match Output B) PCINT18 (Pin Change Interrupt 18)
PD1	TXD (USART Data output) OC3A (QFP32: Timer / Counter 3 Compare Match Output A) PCINT17 (Pin Change Interrupt 17)
PD0	RXD (USART data input) PCINT16 (Pin Change Interrupt 16)

ACXN / OC2B / PCINT23- port D Pin 7

ACXN: Analog comparator 0/1 Public negative input

OC2B: Timer / Counter 2 of B Group match output. PD7 As timer / counter 2 Compare match outside. At this point must DDD7 The output pin is set. Simultaneously, OC2B Also timer 2 of PWM Mode output pin;

PCINT23: Pin Change Interrupt twenty three

AC0P / OC0A / PCINT22- port D Pin 6

AC0P: Analog comparator 0 Positive input.

OC0A: Timer / Counter 0 of A Group match output. PD6 As timer / counter 0 Compare match outside. At this point must DDD6 The output pin is set. Simultaneously, OC0A Also timer 0 of PWM Mode output pin

PCINT22: Pin Change Interrupt twenty two