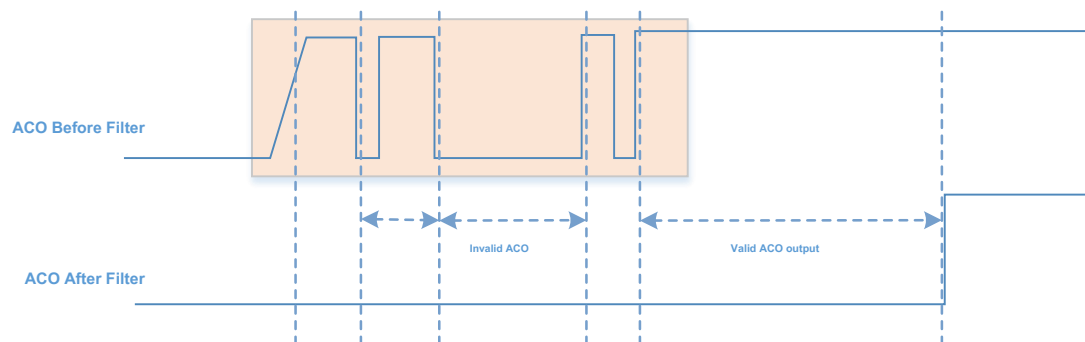


Although the hysteresis circuit is effective in suppressing the ripple voltage near the threshold comparator, the actual application environment, the input signal is subject to interference of different intensity. Strong interference may cause momentary input level elevation, the range of the hysteresis circuit exceeds a threshold, it can not be effectively suppressed. LGT8FX8P The output of the comparator integrates a programmable digital filter can filter out the influence of the instantaneous interference generated by the comparator output. The digital filter according to the application needs, to select the time width appropriate filtering, only when the output of the comparator is stable for the time limit to meet the filter, the filter circuit output of the comparator is updated. So as to achieve a more stable output.



A timing comparator output filter

AC1 By digital filtering C1XR Register C0FEN as well as C1FS Control bits, refer to the specific register arrangement defined in this chapter.

### ***The comparator output PWM control***

LGT8FX8P Multi-channel support PWM Output, PWM Signals can be used with the comparator module. The output of the comparator, can be used directly off PWM Signal, in order to achieve a more flexible PWM Protection scheme.

versus PWM Related control output, please refer to the relevant part of the timer section.