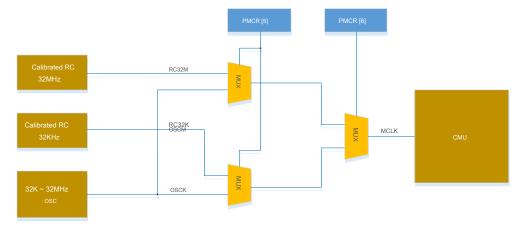
After the clock is enabled and waiting for stabilization, by PMCR [6: 5] Switching the master clock. among them PMCR [5] Selects internal RC And an external crystal oscillator, PMCR [6] For selecting the low-speed and high-speed clock source clock source.



Master clock source selection:

PMCR [6]	PMCR [5]	Master clock source
0	0	internal 32MHz RC Oscillator (default)
0	1	external 400K ~ 32MHz Fast Crystal
1	0	internal 32KHz RC Oscillator
1	1	external 32K ~ 400KHz Low-speed oscillator

Timing Clock Source Control

To protect PMCR Register unexpected modification of PMCR Modifications of the register needs to be strictly specified installation sequence.

PMCR MSB register (PMCR [7]) For implementing timing control. Users modify PMCR Before other bits, you must first of all to PMCR [7] Put 1 At home

1 After the operation 6 Within a period of change PMCR Other registers. 6 After a period of PMCR Direct modification will fail.

Below to switch to the external high frequency oscillator, for example, lists the suggested steps:

- (1) Enable clock source
 - Set up PMCR [7] = 1
 - Within a period of six set PMCR [2] = 1 , An external high speed mode to enable the external oscillator
 - Waiting for an external crystal oscillator is stable (wait time varies due to different crystal, general us Level can wait)
- (2) Switching the primary clock source
 - Set up PMCR [7] = 1
 - Within a period of six set PMCR [6: 5] = 01 The system operates automatically switches to the external oscillator clock
 - Performs several NOP Operation, to improve the stability (optional)

[NOTE]: In the above switching operation of the master clock, the system clock to ensure that the current normal operation, after switching to the external crystal, it can be closed before the interior RC Oscillator.