PWM Off automatically after the output is enabled, which also need to set the trigger conditions from TCCR0C Register DSX0n Bits to select trigger source. Triggered by an analog comparator interrupt, external interrupt, the interrupt pin change and the timer overflow interrupt, please refer to the specific circumstances TCCR0C Register description. Or when a certain trigger source is selected as the trigger condition, in which the interrupt flag is set at the same time, the hardware will be cleared COM0x Bit to close PWM Output.

In the event of a triggering event closed PWM After the output, the timer module is no corresponding interrupt flag, the software needs to know the trigger and the trigger event by source interrupt flag read.

when PWM When the output is automatically switched off and the need to restart output again, the software only needs to be reset COM0x Position to switch OC0x Signal is output to the corresponding pin. It should be noted, occurs automatically shut down after the timer did not stop working, OC0x State of the signal has also been updated. After the software or compare match timer overflows, then set COM0x Bit output OC0x Signal, so you can get a clear PWM Output state.

Dead-time control

Set up DTEN0 Bit "1" When inserting the dead time function is enabled, OC0A with OC0B The output waveform will B Deadtime comparator output channel waveform based on the generated set of insertion, the length of time of DTR0 Register count clock number corresponding to the time value. As shown below, OC0A with OC0B Deadtime insertion are based channel B Comparing the output waveform as a reference. when COM0A with COM0B The same "2" or "3" Time, OC0A The polarity of the waveform OC0B The waveform of the same polarity, when COM0A with COM0B Respectively "2" or "3" Time, OC0A The waveform OC0B Opposite polarity waveform.

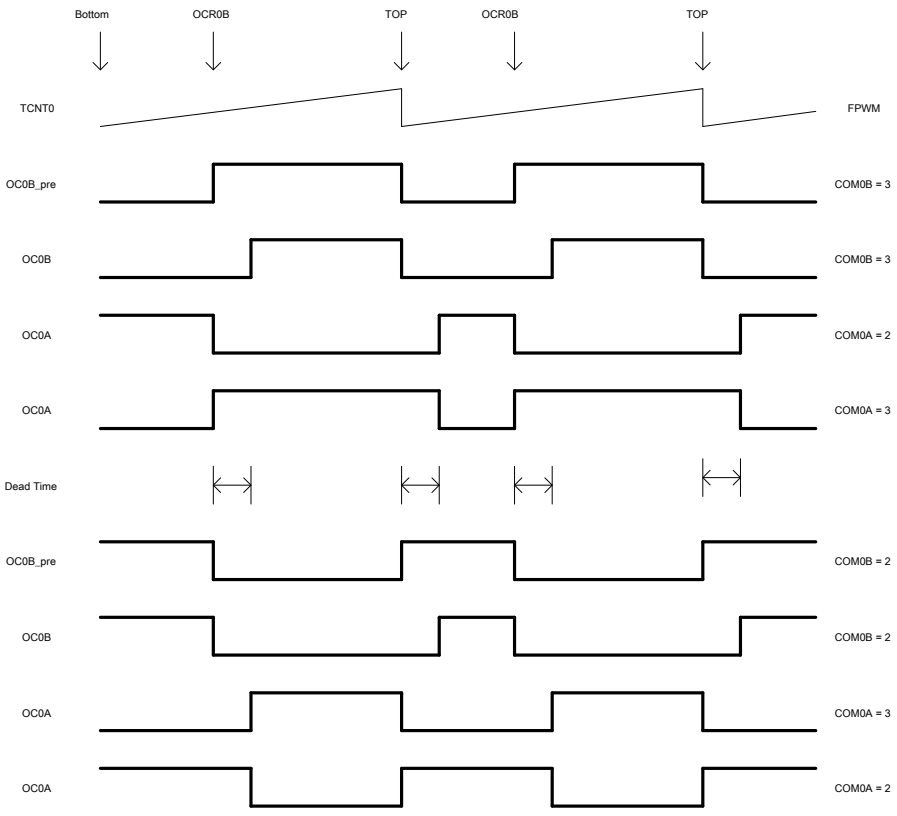

Figure 1 FPWM Mode TC0 Dead-time control