

Name	TCNT3L7	TCNT3L6	TCNT3L5	TCNT3L4	TCNT3L3	TCNT3L2	TCNT3L1	TCNT3L0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit	Name	description						
7: 0	TCNT3L	<p>TC3 Low byte count value.</p> <p>TCNT3H with TCNT3L Incorporated into the composition together TCNT3 ,by TCNT3 Directly to the counter register 16 Place</p> <p>The count value read and write access. Read and write 16 Bit register requires two operations. write 16 Place TCNT3 When, you should write TCNT3H .</p> <p>read 16 Place TCNT3 When, it should read TCNT3L .</p> <p>CPU Correct TCNT3 Writes to this register on the next clock cycle to prevent a compare match timer occurs, even if the timing</p> <p>It has stopped. This allows initialization TCNT3 And the value of the register OCR3x The value of the agreement without causing disruption.</p> <p>If you write TCNT3 The value is equal to or bypassed OCR3x Value, compare match will be lost, resulting in incorrect waveform hair</p> <p>Health results.</p> <p>When the timer stops counting the clock source is not selected, but CPU Still access TCNT3 . CPU Write counter than plus or cleared</p> <p>High priority subtraction operation.</p>						

TCNT3H-TC3 High Byte Counter Register

TCNT3H - TC3 High byte count value register								
address: 0x95					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
Name	TCNT3H7	TCNT3H6	TCNT3H5	TCNT3H4	TCNT3H3	TCNT3H2	TCNT3H1	TCNT3H0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit	Name	description						
7: 0	TCNT3H	<p>TC3 The count value of the high byte.</p> <p>TCNT3H with TCNT3L Incorporated into the composition together TCNT3 ,by TCNT3 Directly to the counter register 16 Level Meter</p> <p>Values for read and write access. Read and write 16 Bit register requires two operations. write 16 Place TCNT3 When, you should write TCNT3H .</p> <p>read 16 Place TCNT3 When, it should read TCNT3L .</p> <p>CPU Correct TCNT3 Writes to this register on the next clock cycle to prevent a compare match timer occurs, even if the timing</p> <p>It has stopped. This allows initialization TCNT3 And the value of the register OCR3x The value of the agreement without causing disruption.</p> <p>If you write TCNT3 The value is equal to or bypassed OCR3x Value, compare match will be lost, resulting in incorrect waveform hair</p> <p>Health results.</p> <p>When the timer stops counting the clock source is not selected, but CPU Still access TCNT3 . CPU Write counter than plus or cleared</p> <p>High priority subtraction operation.</p>						

ICR3L-TC3 Capture register low byte

ICR3L - TC3 Input Capture Register Low Byte								
address: 0x96					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
Name	ICR3L7	ICR3L6	ICR3L5	ICR3L4	ICR3L3	ICR3L2	ICR3L1	ICR3L0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit	Name	description						
7: 0	ICR3L	<p>TC3 Input capture the low byte value.</p> <p>ICR3H with ICR3L Incorporated into the composition together 16 Bit ICR3 . Read and write 16 Bit register requires two operations. write 16 Place</p> <p>ICR3 When, you should write ICR3H . read 16 Place ICR3 When, it should read ICR3L .</p>						