

| QFP48 | QFP32 | SSOP20 | |
|-------|-------|--------|--|
| 39 | 27 | 19 | PC4/ADC4/SDA |
| | | | PC4: I/O Pin C4 ADC4: ADC Input Channel 4 SDA: I2C Data |
| 40 | 28 | 20 | PC5/ADC5/SCL |
| | | | PC5: I/O Pin C5 ADC5: ADC Input Channel 5 SCL: I2C Clock |
| 41 | 29 | 1 | PC6/RESETN |
| | | | PC6: I/O Pin C6 RESETN: External Reset Input |
| 42 | - | - | PC7/ADC8/APN2 |
| | | | PC7: I/O Pin C7 ADC8: ADC Input Channel 8 APN2: Differential Amplifier Channel 2 Inverting Input |
| 43 | - | - | PF0/ADC9/APN3 |
| | | | PF0: I/O Pin F0 ADC9: ADC Input Channel 9 APN3: Differential Amplifier Channel 3 Inverting Input |
| 44 | 30 | - | PD0/RXD |
| | | | PD0: I/O Pin D0 RXD: USART Receive |
| 45 | 31 | - | PD1/TXD |
| | | | PD1: I/O Pin D1 TXD: USART Transmit |
| 46 | 31 | 1 | PF1/OC3A |
| | | | PF1: I/O Pin F1 OC3A: Timer/Counter 3 Output Compare Match A |
| 47 | 32 | 2 | PD2/INT0/AC00 |
| | | | PD2: I/O Pin D2 INT0: External Interrupt 0 Input AC00: Analog Comparator 0 Output |
| 48 | 32 | 2 | PF2/OC3B |
| | | | PF2: I/O Pin F2 OC3B: Timer/Counter 3 Output Compare Match B |
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