TC0 Control register C - TCCR0C

TCCR0C - *TC0 Control register C*							
address: 0x49				Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	DSX07	DSX06	DSX05	DSX04	-	-	DSX01	DSX00
R / W	R / W	R / W	R / W	R / W	-	-	R / W	R / W

Bit	Name description	
7	DSX07	TC0 Select the trigger source control enables the first 7 Bit. When set DSX07 Bit "1" Time, TC1 As the output of the comparator is off the overflow signal waveform OC0A / OC0B The trigger source is enabled. when DOC0A / DOC0B Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC0A / OC0B The waveform output. When set DSX07 Bit "0" Time, TC1 As the output of the comparator is off the overflow signal waveform OC0A / OC0B The trigger source is prohibited.
6	DSX06	TC0 Select the trigger source control enables the first 6 Bit. When set DSX06 Bit "1" Time, TC2 As the output of the comparator is off the overflow signal waveform OC0A / OC0B The trigger source is enabled. when DOC0A / DOC0B Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC0A / OC0B The waveform output. When set DSX06 Bit "0" Time, TC2 As the output of the comparator is off the overflow signal waveform OC0A / OC0B The trigger source is prohibited.
5	DSX05	TC0 Select the trigger source control enables the first 5 Bit. When set DSX05 Bit "1" When, pin change 0 As a comparison output signal waveform is off OC0A / OC0B The trigger source is enabled. when DOC0A / DOC0B Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC0A / OC0B The waveform output. When set DSX05 Bit "0" When, pin change 0 As a comparison output signal waveform is off OC0A / OC0B The trigger source is prohibited.
4	DSX04	TC0 Select the trigger source control enables the first 4 Bit. When set DSX04 Bit "1" When the external interrupt 0 As a comparison output signal waveform is off OC0A / OC0B The trigger source is enabled. when DOC0A / DOC0B Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC0A / OC0B The waveform output. When set DSX04 Bit "0" When the external interrupt 0 As a comparison output signal waveform is off OC0A / OC0B The trigger source is prohibited.
3: 2	-	Are reserved
1	DSX01	TC0 Select the trigger source control enables the first 1 Bit. When set DSX01 Bit "1" When, analog comparator 1 As a comparison output signal waveform is off OC0A / OC0B The trigger source is enabled. when DOC0A / DOC0B Bit "1" Rising edge triggered interrupt source, the selected flag register bits will automatically shut down OC0A / OC0B The waveform output.