

		<p>100 = ADC5 101 = External reference input ( AVREF)</p> <p>110 = System Power</p>
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**DAPCR - Differential amplifier control register**

DAPCR - Differential amplifier control register								
address: 0xDC					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
Name	DAPEN	GA1	GA0	DNS2	DNS1	DNS0	DPS1	DPS0
R / WW / R		W / R	W / R	W / R	W / R	R / W	R / W	R / W
Bit	Name	description						
7	DAPEN	1 = Enabling differential amplifier; 0 = Close differential amplifier						
6: 5	GA	[1: 0] Differential amplifier gain control						
		00 = x1 01 = = x8 10 = x16 11 = x32						
4: 2	DNS	[2: 0] Inverting input terminal of the differential amplifier input source selection						
		000 = ADC2 / APN0 001 = ADC3 / APN1 010 = ADC8 / APN2 011 = ADC9 / APN3 100 = PE0 / APN4 101 = ADC Multiplexer  110 = AGND 111 = Close inverting input of the differential amplifier						
1: 0	DPS	[1: 0] The positive input terminal of the differential amplifier source selection						
		00 = ADC Multiplexer 01 = ADC0 / APP0 10 = ADC1 / APP1 11 = AGND						

**OFR0 - Offset compensation register 0**

OFR0 - Offset compensation register 0								
address: 0xA3					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
Name	OFR0 [7: 0]							
R / W	W / R							
Bit	Name	description						
7: 0	OFR0	Offset compensation register 0 ; OFR0 As signed. Stored in twos complement format						