

Addr	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
\$ 8A	<a href="#">OCR1BL</a>	Timer 1 Output Compare B Low							
\$ 89	<a href="#">OCR1AH</a>	Timer 1 Output Compare A High							
\$ 88	<a href="#">OCR1AL</a>	Timer 1 Output Compare A Low							
\$ 87	<a href="#">ICR1H</a>	Timer 1 Input Capture High							
\$ 86	<a href="#">ICR1L</a>	Timer 1 Input Capture Low							
\$ 85	<a href="#">TCNT1H</a>	Timer 1 Counter High							
\$ 84	<a href="#">TCNT1L</a>	Timer 1 Counter Low							
\$ 83	<a href="#">TCR1D</a>	DSX17	DSX16	DSX15	DAX14	-	-	DSX11	DSX10
\$ 82	<a href="#">TCR1C</a>	FOC1A	FOC1B	DOC1B	DOC1A	DTEN1	-	-	-
\$ 81	<a href="#">TCR1B</a>	ICNC1	ICES1	-	WGM13	WGM12	CS1		
\$ 80	<a href="#">TCR1A</a>	COM1A1	COM1A0	COM1B1	COM1B0	-	-	WGM11	WGM10
\$ 7F	<a href="#">DIDR1</a>	PE7D	PE6D	PE0D	COPD	PF0D	PC7D	PD7D	PD6D
\$ 7E	<a href="#">DIDR0</a>	PE3D	PE1D	PC5D	PC4D	PC3D	PC2D	PC1D	PC0D
\$ 7D	<a href="#">ADCSRC</a>	OFEN	-	SPN	AMEN	-	SPD	DIFS	ADTM
\$ 7C	<a href="#">ADMUX</a>	REFS1	REFS0	ADLAR	CHMUX				
\$ 7B	<a href="#">ADCSR8</a>	CME01	CME00	CME11	CME10	-	ADTS		
\$ 7A	<a href="#">ADCSRA</a>	ADEN	ADSC	ADATE	ADIF	ADIE	ADPS		
\$ 79	<a href="#">ADCH</a>	ADC Data High							
\$ 78	<a href="#">ADCL</a>	ADC Data Low							
\$ 76	DIDR2	-	PB5D	-	-	-	-	-	-
\$ 75	<a href="#">IVBASE</a>	Interrupt Vector Base Address							
\$ 74	<a href="#">PCMSK4</a>								
\$ 73	<a href="#">PCMSK3</a>	PCINT [39:32]							
\$ 71	<a href="#">TIMSK3</a>			ICIE3	-	OCIE3C	OCIE3B	OCIE3A	TOIE3
\$ 70	<a href="#">TIMSK2</a>	-	-	-	-	-	OCIE2B	OCIE2A	TOIE2
\$ 6F	<a href="#">TIMSK1</a>	-	-	ICIE1	-	-	OCIE1B	OCIE1A	TOIE1
\$ 6E	<a href="#">TIMSK0</a>	-	-	-	-	-	OCIE0B	OCIE0A	TOIE0
\$ 6D	<a href="#">PCMSK2</a>	PCINT [23:16]							
\$ 6C	<a href="#">PCMSK1</a>	PCINT [15: 8]							
\$ 6B	<a href="#">PCMSK0</a>	PCINT [7: 0]							
\$ 69	<a href="#">EICRA</a>	-	-	-	-	ISC11	ISC10	ISC01	ISC00
\$ 68	<a href="#">EICR</a>	-	-	-	PCIE4	PCIE3	PCIE2	PCIE1	PCIE0
\$ 67	<a href="#">RCKCAL</a>	RC32K Calibration							
\$ 66	<a href="#">RCMCAL</a>	RC32M Calibration							
\$ 65	<a href="#">PRR1</a>	-	-	PRWDT	-	PRTIM3	PREFL	PRPCI	-
\$ 64	<a href="#">PRR10</a>	PRTWI	PRTIM2	PRTIM0	-	PRTIM1	PRSPI	PRUART0	PRADC
\$ 62	<a href="#">VDTCSR</a>	WCE	SWR	-	VDTs			VDREN	VDTEN
\$ 61	<a href="#">CLKPR</a>	WCE	CKOE1	CKOE0	-	CLKPS			
\$ 60	<a href="#">WDTCSPR</a>	WDIF	WDIE	WDP3	WDCE	WDE	WDP2	WDP1	WDP0
DirectIO Register									
\$ 5F	<a href="#">SREG</a>	I	T	H	S	V	N	Z	C
\$ 5E	<a href="#">SPH</a>	Stack Point High							