

Addr	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
\$8A	OCR1BL	Timer 1 Output Compare B Low							
\$89	OCR1AH	Timer 1 Output Compare A High							
\$88	OCR1AL	Timer 1 Output Compare A Low							
\$87	ICR1H	Timer 1 Input Capture High							
\$86	ICR1L	Timer 1 Input Capture Low							
\$85	TCNT1H	Timer 1 Counter High							
\$84	TCNT1L	Timer 1 Counter Low							
\$83	TCCR1D	DSX17	DSX16	DSX15	DAX14	-	-	DSX11	DSX10
\$82	TCCR1C	FOC1A	FOC1B	DOC1B	DOC1A	DTEN1	-	-	-
\$81	TCCR1B	ICNC1	ICES1	-	WGM13	WGM12	CS1		
\$80	TCCR1A	COM1A1	COM1A0	COM1B1	COM1B0	-	-	WGM11	WGM10
\$7F	DIDR1	PE7D	PE6D	PE0D	COPD	PF0D	PC7D	PD7D	PD6D
\$7E	DIDR0	PE3D	PE1D	PC5D	PC4D	PC3D	PC2D	PC1D	PC0D
\$7D	ADCSRC	OFEN	-	SPN	AMEN	-	SPD	DIFS	ADTM
\$7C	ADMUX	REFS1	REFS0	ADLAR	CHMUX				
\$7B	ADCSRB	CME01	CME00	CME11	CME10	-	ADTS		
\$7A	ADCSRA	ADEN	ADSC	ADATE	ADIF	ADIE	ADPS		
\$79	ADCH	ADC Data High							
\$78	ADCL	ADC Data Low							
\$76	DIDR2	-	PB5D	-	-	-	-	-	-
\$75	IVBASE	Interrupt Vector Base Address							
\$74	PCMSK4								
\$73	PCMSK3	PCINT[39:32]							
\$71	TIMSK3			ICIE3	-	OCIE3C	OCIE3B	OCIE3A	TOIE3
\$70	TIMSK2	-	-	-	-	-	OCIE2B	OCIE2A	TOIE2
\$6F	TIMSK1	-	-	ICIE1	-	-	OCIE1B	OCIE1A	TOIE1
\$6E	TIMSK0	-	-	-	-	-	OCIE0B	OCIE0A	TOIE0
\$6D	PCMSK2	PCINT[23:16]							
\$6C	PCMSK1	PCINT[15:8]							
\$6B	PCMSK0	PCINT[7:0]							
\$69	EICRA	-	-	-	-	ISC11	ISC10	ISC01	ISC00
\$68	PCICR	-	-	-	PCIE4	PCIE3	PCIE2	PCIE1	PCIE0
\$67	RCKCAL	RC32K Calibration							
\$66	RCMCAL	RC32M Calibration							
\$65	PRR1	-	-	PRWDT	-	PRTIM3	PREFL	PRPCI	-
\$64	PRR/0	PRTWI	PRTIM2	PRTIM0	-	PRTIM1	PRSPI	PRUART0	PRADC
\$62	VDTCSR	WCE	SWR	-	VDTS			VDREN	VDTEN
\$61	CLKPR	WCE	CKOE1	CKOE0	-	CLKPS			
\$60	WDTCR	WDIF	WDIE	WDP3	WDCE	WDE	WDP2	WDP1	WDPO
DirectIO Register									
\$5F	SREG	I	T	H	S	V	N	Z	C
\$5E	SPH	Stack Point High							