		When set PSS3 Bit "0" When, for the prescaler multiplexed mode. TC0 with TC3 Shared prescaler CPS310 . Prescaler CPS3 Invalid, it will have to be reset. If the PSS1 Bit at the same time "0" , TC1 with TC0 , TC3 Shared prescaler CPS310 . Prescaler CPS1 with CPS3 They are invalid and will always be reset.
5: 2	-	Reservations.
1	PSR3	Prescaler CPS3 Reset control bits. PSR3 Bit only TC3 Active alone mode. When set PSR3 Bit "1" When, prescaler CPS3 It will be reset. After hardware reset will clear PSR3 Bit. When set PSR3 Bit "0" When the setting is invalid. Read this value will always be a "0" .
0		PSR1 Prescaler CPS1 Reset control bits. PSR1 Bit only TC1 Active alone mode. When set PSR1 Bit "1" When, prescaler CPS1 It will be reset. After hardware reset will clear PSR1 Bit. When set PSR1 Bit "0" When the setting is invalid. Read this value will always be a "0" .