Register Definition

Low voltage detection (LVD) Control Register - VDTCR

VDTCR - LVD Control register								
VDTCR: 0x62					Defaults: 0x00			
Bits	WCE	SWR	- VDTS2		VDTS1	VDTS0 VDREN VDTEN		
R / W	R / WW / R		- R / W		R / W	R / W	R / W	R / W
Bit Definitions								
[0]	VDTEN	Low-pressure detecting module enable control, 1 Enable, 0 Ban						
[1]	VDREN Low voltage reset enable control function, 1 Enable, 0 Ban							
[4: 2]	VDTS	Low voltage detection threshold configuration bits 000 = 1.8V 001 = 2.2V 010 = 2.5V 011 = 2.9V 100 = 3.2V 101 = 3.6V 110 = 4.0V 111 = 4.4V						
[5]	-	Are reserved						
[6]	SWR	Soft Reset Enable bit, this bit is cleared to generate a software reset						
[7]	WCE	VDTCR Enable users to change the value of the change in VDTCR Before the value of the register, you must first write this bit 1 , After the 6 Clock cycles, change VDTCR The value of the other bits. After four cycles WCE Automatically cleared of VDTCR Register update operation is invalid.						

IO Register Function Multiplexing - PMX2

PMX2 - IO Register Function Multiplexing								
PMX2: 0xF0					Defaults: 0x00			
Bits	WCE	STSC1	STSC0	-	-	XIEN	E6EN	C6EN
R / W	R / W	R / W	R / W	-	- R / W		R / W	R / W
Bit Definitions								
0	C6EN	PC6 Pins default reset, this bit is set 1 External reset function is disabled, the reset function is disabled, PC6 It can be used as an ordinary I / O use						
1	E6EN	PE6 The default function as an analog input pin, setting this bit 1 , Closes the analog input, this pin can be used as GPIO use						
2	XIEN	External clock input enable control						
4: 3	-	Are reserved						
5	STSC0	Low-speed oscillator start control						
6	STSC1	High-speed crystal startup control						
7	WCE	IOCR Enable users to change the value of the change in IOCR Before the value of the register, you must first write this bit 1 ,in						