And sleep control input enable

From I / O The equivalent circuit diagram, we can see, the digital inputs can be SLEEP Under the control signal is clamped to the ground level. SLEEP Signal from the MCU Sleep controllers and various dormant mode control. This ensures that after entering hibernation, the system will not enter the port float caused by leakage.

Ports SLEEP Control of external interrupt function will be replaced. If an external interrupt request is invalid, SLEEP Control can still play a role. SLEEP Control functions may also be substituted by other second function, please refer to the following describes the specific port on the second function.

Fast Flip port status

State is set to output port IO ,able to pass PORTn Port status register changes. If the need to flip the current state output port, generally need to read the current status of the port PINx And then negated written back PORTn Register complete flip. LGT8FX8P Provide another more efficient way reversing port state, directly to the PINx Register Write
1 It can achieve the specified port status flip. For example, we write PINB [3] for 1 Can achieve PB3 The port status flip. For applications required to generate the output clock, this embodiment is very practical.

Digital / analog multiplexing port

LGT8FX8P Port number modulo function portion hybrid multiplexing port. In addition to internal DAC Output PD4 In addition, other mixing both as analog input ports. When used as an analog port, the software needs to be set to the input mode the port, and turn off the internal pull necessary to avoid influence on the analog revenue. DIDR0 ~ 2 Register for closing the mixing function port digital input channel, in order to avoid unnecessary power loss caused by the analog input to a digital circuit. DIDRx It does not close the digital output port.

High current push-pull drive port

LGT8FX8P Support for multiple 6 High-current push-pull drive ports, support up to 80mA The push-pull driver. Considering the chip VCC The maximum over current capacity constraints, is not recommended to open simultaneously 6 High-current drive. Especially for only one set of power supply ports QFP32 Package, it is recommended not to enable and drive 4 More high-current loads.

Common ports to drive 12mA Software need HDR Register large current driving open port. Port includes a large current drive capability as follows:

HDR port QFP48	QFP32		HDR	Function Description
PD5	PD5	PD5	HDR [0] N / A	
PD6	PD6	PD6	HDR [1] N / A	
PF1	PF1	PD1 PF1	HDR [2] QFP32 Package PD1 Internal equivalent QFP48 of PD1 versus PF1 in parallel	
PF2	PF2	PD2 PF2	HDR [3] QFP32 Package PD2 Internal equivalent QFP48 of PD2 versus PF2 in parallel	
PF4	PF4	PE4 PF4	HDR [4] QFP32 Package PE4 Internal equivalent QFP48 of PF4 versus PE4 in parallel	
PF5	PF5	PE5 PF5	HDR [5] QFP32 Package PE5 Internal equivalent QFP48 of PF5 versus PE5 in parallel	