		Changes in the level set PCIF0 If the PCIE0 And a global interrupt bit set, will have PCI0 Interrupted. When set PCINT1 Bit "0" Time, PB1 Pin change interrupts are disabled.
0		PCINT0 Pin change enable mask 0 . When set PCINT0 Bit "1" Time, PB0 Pin change interrupt is enabled. PB0 Level change on the pin will be set PCIF0 If the PCIE0 And a global interrupt bit set, will have PCI0 Interrupted. When set PCINT0 Bit "0" Time, PB0 Pin change interrupts are disabled.

PCMSK1 - Pin change interrupt mask register 1

PCMSK1 - Pin change mask register 1								
address: 0x6C					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	PCINT15	PCINT14	PCINT13	PCINT12	PCINT11	PCINT10	PCINT9	PCINT8
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W

Bit	Name description
7	PCINT15 Pin change enable mask 15 . When set PCINT15 Bit "1" Time, PC7 Pin change interrupt is enabled. PC7 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT15 Bit "0" Time, PC7 Pin change interrupts are disabled.
6	PCINT14 Pin change enable mask 14 . When set PCINT14 Bit "1" Time, PC6 Pin change interrupt is enabled. PC6 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT14 Bit "0" Time, PC6 Pin change interrupts are disabled.
5	PCINT13 Pin change enable mask 13 . When set PCINT13 Bit "1" Time, PC5 Pin change interrupt is enabled. PC5 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT13 Bit "0" Time, PC5 Pin change interrupts are disabled.
4	PCINT12 Pin change enable mask 12 . When set PCINT12 Bit "1" Time, PC4 Pin change interrupt is enabled. PC4 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT12 Bit "0" Time, PC4 Pin change interrupts are disabled.
3	PCINT11 Pin change enable mask 11 . When set PCINT11 Bit "1" Time, PC3 Pin change interrupt is enabled. PC3 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT11 Bit "0" Time, PC3 Pin change interrupts are disabled.
2	PCINT10 Pin change enable mask 2 . When set PCINT10 Bit "1" Time, PC2 Pin change interrupt is enabled. PC2 Level change on the pin will be set PCIF1 If the PCIE1 And a global interrupt bit set, will have PCI1 Interrupted. When set PCINT10 Bit "0" Time, PC2 Pin change interrupt disabled