

### Interrupts and Interrupt Vector

- 28 Interrupt sources
- Programmable starting address vector

**LGT8F88P / 168P / 328P** The interruption is basically the same resources, the main difference is: **LGT8F88P** The interrupt vector is 1 Instruction Word ( 16 Bit), and **LGT8F168P / 328P** The interrupt vector is 2 Instructions.

#### LGT8F88P Interrupt vector list

LGT8F88P Interrupt vector list:

| Numbering    | Vector address | Interrupt source signals | Interrupt Source Description  |
|--------------|----------------|--------------------------|---|
| 1            | 0x0000         | RESET                    | External reset, power-on reset, a watchdog reset,<br>SWD Debug reset, low voltage reset |
| 2            | 0x0001         | INT0                     | External Interrupt Request 0  |
| 3            | 0x0002         | INT1                     | External Interrupt Request 1  |
| 4            | 0x0003         | PCI0                     | Interrupt pin level 0   |
| 5            | 0x0004         | PCI1                     | Interrupt pin level 1   |
| 6            | 0x0005         | PCI2                     | Interrupt pin level 2   |
| 7            | 0x0006         | WDT                      | Watchdog overflow interrupt   |
| 8            | 0x0007         | TC2 COMPA                | Timer 2 Compare match A Interrupt   |
| 9            | 0x0008         | TC2 COMPB                | Timer 2 Compare match B Interrupt   |
| 10           | 0x0009         | TC2 OVF                  | Timer 2 Overflow  |
| 11           | 0x000A         | TC1 CAPT                 | Timer 1 Input Capture interrupt   |
| 12           | 0x000B         | TC1 COMPA                | Timer 1 Compare match A Interrupt   |
| 13           | 0x000C         | TC1 COMPB                | Timer 1 Compare match B Interrupt   |
| 14           | 0x000D         | TC1 OVF                  | Timer 1 Overflow  |
| 15           | 0x000E         | TC0 COMPA                | Timer 0 Compare match A Interrupt   |
| 16           | 0x000F         | TC0 COMPB                | Timer 0 Compare match B Interrupt   |
| 17           | 0x0010         | TC0 OVF                  | Timer 0 Overflow  |
| 18           | 0x0011         | SPI STC                  | SPI Serial transfer end interrupt   |
| 19           | 0x0012         | USART RXC                | USART RX Complete   |
| 20           | 0x0013         | USART UDRE               | USART Data Register Empty   |
| twenty one   | 0x0014         | USART TXC                | USART End of Transmit Interrupt   |
| twenty two   | 0x0015         | ADC                      | ADC Conversion end interrupt  |
| twenty three | 0x0016         | EE_RDY                   | EEPROM Ready interrupt  |
| twenty four  | 0x0017         | ANA_COMP                 | Analog comparator 0 Interrupt   |
| 25           | 0x0018         | TWI                      | Two-wire serial interface interrupt   |
| 26           | 0x0019         | ANA_COMP1                | Analog comparator 1 Interrupt   |
| 27           | 0x001A         | -                        | Retention   |
| 28           | 0x001B         | PCI3                     | Interrupt pin level 3   |
| 29           | 0x001C         | PCI4                     | Interrupt pin level 4   |
| 30           | 0x001D         | TC3_INT                  | Timer 3 Interrupt   |