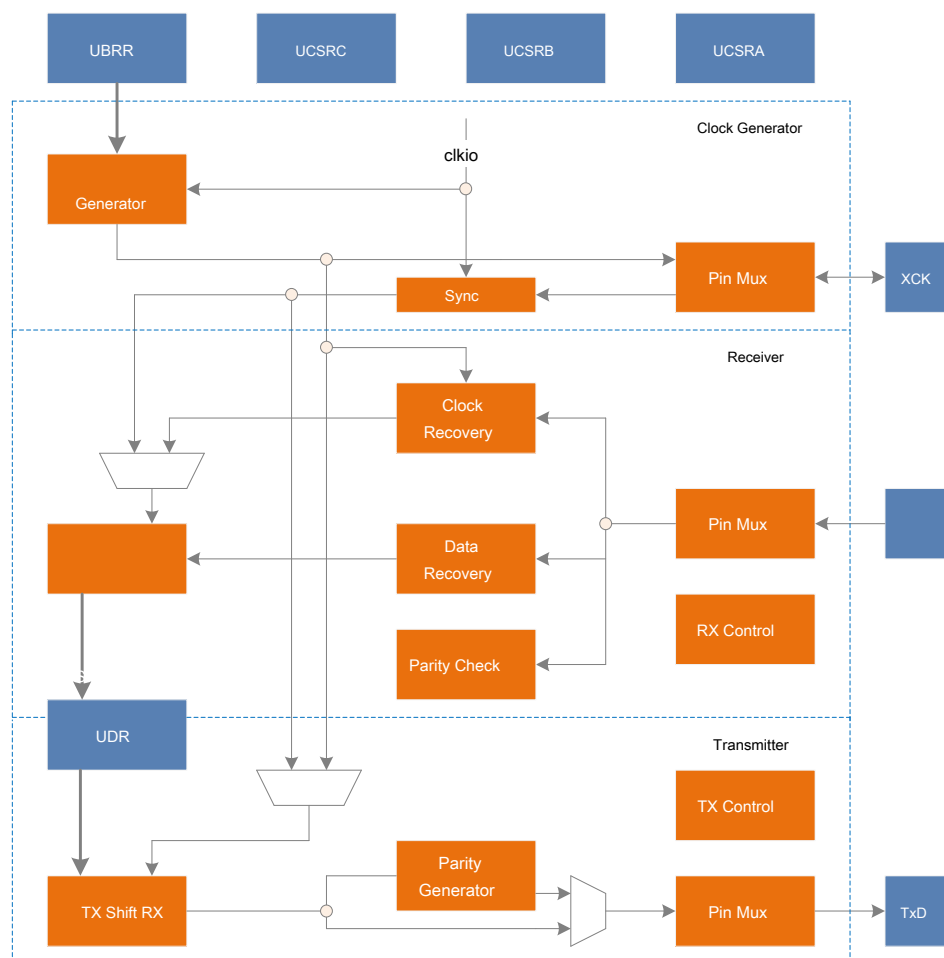


USART0 - Universal Synchronous / Asynchronous Serial Transceiver

- Full-duplex operation (Separate receive and transmit serial register)
- Asynchronous or synchronous operation
- Master or slave operation
- Precision baud rate generator
- **stand by 5 , 6 , 7 , 8 ,or 9 Data bits and 1 ,or 2 Stop bit**
- Supported hardware parity generation and checking mechanisms
- Data overspeed detection
- Framing error detection
- Noise filtering, including false start bit detection and a digital low-pass filter
- Three separate interrupt: transmit complete interrupt, transmitting and receiving data Register Empty Interrupt End
- Multiprocessor Communication Mode
- Speed asynchronous communication mode

Overview

USART Structure chart