

2: 0 ADPS [2: 0] ADC	Prescaler select bit.	
	ADPS Selection system clock generation ADC Clock prescale factor.	
	ADPS [2: 0]	Prescale factor
	0	2
	1	2
	2	4
	3	8
	4	16
	5	32 (default)
	6	64
	7	128

ADCSRB - ADC Control and status registers B

ADCSRB - ADC Control and status registers B									
address: 0x7B						Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0	
Name	ACME01	ACME00	ACME1	1	ACME10	ACTS	ADTS2	ADTS1	ADTS0
R / W	R / W	R / W	R / W	R / W	R / WW / O	R / W	R / W	R / W	
Initial	0	0	0	0	0	0	0	0	
Bit	Name	description							
7	ACME01	Comparators 0 Negative input selection							
6	ACME00	00 : The negative terminal of the external input select ACIN0 01 : Select the negative terminal ADC Multiplexed output 1X : Select the negative terminal of the operational amplifier 0 Output							
5	ACME11	Comparators 1 Negative input selection							
4	ACME10	00 : The negative terminal of the external input select ACIN2 01 : Select the negative terminal ADC Multiplexed output 1X: Select the negative terminal of the operational amplifier 1 Output							
3	ACTS	AC Trigger source channel selection 0 - AC0 Output as ADC The automatic conversion trigger 1 - AC1 Output as ADC The automatic conversion trigger							
2: 0	ADTS [2: 0]	ADC Automatically trigger source select bit. When set ADATE Bit "1" When triggered automatically select function is enabled by a trigger source ADTS To control. When set ADATE Bit "0" Time, ADTS The setting is invalid. The rising edge of the selected trigger signal interrupt flag open a conversion. When the flag is cleared the interrupt trigger a switch to the interrupt flag is set, the source will trigger a rising edge of the trigger signal is generated, if at this time ADEN Position, ADC It will open a conversion. When the switching to the continuous conversion mode (ADTS = 0 Time), triggering an automatic function is disabled.							
	ADTS [2: 0]	Trigger source							
	0	Continuous conversion mode							
	1	Comparators 0/1							