## Pin Description

| • | Descripcion   |    |   |
|---|---|----|---|
| QFP48                                   |   |    |   |
| 25                                      | PB5/SPCK/AC1P PB5: I/O Pin B5 SPCK: SPI Clock AC1P: Analog Comparator 1 Noninverting Input                    | 37 | PC2/ADC2/APN0 PC2: I/O Pin C2 ADC2: ADC Input Channel 2 APN0: Differential Amplifier Ch-0 Inverting Input |
| 26<br>27                                | PE7/ADC11 PE7: I/O Pin E7 ADC11: ADC Input Channel 11  AVCC: Analog Circuit Positive Power Supply             | 38 | PC3/ADC3/APN1 PC3: I/O Pin C3 ADC3: ADC Input Channel 3 APN1: Differential Amplifier Ch-1 Inverting Input |
| 28                                      | PEO/SWC/APN4 PEO: I/O Pin EO SWC: SWD Debug Interface Clock APN4: Differential Amplifier Ch-4 Inverting Input | 39 | PC4/ADC4/SDA PC4: I/O Pin C4 ADC4: ADC Input Channel 4 SDA: Serial - I2C Data                             |
| 29                                      | PE1/ADC6/ACXP  PE1: I/O Pin E1  ADC6: ADC Input Channel 6  ACXP: Analog Comparator 0/1 Noninverting Input     | 40 | PC5/ADC5/SCL PC5: I/O Pin C5 ADC5: ADC Input Channel 5 SCL: Serial - I2C Clock                            |
| 30                                      | PE6/ADC10/AVREF PE6: I/O Pin E6 ADC10: ADC Input Channel 10 AVREF: ADC External Reference Voltage Input       | 41 | PC6/RESETN PC6: I/O Pin C6 RESETN: External Reset Input PC7/ADC8/APN2                                     |
| 31<br>32                                | CVREF: ADC Ref. External Filter Capacitor (0.1uF)  AGND: Internal Analog Circuit Ground  PE2/SWD              | 42 | PC7: I/O Pin C7 ADC8: ADC Input Channel 8 APN2: Differential Amplifier Ch-2 Inverting Input               |
| 33                                      | PE2: I/O Pin E2 SWD: SWD Debug Interface Data PE3/ADC7/AC1N   | 43 | PFO/ADC9/APN3 PFO: I/O Pin FO ADC9: ADC Input Channel 9   |
| 34                                      | PE3: I/O Pin E3  ADC7: ADC Input Channel 7  AC1N: Analog Comparator 1 Inverting Input                         | 44 | APN3: Differential Amplifier Ch-3 Inverting Input PD0/RXD PD0: I/O Pin D0                                 |
| 35                                      | PCO/ADCO/APPO PCO: I/O Pin CO ADCO: ADC Input Channel O APPO: Differential Amplifier Ch-0 Positive Input      | 45 | RXD: Serial USART Receive  PD1/TXD  PD1: I/O Pin D1  TXD: Serial USART Transmit                           |
| 36                                      | PC1/ADC1/APP1 PC1: I/O Pin C1 ADC1: ADC Input Channel 1 APP1: Differential Amplifier Ch-1 Positive Input      | 46 | PF1/0C3A PF1: I/O Pin F1 0C3A: Timer/Counter 3 - Output Compare Match A PD2/INT0/AC00                     |
|   | AFFI. Differential Ampliner Cit-TPositive input   | 47 | PD2: I/O Pin D2 INTO: External Interupt 0 Input ACOO: Analog Comparator 0 Output                          |
|   |   | 48 | PF2/0C3B  PF2: I/O Pin F2  0C3B: Timer/Counter 3 - Output Compare Match B                                 |