	Name	Diez	inal list of	Ditto	Bit4	1016/3	DH9	Bit1	BitO
Addr 0xF6	GUID3	DIL	DILLO	010		Byte 3	DILL	DILL	Ditt
0xF5	GUID2					Byte 2			
0xF4	GUID1					Byte 1			
0xF3	GUID0					Byte 0			
0xF2	PMCR	PMCE	CLKFS	CLKSS	WCLKS	OSCKEN	OSCMEN	RCKEN	RCMEN
0xF0	PMX2	WCE	STOSC1	STOSC0	(*)		XIEN	E6EN	C6EN
0xEE	PMX0	PMXCE	C1BF4	C1AF5	C0BF3	C0AC0	SSB1	TXD6	RXD5
0xED	PMX1		-	-	12	-	C3AC	C2BF7	C2AF6
0xEC	TCKCSR		F2XEN	TC2XF1	TC2XF0	- 17	AFCKS	TC2XS1	TC2XS0
0xE2	PSSR	PSS1	PSS3		-			PSR3	PSR1
0xE1	OCPUE	PUE7	PUE6	PUE5	PUE4	PUE3	PUE2	PUE1	PUE0
0xE0	HDR		-	HDR5	HDR4	HDR3	HDR2	HDR1	HDR0
0xDE	DAPTE	DAPTE			-	-	-	1.0	-
0xDD	DAPTR	DAPTP	C41	C 4 0		Trimming	DNICO	0.001	DDCO
0xDC	DAPCR	DAPEN	GA1	GA0	DNS2	DNS1	DNS0	DPS1	DPS0
0xCF 0xCE	LDOCR VCAL2	WCE	Cal	ibration va	alua for 2	PDEN 048V into	VSEL2	VSEL1	VSEL0
0xCD	VCAL2			ibration va					
0xCC	VCAL3			ibration va					
0xC8	VCAL			ernal Volta					
0xAF	DPS2R		- 1110	-	-	DPS2E	LPRCE	TOS1	TOS0
0xAE	IOCWK	IOCD7	IOCD6	IOCD5	IOCD4	IOCD3	IOCD2	IOCD1	IOCDO
0xAD	ADCSRD	BGEN	REFS2	IVSEL1	IVSEL0	-	VDS2	VDS1	VDS0
0xAC	ADMSC	AMOF		-	-	AMFC3	AMFC2	AMFC1	AMFCC
0xAB	ADT1H		ADC	Auto-moi	nitor Over				
0xAA	ADT1L			Auto-mo					
0xA9	PORTE		Por	t Output E	(for com	patible wil	h LGT8FX	8D)	
0xA8	DDRE			Direction					
0xA7	PINE			rt Input E					
0xA6	ADT0H			Auto-mon			- A district the second state of the second		
0xA5	ADT0L		ADC	Auto-mor				byte	
0xA4	OFR1					offset trim		00	
0xA3	OFR0			ADC		offset trim	ming		
0xA1	DALR				DAC dat	a register			
0xA0	DACON	15		-		DACEN	DAOE	DAVS1	DAVSO
0x9F	OCR3CH		Compare output register high byte of Timer3 C channel						
0x9E	OCR3CL	Compare output register low byte of Timer3 C channel							
0x9D	DTR3H	Dead-band register high byte of Timer3							
0x9C	DTR3L	Dead-band register low byte of Timer3							
0x9B	OCR3BH	Compare output register high byte of Timer3 B channel							
0x9A	OCR3BL	Compare output register low byte of Timer3 B channel							
0x99 0x98	OCR3AH OCR3AL	Compare output register high byte of Timer3 A channel Compare output register low byte of Timer3 A channel							
						gh byte of	Timer3 A	channel	
			Compa	re output i	register lo	gh byte of w byte of	Timer3 A Timer3 A	channel channel	
0x97	ICR3H		Compa	re output i nput captu	register lo ire registe	gh byte of w byte of r high byte	Timer3 A Timer3 A e of Timer	channel channel 3	
0x97 0x96	ICR3H ICR3L		Compa	re output i nput captu nput captu	register lo ire registe ire registe	gh byte of w byte of ir high byte er low byte	Timer3 A Timer3 A e of Timer e of Timer	channel channel 3	
0x97 0x96 0x95	ICR3H ICR3L TCNT3H		Compa	re output i nput captu nput captu Counter	register lo ire registe ire registe register h	gh byte of w byte of er high byte er low byte igh byte o	Timer3 A Timer3 A e of Timer of Timer f Timer3	channel channel 3	
0x97 0x96 0x95 0x94	ICR3H ICR3L TCNT3H TCNT3L		Compa	re output r nput captu nput captu Counter Counter	register lo ire registe ure registe register h register l	gh byte of w byte of r high byte er low byte iigh byte o ow byte o	Timer3 A Timer3 A e of Timer of Timer f Timer3 f Timer3	channel channel 3	
0x97 0x96 0x95 0x94 0x93	ICR3H ICR3L TCNT3H		Compa	re output r nput captu nput captu Counter Counter Con	register lo ire registe ire registe register l register l trol regist	gh byte of w byte of ir high byte or low byte igh byte o ow byte o er D of Tir	Timer3 A Timer3 A e of Timer of Timer3 f Timer3 f Timer3 mer3	channel channel 3	
0x97 0x96 0x95 0x94	ICR3H ICR3L TCNT3H TCNT3L TCCR3D		Compa	re output r nput captu nput captu Counter Counter Con	register lo ire registe ire register register l register l trol regist trol regist	gh byte of w byte of r high byte er low byte iigh byte o ow byte o	Timer3 A Timer3 A e of Timer e of Timer3 f Timer3 mer3 ner3	channel channel 3	
0x97 0x96 0x95 0x94 0x93 0x92	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C		Compa	re output r nput captu nput captu Counter Counter Con Con	register lo ire registe ire register h register h register l trol regist trol regist	gh byte of w byte of r high byte or low byte ligh byte o ow byte o er D of Tir er C of Tir	Timer3 A Timer3 A e of Timer of Timer f Timer3 f Timer3 mer3 mer3 ner3	channel channel 3	
0x97 0x96 0x95 0x94 0x93 0x92 0x91	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B		Compa li	re output r nput captu nput captu Counter Counter Con Con	register lo ure registe ure register h register l trol regist trol regist trol regist trol regist	gh byte of w byte of er high byte er low byte eigh byte o ow byte o er D of Tin er C of Tin er B of Tin	Timer3 A Timer3 A e of Timer of Timer3 f Timer3 f Timer3 ner3 ner3 ner3 ner3	channel channel 3	
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A		Compa li	re output r nput captu nput captu Counter Counter Con Con Con Con Dead-ban	register lo ure registe ure register h register l trol regist trol regist trol regist trol regist d register	gh byte of w byte of er high byte er low byte eigh byte o ow byte o er D of Tin er C of Tin er B of Tin	Timer3 A Timer3 A e of Timer of Timer3 f Timer3 mer3 mer3 mer3 mer3 ner3 ner3 ner3 ner3 ner3	channel channel 3 3	
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x83	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D	DSX17	Compa li	re output r nput captu nput captu Counter Counter Con Con Con Con Dead-ban Dead-ban	register lo ure register register hand register land trol registator land trol registator land trol registator land d register land DAX14	gh byte of w byte of er high byte er low byte igh byte o ow byte o er D of Tin er C of Tin er B of Tin er A of Tin high byte	Timer3 A Timer3 A e of Timer of Timer3 f Timer3 ner3 ner3 ner3 of Timer1 of Timer1	channel channel 3 3	
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x83	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC	OFEN	Compai	re output r nput captu nput captu Counter Counter Con Con Con Con Dead-ban Dead-ban	register lo ure registe ure register h register l trol regist trol regist trol regist trol regist d register d register DAX14 AMEN	gh byte of w byte of er high byte er low byte igh byte o ow byte o er D of Tin er C of Tin er B of Tin er A of Tin high byte	Timer3 A Timer3 A e of Timer of Timer3 f Timer3 mer3 mer3 mer3 mer3 ner3 ner3 ner3 ner3 ner3	channel channel 3 3 3	ADTM
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x83 0x7D	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2		Compai II	re output r nput captu nput captu Counter Counter Con Con Con Con Dead-ban DSX15 SPN	register lo are register register h register l trol regist trol regist trol regist trol register d register d register DAX14 AMEN	gh byte of w byte of ir high byte er low byte igh byte o ow byte o er D of Tir er C of Tir er A of Tir high byte low byte	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 of Timer1 - SPD -	channel channel 3 3	
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x83 0x7D 0x76 0x75	ICR3H ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE	OFEN	Compai	re output r nput captu nput captu Counter Counter Con Con Con Con Dead-ban DSX15 SPN	register lo are register register h register l trol regist trol regist trol regist trol register d register d register DAX14 AMEN	gh byte of w byte of w byte of or high byte of ow byte of ow byte of or D of Tirer C of Timer A of Tinhigh byte of low byte or byte of the control of the co	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 of Timer1 - SPD -	channel channel 3 3 3	ADTM
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x83 0x7D 0x76 0x75	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4	OFEN	Compai	re output r nput captu nput captu Counter Counter Con Con Con Con Dead-ban DSX15 SPN	register lo re registe register h register h register l register l trol regist trol regist trol regist trol regist d register d register DAX14 AMEN - rupt Vector	gh byte of w byte of w byte of ir high byte or low byte igh byte o ow byte o er D of Tir er B of Tir er B of Tir high byte - or Base Ad	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 of Timer1 - SPD -	channel channel 3 3 3	ADTM
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8C 0x7D 0x76 0x75 0x74	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3	OFEN	Compai	re output r nput captu nput captu Counter Counter Com Con Con Dead-ban DEX15 SPN	register lo re registe register h register h register l register l trol regist trol regist trol regist trol regist trol regist d register d register DAX14 AMEN - rupt Vector	gh byte of w byte of re high byte re high byte er low byte igh byte o ow byte o er D of Tir er C of Tir er G of Tir high byte low byte or Base Ad [39:32]	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 ner3 ner3 of Timer1 - SPD - dress	channel channel 3 3 3	ADTM -
0x97 0x96 0x95 0x94 0x93 0x90 0x80 0x80 0x80 0x70 0x75 0x74 0x73 0x71	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3	OFEN	Compai	re output r nput captu nput captu Counter Counter Con Con Con Con Dead-ban DSX15 SPN	register lo re registe re register register h register h register l trol regist trol regist trol regist trol regist trol regist trol regist d register d register DAX14 AMEN - rupt Vector	gh byte of w byte of w byte of re high byte er low byte o ow byte o oer D of Tir er C of Tir er B of Tir er A of Tir high byte low byte or Base Ad [39:32] OCIESC	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 of Timer1 - SPD -	channel channel 3 3 3	ADTM -
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x80 0x80 0x80 0x70 0x75 0x75 0x74 0x73 0x71 0x67	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 RCKCAL	OFEN	Compai	re output raput captu nput captu nput captu Counter Counter Con Con Con Con Dead-ban Dead-ban DSX15 SPN Inten	register lo re registe re register le register le register le register le trol registe trol registe trol registe trol registe trol registe trol registe de register de register DAX14 AMEN - rupt Vecto PCINT - RC32K C	gh byte of w byte of w byte of re high byte er low byte of ow byte o ow byte o oer D of Tir er C of Tir er B of Tir er A of Tir high byte output out	Timer3 A Timer3 A of Timer of Timer of Timer of Timer3 of Timer3 oner3 of Timer3 of Timer1 of Timer1 of Timer1 of Timer1	channel channel 3 3 3 DSX11 DIFS - OCIE3A	ADTM -
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x7D 0x76 0x75 0x74 0x73 0x71 0x67 0x67	ICR3H ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 RCKCAL PRR1	OFEN -	DSX16 PB5D	re output r nput captu nput captu Counter Counter Com Con Con Dead-ban DEX15 SPN	register lo re registe re register register h register h register l trol regist trol regist trol regist trol regist trol regist trol regist d register d register DAX14 AMEN - rupt Vector	gh byte of w byte of w byte of r high byte r low byte igh byte o ow byte o oer D of Tir er C of Tir er B of Tir er A of Tir high byte low byte or Base Ad (39:32] OCIE3C allibration PRTIM3	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 ner3 ner3 of Timer1 - SPD - dress	channel channel 3 3 3 S S S S S S S S S S S S S S S S	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x7D 0x76 0x75 0x74 0x73 0x71 0x67 0x65 0x62	ICR3H ICR3L TCNT3H TCNT3H TCCR3D TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 TIMSK3 RCKCAL PRR1 VDTCR	OFEN	Compai	re output re put captu nput captu nput captu nput captu Counter Counter Com Com Com Com Dead-ban Dead-ban DSX15 SP Inten	register lo re registe register lo re register lo register lo trol regist trol regist trol regist trol regist trol regist trol regist d register d register DAX14 AMEN - rupt Vecto PCINT - RC32K C	gh byte of w byte of w byte of r high byte r low byte igh byte o ow byte o oer D of Tir er C of Tir er A of Tir high byte low byte OCIE3C alibration PRTIM3 VDTS	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress	channel channel 3 3 3 DSX11 DIFS - OCIE3A	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x70 0x76 0x75 0x74 0x73 0x71 0x65 0x62 0x55	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3	OFEN -	DSX16 PB5D	re output re put captu nput captu nput captu nput captu Counter Counter Com Com Com Com Dead-ban Dead-ban DSX15 SP Inten	register lo re registe register h register h register l trol regist trol regist trol regist trol regist trol regist d register d register AMEN - rupt Vecto PCINT - RC32K C - CTL Data	gh byte of w byte of w byte of r high byte r low byte of ow byte of ow byte of or	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress	channel channel 3 3 3 S S S S S S S S S S S S S S S S	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x76 0x75 0x74 0x73 0x71 0x65 0x65 0x5C	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR	OFEN -	DSX16 PB5D	re output re put captu nput captu nput captu captu nput captu Counter Counter Com Com Com Dead-ban DEAL5 SPN - Intern ICIE3	register lo re registe re register lo re register lo register lo trol regist trol regist trol regist trol regist d register d register d register AMEN - rupt Vector PCINT - RC32K C - CTL Data AC1 trim	gh byte of w byte of w byte of r high byte r high byte r low byte igh byte o ow byte o er D of Tir er C of Tir er C of Tir high byte - or Base Ad (39:32] OCIE3C allibration PRTIM3 VDTS register by ming data	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 f Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL	channel channel 3 3 3 S S S S S S S S S S S S S S S S	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x80 0x80 0x80 0x76 0x75 0x74 0x73 0x71 0x67 0x67 0x62 0x5C	ICR3H ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR EZPD3 C1TR EZPD1	OFEN -	DSX16 PB5D	re output in put captu nput captu nput captu nput captu Counter Counter Con	register loure register register register register register register register let trol register let trol register to register let register de register de register DAX14 AMEN - rupt Vector PCINT - RC32K C - CTL Data AC1 trim CTL Data	gh byte of w byte of w byte of re high byte er low byte o ow byte o oer D of Tir er C of Tir er B of Tir er A of Tir high byte out	Timer3 A Timer3 A e of Timer of Timer of Timer3 f Timer3 f Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 SPD - dress OCIE3B PREFL yte 3	channel channel 3 3 3 S S S S S S S S S S S S S S S S	DSX10 ADTM - TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x80 0x70 0x76 0x75 0x74 0x73 0x71 0x67 0x65 0x5B 0x5B 0x5B	ICR3H ICR3L ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK4 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH	OFEN -	DSX16 PB5D	re output re put captu nput captu nput captu nput captu nput captu Counter Counter Con	register lo re registe register lo registe	gh byte of w byte of w byte of re high byte er low byte oigh byte o ow byte o er D of Tir er C of Tir er B of Tir er A of Tir high byte low byte or Base Ad [39:32] OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f Timer3 ner3 ner3 ner3 ner3 ner3 ner3 ner3 n	channel channel 3 3 3 S S S S S S S S S S S S S S S S	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x80 0x80 0x70 0x76 0x75 0x74 0x73 0x71 0x67 0x65 0x62 0x5C 0x5B 0x5A 0x59	ICR3H ICR3L ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL	- WCE	DSX16 - PB5D	re output re put captu nput captu nput captu nput captu nput captu Counter Counter Con	register lo re registe register lo re register lo register lo register lo register lo register rol regist rol regist rol regist rol register rol reg	gh byte of w byte of w byte of r high byte er low byte igh byte o ow byte o er D of Tir er C of Tir er B of Tir er A of Tir high byte low byte or Base Ad (39:32] OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f Timer3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL rte 3 yte1 fupSC uDSC	channel channel 3 3 3 3 3 5 5 5 5 5 5 5 5 5 5 5 5 5 5	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x80 0x70 0x76 0x75 0x74 0x73 0x71 0x67 0x65 0x5B 0x5B 0x5B	ICR3H ICR3L ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK4 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH	OFEN -	DSX16 PB5D	re output riput capturput	register lo re registe register le registe	gh byte of w byte of w byte of r high byte r low byte igh byte o ow byte o oer D of Tir er C of Tir er G of Tir er A of Tir high byte low byte or Base Ad (39:32] OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of CP1	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 ner3 of TimerI - SPD - dress OCIE3B PREFL Ate 3 yte1 f UDSC UDSC CP0	channel channel 3 3 3 S S S S S S S S S S S S S S S S	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x7D 0x76 0x75 0x74 0x73 0x71 0x67 0x65 0x62 0x5C 0x5B 0x5A 0x58 0x58 0x58	ICR3H ICR3L ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 TIMSK3 TIMSK3 TCK3A TIMSK3 TCR1D TCR	- WCE	DSX16 - PB5D	re output riput capturput	register lo re registe register le registe	gh byte of w byte of w byte of r high byte er low byte igh byte o ow byte o er D of Tir er C of Tir er B of Tir er A of Tir high byte low byte or Base Ad (39:32] OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of	Timer3 A Timer3 A e of Timer e of Timer f Timer3 f Timer3 ner3 ner3 ner3 ner3 of TimerI - SPD - dress OCIE3B PREFL Ate 3 yte1 f UDSC UDSC CP0	channel channel 3 3 3 3 3 5 5 5 5 5 5 5 5 5 5 5 5 5 5	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x75 0x74 0x75 0x75 0x65 0x65 0x62 0x5C 0x5B 0x5A 0x58 0x58 0x58	ICR3H ICR3L ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR	- WCE	DSX16 - PB5D - SWR	re output re put captu nput captu nput captu nput captu nput captu Counter Counter Com Con Con Con Con Con Dead-ban DSX15 SPN International PRWDT - E2PU E2PU DSA[3 DSA[3 ERN A COHYSE	register lo re registe register le registe	gh byte of w byte of w byte of r high byte r high byte r high byte of r D of Tir er C of Tir high byte of or Base Ad (39:32] OCIE3C allibration PRTIM VDTS register by ming data register by ess port of CP1 ing register	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL tte 3 yte1 uDSC uDSC CP0 or COFEN	channel channel 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x76 0x75 0x74 0x73 0x71 0x65 0x65 0x5C 0x5B 0x5A 0x5B 0x5A	ICR3H ICR3L ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR	- WCE	DSX16 - PB5D - SWR	re output re put captu nput captu nput captu nput captu nput captu Counter Counter Com Con Con Con Con Con Dead-ban DSX15 SPN International PRWDT - E2PU E2PU DSA[3 DSA[3 ERN A COHYSE	register lo re registe register le registe	gh byte of w byte of w byte of r high byte er low byte oo ow byte o er D of Tir er C of Tir er C of Tir high byte or Base Ad (39:32] OCIE3C allibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing registe COWKE	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL tte 3 yte1 uDSC uDSC CP0 or COFEN	channel channel 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 VDTEN ECS0 COFS0
0x97 0x96 0x95 0x94 0x93 0x92 0x80 0x80 0x70 0x76 0x75 0x74 0x73 0x71 0x65 0x62 0x5C 0x5B 0x5A 0x59 0x58 0x50 0x54 0x54 0x54 0x54 0x54 0x55 0x54 0x55 0x54 0x55 0x54 0x55 0x55 0x56	ICR3H ICR3L ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR COXR	- WCE	DSX16 PB5D SWR	re output riput captur put	register loure register register la register la register la troi registe troi registe troi register la	gh byte of w byte of w byte of r high byte er low byte oo ow byte o er D of Tir er C of Tir er C of Tir er A of Tir high byte or Base Ad [39:32] OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing registr COWKE	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL tte 3 yte1 uDSC uDSC CP0 or COFEN	channel channel 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 TOIE3 VDTEN ECS0 COFS0
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x80 0x80 0x76 0x75 0x74 0x73 0x71 0x67 0x65 0x5B	ICR3H ICR3L ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK4 PCMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR COXR DTR0 TCCR0C	OFEN - WCE WEN - DSX07	DSX16 PB5D SWR EEN COOE	re output riput captur put	register loure register register loure register la register la trol registe trol registe trol register la register	gh byte of w byte of w byte of r high byte r low byte igh byte o ow byte o oer D of Tir er C of Tir er C of Tir er A of Tir high byte low byte or Base Ad (39:32) OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing registe COWKE ning control	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL vte 3 yte1 fupSC uDSC CP0 or COFEN or cofeen or cofeen or register - C1FEN	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 TOIE3 VDTEN ECS0 C0FS0 D5X00 C1FS0
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x80 0x80 0x70 0x76 0x75 0x74 0x73 0x71 0x67 0x65 0x62 0x5C 0x5B 0x5A 0x59 0x58 0x54	ICR3H ICR3L ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR COTR COTR COTR TCCROC C1XR	OFEN - WCE WEN - DSX07	DSX16 - PB5D - SWR - COOE DSX06 C1OE	re output raptu captu raptu ra	register lo re registe register lo re register le regi	gh byte of w byte of w byte of r high byte r low byte igh byte o ow byte o oer D of Tir er C of Tir er C of Tir er A of Tir high byte low byte or Base Ad (39:32) OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing registe COWKE ning control	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL vte 3 yte1 fupSC uDSC CP0 or COFEN or cofeen or cofeen or register - C1FEN	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X000 C1FS0 WRPTR
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x76 0x75 0x74 0x73 0x71 0x65 0x65 0x5C 0x5B 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5A 0x5S 0x5S 0x5A 0x5S 0x5A 0x5S 0x5S 0x5A 0x5S	ICR3H ICR3L ICR3L TCNT3H TCNT3H TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR COXR DTR0 TCCROC C1XR SPFR	OFEN - WCE WEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output in put captu nput nput nput nput nput nput nput n	register loure register regist	gh byte of w byte of w byte of w byte of r high byte er low byte igh byte o ow byte o er D of Tir er C of Tir er C of Tir high byte or Base Ad [39:32] OCIE3C allibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing registr COWKE hing control CTUKKE WRFULL t of Group	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL tte 3 vte 1 uDSC uDSC CP0 or COFEN ol register - C1FEN WREMPT OCF3B F	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X000 C1FS0 WRPTR
0x97 0x96 0x95 0x94 0x93 0x90 0x80 0x80 0x70 0x76 0x75 0x71 0x67 0x65 0x52 0x58 0x58 0x51 0x58 0x58 0x58 0x71 0x67 0x65 0x52 0x52 0x52 0x54 0x53 0x54 0x53 0x54 0x55 0x54 0x56 0x56 0x56 0x56 0x56 0x56 0x56 0x56 0x57 0x56 0x57 0x57 0x57 0x57 0x57 0x57 0x57 0x57 0x57 0x57 0x58 0x58 0x58 0x58 0x58 0x56 0x56 0x56 0x56 0x56 0x56 0x57	ICR3H ICR3L ICR3L ICR3L TCNT3H TCNT3H TCNT3CR3C TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK4 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR C0TR COXR DTR0 TCCR0C C1XR SPFR TIFR3 PORTF DDRF	OFEN - WCE WEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output in put captu nput nput nput nput nput nput nput n	register loure register regist	gh byte of w byte of w byte of r high byte er low byte of er low byte ow byte of er D of Tir er C of Tir er B of Tir er A of Tir high byte low byte or Base Ad (39:32) OCIE3C allibration PRTIM3 VDTS register by ming data register by ess port of COWKE hing control COWKE write t of Group on of Group	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x76 0x75 0x74 0x73 0x71 0x65 0x62 0x5C 0x5B 0x5A 0x58 0x5A 0x58 0x54 0x54 0x53 0x74	ICR3H ICR3L ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR C0TR C0XR DTR0 TCCR0C C1XR SPFR TIFR3 PORTF	OFEN - WCE WEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output re put captu counter Counter Counter Con	register loure register regist	gh byte of w byte of w byte of r high byte er low byte igh byte o ow byte o er D of Tir er C of Tir er C of Tir er A of Tir high byte low byte or Base Ad (39:32) OCIE3C allibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing register by ess port of CP1 COWKE ing contro C1WKE WRFULL t of Group on of Group of Group	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f Timer3 f Timer3 f Timer3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL Vte 3 Vte1 f UDSC CP0 or COFEN of register - C1FEN WREMPT OCF3B F p F	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x76 0x75 0x74 0x75 0x67 0x65 0x62 0x5B 0x5C 0x5B 0x5C 0x5B 0x5A 0x59 0x58 0x56 0x52 0x51 0x44 0x33 0x31	ICR3H ICR3L ICR3L ICR3L TCNT3H TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR COXR DTR0 TCCROC C1XR SPFR TIFR3 PORTF DDRF PINF DSDY	OFEN - WCE WEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output raptu captu counter Counter Counter Con	register lo re registe register lo re register le regi	gh byte of w byte of w byte of r high byte er low byte igh byte o ow byte o oer D of Tir er C of Tir er C of Tir er A of Tir high byte low byte or or Base Ad (39:32) OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing register COWKE ming control	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f Timer1 of Timer1 of Timer1 of Timer1 clubsc crack c	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x75 0x74 0x75 0x67 0x65 0x62 0x5C 0x5B 0x5A 0x5B 0x5B 0x5A 0x5B 0x5B 0x5B 0x5B 0x5B 0x5B 0x5B 0x5B	ICR3H ICR3L ICR3L ICR3L TCNT3H TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 TRKSCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR COXR DTR0 TCCROC C1XR SPFR TIFR3 PORTF DDRF PINF DSDY DSDX	VEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output in put captur put	register lo re registe register lo re register le regi	gh byte of w byte of w byte of r high byte er low byte igh byte o ow byte o oer D of Tir er C of Tir er C of Tir er B of Tir high byte low byte or or Base Ad (39:32] OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing registr COWKE ning control crum crum crum crum crum crum crum crum	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL vte 3 vte 1 uDSC uDSC uDSC uDSC uDSC uDSC uDSC uDSC	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR TOV3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x75 0x74 0x75 0x75 0x65 0x65 0x65 0x5C 0x5B 0x5A 0x59 0x58 0x5A 0x59 0x58 0x54 0x52 0x51 0x4F 0x49 0x38 0x34 0x33 0x32 0x31 0x30 0x2F	ICR3H ICR3L ICR3L ICR3L TCNT3H TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR C0TR C0XR DTR0 TCCR0C C1XR SPFR TIFR3 PORTF DDRF PINF DSDY DSDX C1SR	OFEN - WCE WEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output in put captu nput nput nput nput nput nput nput n	register lo re registe register register le register l	gh byte of w byte of w byte of r high byte er low byte igh byte oo ow byte of er D of Tir er C of Tir er C of Tir high byte low byte or Base Ad (39:32] OCIE3C allibration PRTIM3 VDTS register by ming data register by ming contro CP1 ing registe COWKE ing contro control t of Group on of Group on of Group on of Group on of Group of Group t of Ut t port of ut t of LIE	Timer3 A Timer3 A Timer3 A e of Timer of Timer of Timer3 f Timer3 f Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL Ate 3 yte1 uDSC uDSC CP0 er COFEN ol register - C1FEN WREMPT OCF3B F p F = SSC DSC C1IC	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X000 C1FS0 WRPTR
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x83 0x7D 0x76 0x75 0x74 0x73 0x71 0x65 0x62 0x5C 0x5B 0x5A 0x59 0x5A 0x59 0x58 0x54 0x52 0x51 0x4F 0x49 0x3A 0x33 0x32 0x31 0x32 0x31 0x32	ICR3H ICR3L ICR3L ICR3L TCNT3H TCNT3L TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK3 TIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR C0TR C0XR DTR0 TCCR0C C1XR SPFR TIFR3 PORTF DDRF PINF DSDY DSDX C1SR PORTE	VEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output in put captu nput captu nput captu nput captu nput captu nput captu nput captu Counter Counter Con Com	register loure register regist	gh byte of w byte of w byte of w byte of r high byte er low byte igh byte o ow byte o er D of Tir er C of Tir er C of Tir high byte or Base Ad (39:32] OCIE3C alibration PRTIM3 VDTS register by ming data register by ess port of CP1 ing registr toWKE WRFULL tof Group of Group of Group of Group of Group of Group port of ut colle tof Group colle tof G	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer of Timer3 ner3 ner3 ner3 ner3 of Timer1 of Timer1 - SPD - dress OCIE3B PREFL tte 3 vte1 uDSC uDSC CPO er COFEN ol register - CIFEN ol register - CIFEN ocra SPD ocra COFEN oc	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR TOV3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x80 0x85 0x7D 0x76 0x75 0x74 0x73 0x67 0x65 0x52 0x58 0x58 0x58 0x58 0x58 0x58 0x58 0x58	ICR3H ICR3L ICR3L ICR3L ICR3L TCNT3H TCNT3H TCNT3C TCCR3D TCCR3C TCCR3B TCCR3A DTR1H DTR1L TCCR1D ADCSRC DIDR2 IVBASE PCMSK4 PCMSK4 PCMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD1 DSAH DSAL ECCR C0TR COTR COTR COTR TCCR0C C1XR SPFR TIFR3 PORTF DDRF PINF DSDY DSDX C1SR PORTE DDRE	VEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output in put captur put	register loss register loss register la register la register la trol register la trol register la trol register la	gh byte of w byte of w byte of w byte of er high byte of er high byte of er high byte of ow byte of ow byte of er D of Tirrer C of Tirrer B of Tirrer	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR TOV3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x86 0x76 0x76 0x75 0x74 0x73 0x67 0x65 0x62 0x5C 0x5B 0x5A 0x59 0x58 0x56 0x52 0x51 0x4F 0x49 0x3A 0x39 0x38 0x34 0x30 0x2E 0x2D 0x2C	ICR3H ICR3L ICR3L ICR3L ICR3L ICR3L ICR3H ICR3H ICR3H ICR3H ICCR3C ICCR3B ICCR3C ICCR3B ICCR3C ICR3B ICCR3C ICR3B ICCR3C ICR3C ICR3C ICR3C ICR3C ICR3C IVBASE PCMSK4 PCMSK	VEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output in put captu nput captu nput captu nput captu nput captu nput captu nput captu Counter Counter Con	register loar register loar register loar register la register la register la trol registe trol register la regist	gh byte of w byte of w byte of w byte of or high byte of or high byte of own byte of own byte of own byte of or Both of Timer B of T	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR TOV3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x75 0x74 0x75 0x75 0x67 0x65 0x62 0x5C 0x5B 0x5C 0x5B 0x5C 0x5B 0x5A 0x59 0x58 0x56 0x52 0x51 0x4F 0x49 0x3A 0x39 0x38 0x34 0x30 0x2F 0x2E 0x2C 0x2C	ICR3H ICR3L ICR3L ICR3L ICR3L ICR3L ICR3H ICR3H ICR3H ICR3H ICCR3C IVBASE PCMSK4 PCMSK3 IIMSK3 RCKCAL PRR1 VDTCR E2PD3 C1TR E2PD3 C1TR E2PD1 DSAH DSAL ECCR COTR COXR DTR0 ICCROC C1XR SPFR IIFR3 PORTF PINF DSDY DSDX C1SR PORTE DDRF PINF DSDY DSDX C1SR PORTE DDRE PINE DSSD	VEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output re put captu counter Counter Counter Con	register loar register registe	gh byte of w byte of w byte of w byte of or high byte of or high byte of own byte of one of Tirer C of Tirer B	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X00 C1FS0 WRPTR TOV3
0x97 0x96 0x95 0x94 0x93 0x92 0x91 0x90 0x8D 0x8C 0x76 0x75 0x74 0x73 0x67 0x65 0x62 0x5C 0x5B 0x5A 0x59 0x5A 0x59 0x38 0x30 0x4F 0x49 0x3A 0x39 0x3A 0x30 0x2E 0x2C	ICR3H ICR3L ICR3L ICR3L ICR3L ICR3L ICR3H ICR3H ICR3H ICR3H ICCR3C ICCR3B ICCR3C ICCR3B ICCR3C ICR3B ICCR3C ICR3B ICCR3C ICR3C ICR3C ICR3C ICR3C ICR3C IVBASE PCMSK4 PCMSK	VEN - DSX07 - RDFULL	DSX16 - PB5D - SWR - COOE DSX06 C1OE RDEMPT	re output re put captu counter Counter Counter Con	register loar register registe	gh byte of w byte of w byte of w byte of or high byte of or high byte of own byte of own byte of own byte of or Both of Timer B of T	Timer3 A Timer3 A Timer3 A of Timer of Timer of Timer3 f	channel channel 33 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	TOIE3 - VDTEN ECS0 C0FS0 D5X000 C1FS0 WRPTR TOV3

8-bit LGT8XM

RISC Microcontroller with In-System Programmable FLASH Memory

LGT8F88P LGT8F168P LGT8F328P

Data book Version 1.0.4

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