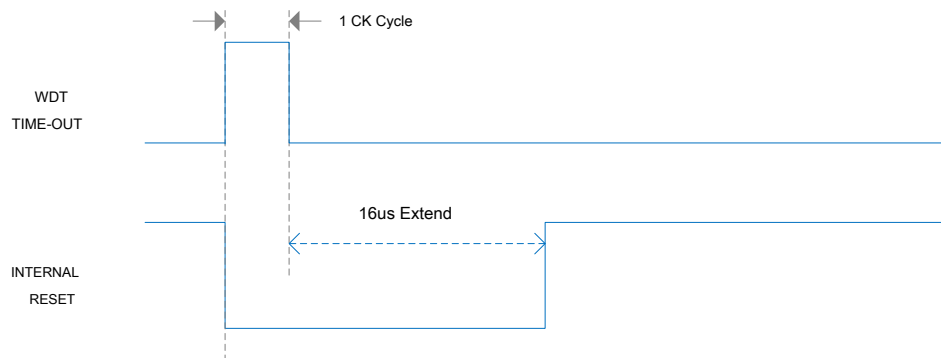


Watchdog reset

When the watchdog timer overflows, if the Watchdog system reset function, the system will generate a reset signal period immediately. General watchdog reset signal will be inside the wide delay counter exhibition. The detailed operation of the watchdog controller, see Details of the test section below.



Software reset, OCD Reset

Software reset is by operation of the user VDTCR Sixth register trigger, a software reset and watchdog reset timing is completely similar. The internal reset signal broadening 16μs .

OCD Reset unit generated by the internal chip debugger, OCD Reset is normally controlled by the debugger, the user can not trigger software OCD Reset.