		0_1010	PF0	
		0_1011	PE6	
		0_1100	PE7	
		0_1110	4 / 5VDO	Internal voltage circuit
		0_1000	1 / 5VDO	
		0_1101	IVREF	Internal Reference
		0_1111	AGND	Analog ground
		1_XXXX	DACO	internal DAC Export

ADCSRC - ADC Control Status Register C

ADCSRC - ADC Control Status Register C								
address: 0x7D				Defaults: 0x00				
Bit	7	6	5	4	3	2	1	0
Name	OFEN	- SPN		AMEN	-	SPD	DIFS	ADTM
R / W	R / W	- R / W		R / W	- R / W		R / W	R / W

Bit	Name	description
7	OFEN	1 = Enable offset compensation; 0 = Close offset compensation
6	-	Unimplemented
5	SPN	ADC Conversion polarity control input, only the offset calibration process. Normal must be cleared
4	AMEN	Enabling automatic monitoring channel; 1 : Channel Enable automatic supervising function 0 : Disable automatic channel monitoring
3	-	Unimplemented
2	SPD	0 = ADC Low conversion mode 1 = ADC High-speed conversion mode, only a low-impedance analog inputs
1	DIFS	0 = ADC Conversion from ADC Multiplexer 1 = ADC Internal conversion from the differential amplifier
0	ADTM	Test mode, from AVREF Internal reference voltage output port

DIDR0 - Digital Input Disable Control Register 0

DIDR0 - Digital Input Disable Control Register 0								
address: 0x7E						Defaults: 0x00		
Bit	7	6	5	4	3	2	1	0
Name	PE3D	PE1D	PC5D	PC4D	PC3D	PC2D	PC1D	PC0D
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W

Bit	Name	description
7	PE3D	1 = shut down PE3 Digital Input Function
6	PE1D	1 = shut down PE1 Digital Input Function
5	PC5D	1 = shut down PC5 Digital Input Function
4	PC4D	1 = shut down PC4 Digital Input Function