

OC1x Signal is output to the corresponding pin. It should be noted, occurs automatically shut down after the timer did not stop working,

OC1x State of the signal has also been updated. After the software or compare match timer overflows, then set COM1x Bit output OC1x Signal, so you can get a clear PWM Output state.

### Dead-time control

Set up DTEN1 Bit "1" When inserting the dead time function is enabled, OC1A with OC1B The output waveform will B

Deadtime comparator output channel waveform based on the generated set of insertion, the length of time of DTR1 Register count clock number

corresponding to the time value. As shown below, OC1A with OC1B Deadtime insertion are based channel B Comparing the output waveform as a reference.

when COM1A with COM1B The same "2" or "3" Time, OC1A The polarity of the waveform OC1B The waveform of the same polarity, when COM1A with COM1B

Respectively "2" or "3" Time, OC1A The waveform OC1B Opposite polarity waveform.

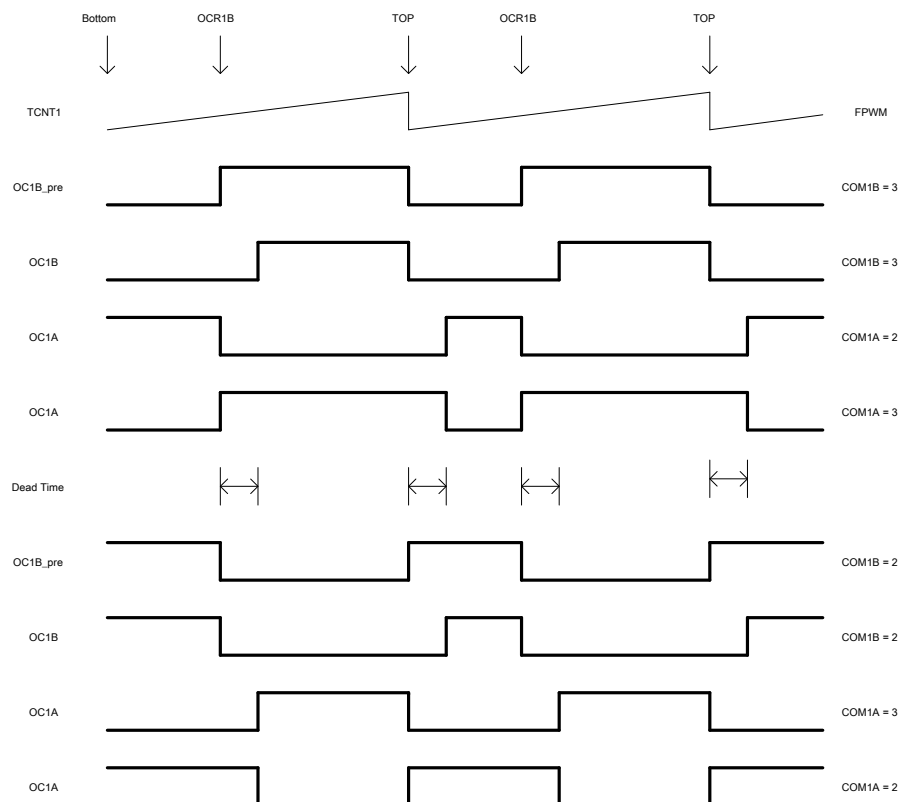


Figure 3 FPWM Mode TC1 Dead-time control