

In Free Running mode, a new conversion will be started immediately after the conversion completes, while ADSC remains high. For a summary of conversion times, see [Table 21-1 on page 255](#).

Figure 21-4. ADC Timing Diagram, First Conversion (Single Conversion Mode)

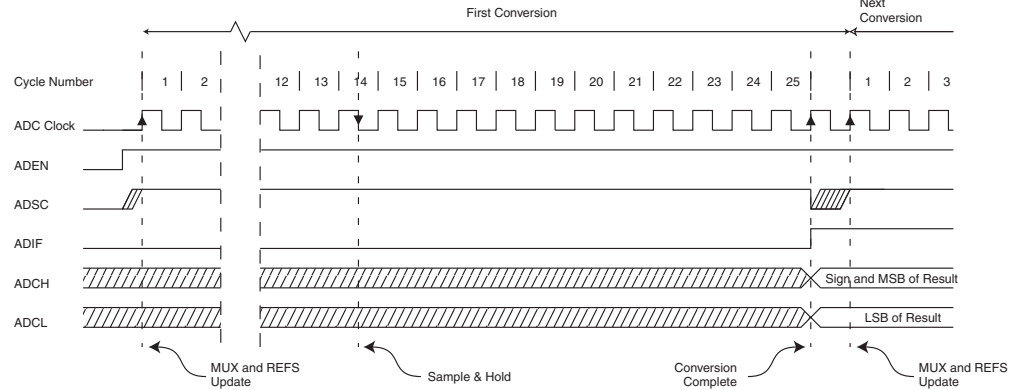


Figure 21-5. ADC Timing Diagram, Single Conversion

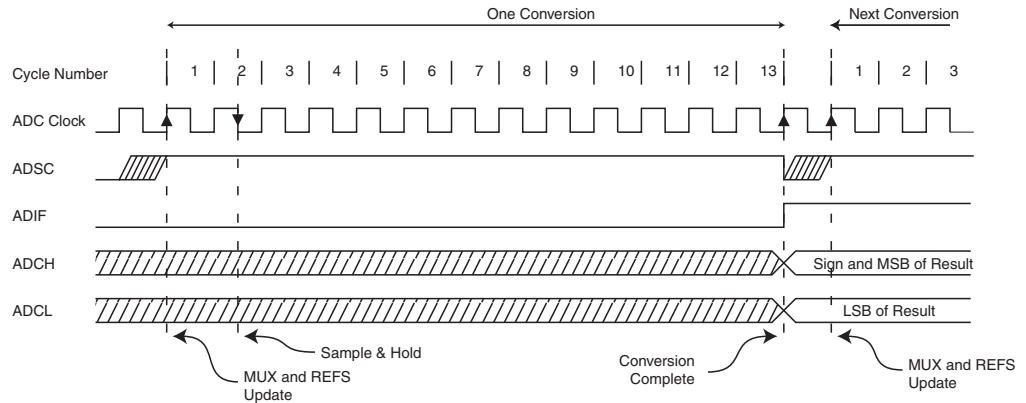


Figure 21-6. ADC Timing Diagram, Auto Triggered Conversion

