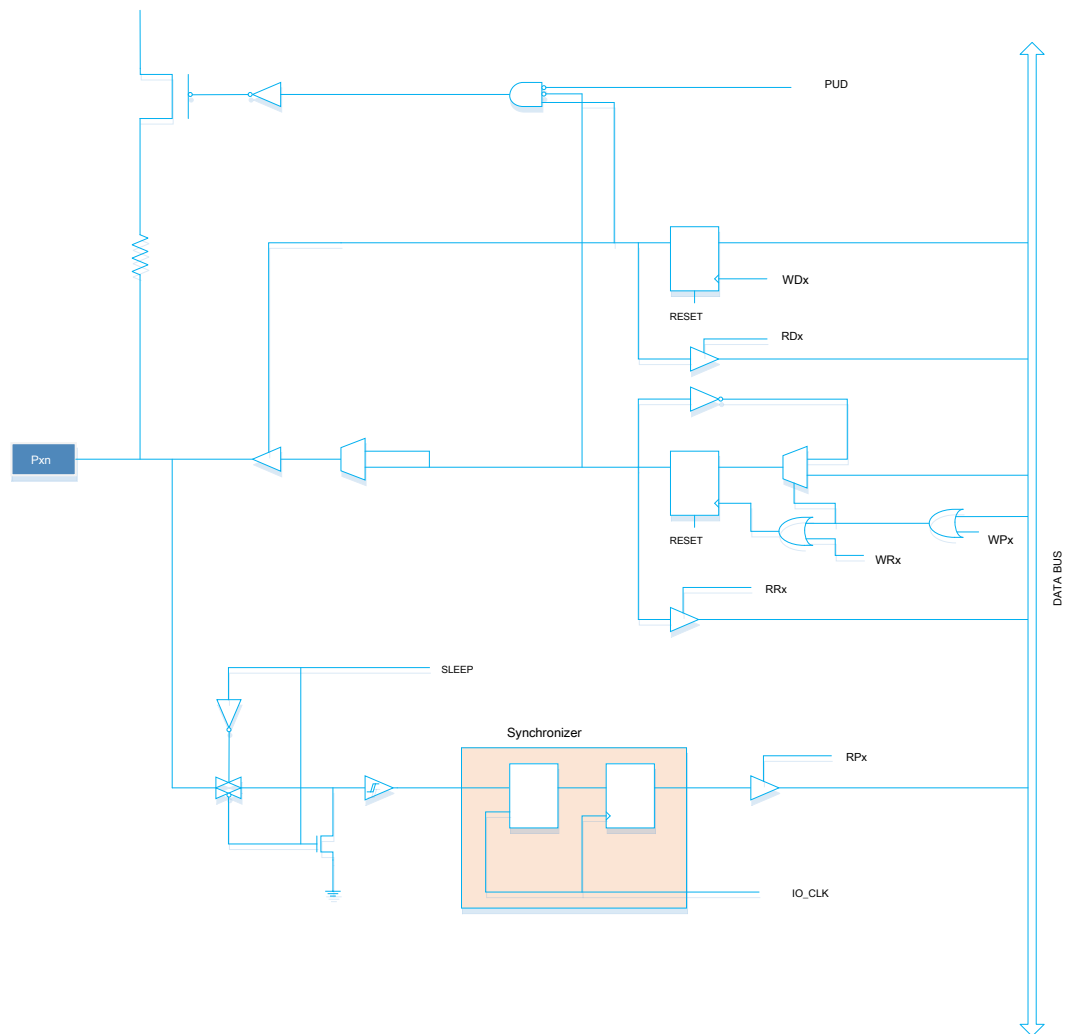


General purpose input / output port

As a general I / O When the port is bidirectional drive I / O Port, internal programmable pull. The figure below shows common I / O Equivalent circuit diagram of the port:



PUD: PULLUP DISABLE

SLEEP: SLEEP CONTROL

IO_CLK: I / O CLOCK

WDx: WRITE DDRx

RDx: READ DDRx

WRx: WRITE PORTx

RRx: READ PORTx REGISTER

RPx: READ PORTx PIN

WPx: WRITE PINx REGISTER

Using the configuration port

Each port consists of three control register bits: DDxn , PORTxn with PINxn . among them DDxn Used by DDRx

Register access, PORTxn able to pass PORTx Register access, PINxn able to pass PINx Register access.

DDRxn Register bit is used to set the input port / output direction. in case DDxn Set as 1 , Pxn It is configured as a port to an output port. in case DDxn Set as 0 , Pxn It is configured as an input port.