The following code describes how to change the value of the watchdog timer timeout.

Assembly code

```
WDT_TOV_Change:
    ; Turn off global interrupt
    CLI
    ; Reset watchdog timer
    WDR
    ; Start timed sequence
    LDS r16, WDTCSR
    ORI r16, (1 << WDCE) | (1 << WDE)
    STS WDTCSR, r16
    ; - Got for cycles to set the new value from here -; Set new
    time-out value = 64k cycles
    LDI r16, (1 << WDE) | (1 << WDP2) | (1 << WDP0)
    STS WDTCSR, r16
    ; - Finished setting new value, used 2 cycles; Turn on
    global interrupt
    SEI
    RET
```

C Language code

```
void WDT_TOV_Change (void) {

    __disable_interrupt ();
    __watchdog_reset ();
    / * Start timed sequence * /
    WDTCSR | = (1 << WDCE) | (1 << WDE);
    / * Set new time-out value = 64K cycles * /
    WDTCSR | = (1 << WDE) | (1 << WDP2) | (1 << WDP0);
  __enable_interrupt ();}
```

【Instructions for use】

Change WDP Before configuration bits, it is recommended to reset the watchdog timer. Because changes WDP Bit to relatively small time-out period is likely to cause the watchdog timeout reset.