The following table fast PWM Mode mode control comparator output waveform of the output comparator.

COM0x [1: 0]	description
0	OC0x Disconnect, GM IO Port operations
1	Retention
2	Clear compare match OC0x Signal is set to match the maximum value OC0x signal
3	When set compare match OC0x Signal is cleared when the maximum matching OC0x signal

The following table shows the comparison output of the phase correction mode the mode control output of the comparator waveform.

COM0x [1: 0]	description
0	OC0x Disconnect, GM IO Port operations
1	Retention
2	Cleared when the match count comparator ascending OC0x Down signal, compare match count descending When set OC0x signal
3	Ascending count comparator match the set OC0x Down signal, compare match count descending When cleared OC0x signal

The following table is a waveform generation mode control.

WGM0 [2: 0] Operating mode	TOP Value	update OCR0X Time	set TOV0 time	
0	Normal	0xFF	immediately	MAX
1	PCPWM	0xFF	TOP	BOTTOM
2	CTC	OCR0A	immediately	MAX
3	FPWM	0xFF	TOP	MAX
4	Retention	-	-	-
5	PCPWM	OCR0A	TOP	BOTTOM
6	Retention	-	-	-
7	FPWM	OCR0A	TOP	TOP

The following table is a dead time enabled OC0A Polarity control signal output waveform.

Dead time enabled mode OC0A Polarity control signal output waveform

DTEN0	COM0A [1: 0]	COM0B [1: 0]	description
0	-	-	OC0A Signal polarity by the OC0A Compare output mode control
1	0	-	OC0A Disconnect, GM IO Port operations
1	1	-	Retention
1	2	2	OC0A Signals OC0B Signals with the same polarity
		3	OC0A Signals OC0B Opposite signal polarity
1	3	2	OC0A Signals OC0B Opposite signal polarity
		3	OC0A Signals OC0B Signals with the same polarity

【note】:

OC0B The polarity of the signal output from the waveform OC0B Compare output control mode, so that the same can not dead time mode.