

15	8-bit Timer/Counter2 with PWM and Asynchronous Operation	144
15.1	Features	144
15.2	Overview	144
15.3	Timer/Counter Clock Sources	145
15.4	Counter Unit	145
15.5	Output Compare Unit	146
15.6	Compare Match Output Unit	148
15.7	Modes of Operation	149
15.8	Timer/Counter Timing Diagrams	153
15.9	Asynchronous Operation of Timer/Counter2	155
15.10	Timer/Counter Prescaler	156
15.11	Register Description	158
16	SPI – Serial Peripheral Interface	166
16.1	Features	166
16.2	Overview	166
16.3	\overline{SS} Pin Functionality	171
16.4	Data Modes	171
16.5	Register Description	173
17	USART0	176
17.1	Features	176
17.2	Overview	176
17.3	Clock Generation	177
17.4	Frame Formats	180
17.5	USART Initialization	182
17.6	Data Transmission – The USART Transmitter	183
17.7	Data Reception – The USART Receiver	186
17.8	Asynchronous Data Reception	190
17.9	Multi-processor Communication Mode	193
17.10	Register Description	195
17.11	Examples of Baud Rate Setting	199
18	USART in SPI Mode	204
18.1	Features	204
18.2	Overview	204
18.3	Clock Generation	204
18.4	SPI Data Modes and Timing	205