

QFP48	QFP32	SSOP20	
12	08	07	<b>PB7/XTALI</b> PB7: I/O Pin B7 XTALI: Crystal Oscillator Input
13	09	08	<b>PD5/RXD*/T1/OC0B</b> PD5: I/O Pin D5 RXD: USART Receive Data T1: Timer 1 External Clock Input OC0B: Timer/Counter 0 Output Compare Match B
14	10	09	<b>PD6/TXD*/OC0A</b> PD6: I/O Pin D6 TXD: USART Transmit Data OC0A: Timer/Counter 0 Output Compare Match A
15			<b>AC0P/OC3A</b> AC0P: Analog Comparator 0 Positive Input OC3A: Timer/Counter 3 Output Compare Match A
16	11	10	<b>PD7/ACXN</b> PD7: I/O Pin D7 ACXN: Analog Comparator 0/1 Inverting Input
17	-		<b>PF7/OC2B</b> PF7: I/O Pin F7 OC2B: Timer/Counter 2 Output Compare Match B
18	12	11	<b>PB0/ICP1</b> PB0: I/O Pin B0 ICP1: Timer 1 Capture Input
19	13		<b>PB1/OC1A</b> PB1: I/O Pin B1 OC1A: Timer/Counter 1 Output Compare Match A
20	14	12	<b>PB2/OC1B/SPSS</b> PB2: I/O Pin B2 OC1B: Timer/Counter 1 Output Compare Match B SPSS: SPI Slave Select
21	-	-	<b>GND</b>
22	-	-	<b>VCC</b>
23	15	12	<b>PB3/MOSI/OC2A</b> PB3: I/O Pin B3 MOSI: SPI Master Output Slave Input OC2A: Timer/Counter 2 Output Compare Match A
24	16	13	<b>PB4/MISO</b> PB4: I/O Pin B4 MISO: SPI Master Input Slave Output
25	17	14	<b>PB5/SPCK/AC1P</b> PB5: I/O Pin B5 SPCK: SPI Clock AC1P: Analog Comparator 1 Noninverting Input
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