[7: 0]	PORTD	D Port output register group

port D Direction Register - DDRD

DDRD - port D Direction Register								
DDRD: 0x0A (0x2A)				Defaults: 0x00				
DDRD	DDD7	DDD6	DDD5	DDD4	DDD3	DDD2	DDD1	DDD0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	DDD	D Output port direction control register group						

port D Input Data Register - PIND

PIND - port D Input data register								
PIND: 0x09 (0x29)				Defaults: 0x00				
PIND	PIND7	PIND6	PIND5	PIND4	PIND3	PIND2	PIND1	PIND0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	PIND	D Group port status register read PIND Get the current port-level state to write PINDn for 1, Overturn PORTDn State of the corresponding bit						

port E Output Data Register - PORTE

PORTE - port E Output data register								
PORTE: 0x0E (0x2E)				Defaults: 0x00				
Bits	PE7	PE6	PE5	PE4	PE3	PE2	PE1	PE0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	PORTE	E Port output register group						

port E Direction Register - DDRE

DDRE - port E Direction Register								
DDRE: 0x0D (0x2D)				Defaults: 0x00				
DDRE	DDE7	DDE6	DDE5	DDE4	DDE3	DDE2	DDE1	DDE0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
Bit Definitions								
[7: 0]	DDE	E Port Direction control register group						

port E Input Data Register - PINE

PINE - port E Input data register								
PINE: 0x0C (0x2C)				Defaults: 0x00				
PINE	PINE7	PINE6	PINE5	PINE4	PINE3	PINE2	PINE1	PINE0