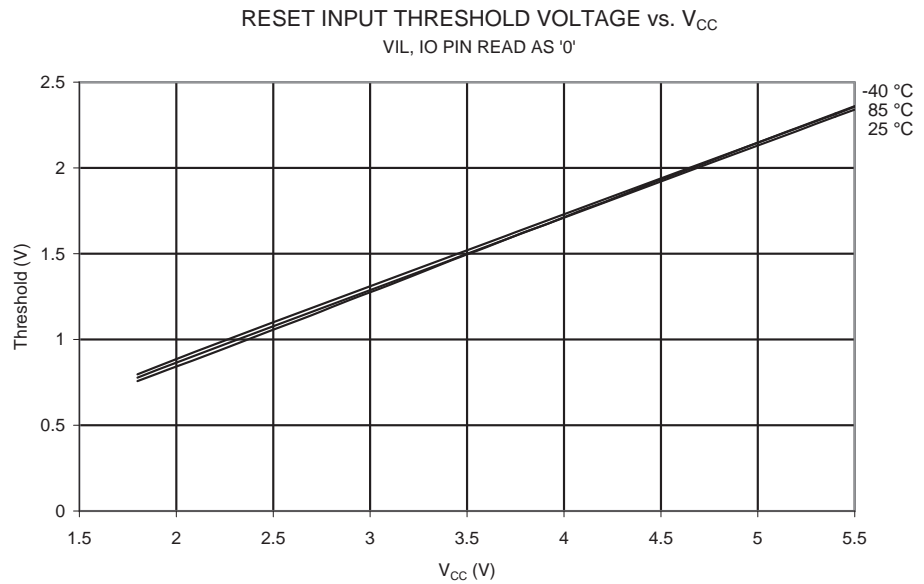


**Figure 27-121.** Reset Input Threshold Voltage vs.  $V_{CC}$  ( $V_{IL}$ , I/O Pin read as '0').



**Figure 27-122.** Reset Pin Input Hysteresis vs.  $V_{CC}$ .

