QFP32			
25	PC2/ADC2/APN0	29	PC6/RESETN
	PC2: I/O Pin C2		PC6: I/O Pin C6
	ADC2: ADC Input Channel 2		RESETN: External Reset Input
	APNO: Differential Amplifier Ch-0 Inverting Input	30	PD0/RXD
26	PC3/ADC3/APN1		PD0: I/O Pin D0
	PC3: I/O Pin C3		RXD: Serial USART Receive
	ADC3: ADC Input Channel 3	31	PD1/TXD/OC3A
	APN1: Differential Amplifier Ch-1 Inverting Input		PD1: I/O Pin D1
27	PC4/ADC4/SDA		TXD: Serial USART Transmit
	PC4: I/O Pin C4		OC3A: Timer/Counter 3 - Output Compare Match A
	ADC4: ADC Input Channel 4	32	PD2/INT0/OC3B
	SDA: Serial - I2C Data		PD2: I/O Pin D2
28	PC5/ADC5/SCL		INTO: External Interupt 0 Input
	PC5: I/O Pin C5		OC3B: Timer/Counter 3 - Output Compare Match B
	ADC5: ADC Input Channel 5		
	SCL: Serial - I2C Clock		
QFP32			