

TCCR1A -TC1 Control register A

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address: 0x80					Defaults: 0x00			
Bit	7	6	5	4	3	2	1	0
	COM1A1	COM1A0	COM1B1	COM1B0	-	-	WGM11	WGM10
R / W	R / W	R / W	R / W	R / W	-	- R / W		R / W
Bit	Name	description						
7	COM1A1	<p>Compare Match Output A Mode control high.</p> <p>COM1A1 with COM1A0 composition COM1A [1: 0] To control the output waveform of comparator OC1A . in case COM1A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC1A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM1A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
6	COM1A0	<p>Compare Match Output A Mode control low.</p> <p>COM1A1 with COM1A0 composition COM1A [1: 0] To control the output waveform of comparator OC1A . in case COM1A of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC1A Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM1A The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
5	COM1B1	<p>Compare Match Output B Mode control high.</p> <p>COM1B1 with COM1B0 composition COM1B [1: 0] To control the output waveform of comparator OC1B . in case COM1B of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC1B Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM1B The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
4	COM1B0	<p>Compare Match Output B Mode control low.</p> <p>COM1B1 with COM1B0 composition COM1B [1: 0] To control the output waveform of comparator OC1B . in case COM1B of 1 Position or 2 Bits are set, the output waveform of comparator occupies OC1B Pin, but the pin data direction register must be set to a high output from this waveform. In different operating modes, COM1B The control waveform output of the comparator is different, the comparison output mode control specifically see table below.</p>						
3: 2	-	Remain unchanged						
1	WGM11	<p>Waveform generation mode control times lower.</p> <p>WGM11 with WGM13, WGM12, WGM10 Together form waveform generation mode control WGM1 [3: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described.</p>						
0	WGM10	<p>Waveform generation mode control lowest bit.</p> <p>WGM10 with WGM13, WGM12, WGM11 Together form waveform generation mode control WGM1 [3: 0] , Control and counting of the counter waveform generation mode, see the specific waveform generation pattern table is described.</p>						

The following table non PWM Mode (ie, normal mode and CTC Mode), the comparison output of the comparator mode control output waveform.