



Figure 8 PCPWM Mode TC3 Dead-time control

Set up DTEN3 Bit "0" When inserting the dead time function is disabled, OC3A with OC3B The waveform of the output waveform generated by each comparator output.

Register Definition

TC3 Register List

register	address	Defaults	description
TCCR3A	0x90	0x00	TC3 Control register A
TCCR3B	0x91	0x00	TC3 Control register B
TCCR3C	0x92	0x00	TC3 Control register C
TCCR3D	0x93	0x00	TC3 Control register D
TCNT3L	0x94	0x00	TC3 Low byte count value register
TCNT3H	0x95	0x00	TC3 High byte count value register
ICR3L	0x96	0x00	TC3 Input Capture Register Low Byte
ICR3H	0x97	0x00	TC3 Input Capture MSB
OCR3AL	0x98	0x00	TC3 Output Compare Register A Low byte
OCR3AH	0x99	0x00	TC3 Output Compare Register A High Byte
OCR3BL	0x9A	0x00	TC3 Output Compare Register B Low byte
OCR3BH	0x9B	0x00	TC3 Output Compare Register B High Byte
DTR3L	0x9C	0x00	TC3 Dead Time Register Low Byte
DTR3H	0x9D	0x00	TC3 High byte dead time register
OCR3CL	0x9E	0x00	TC3 Output Compare Register C Low byte