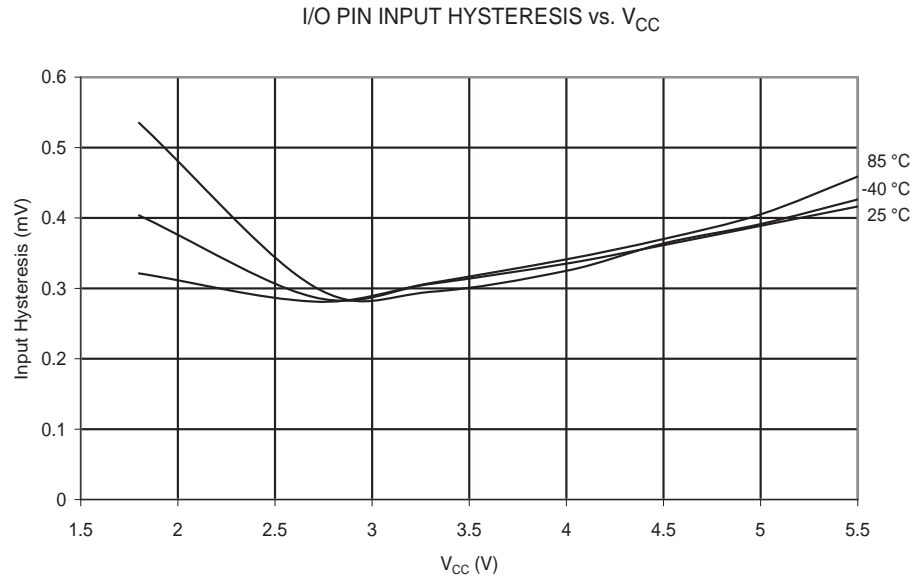


**Figure 27-27.** I/O Pin Input Hysteresis vs.  $V_{CC}$ .



**Figure 27-28.** Reset Input Threshold Voltage vs.  $V_{CC}$  ( $V_{IH}$ , I/O Pin read as '1').

