

**ADCSRB - ADC Control and status registers B**

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address: 0x7B		Defaults: 0x00						
Bit	7	6	5	4	3	2	1	0
Name	CME01	CME00	CME11	CME10	ACTS	ADTS2 ADTS1 ADTS0		
R / W	R / W	R / W	R / W	R / W	R / WR / W		R / W	R / W
Bit	Name description							
7	CME01 AC0	Negative input selection, CME0 = {CME01, CME00}						
6	CME00	00: External ports ACXN As a AC0 Negative input 01: ADC As a multiplexed output AC0 Negative input 10: As the output of the differential amplifier AC0 Negative input 11: shut down AC0 The negative input source						
5	CME11 AC1	Negative input selection, CME1 = {CME11, CME10}						
4	CME10	00: External ports ACXN As a AC1 Negative input 01: External ports AC1N As a AC1 Negative input 10: ADC internal 1/5 As the partial pressure AC1 Negative input 11: Differential op amp's output as AC1 Negative input						
3	ACHS	AC Trigger source channel selection 0 - AC0 Output as ADC The automatic conversion trigger 1 - AC1 Output as ADC The automatic conversion trigger						
2: 0	ADTS see ADC Register description.							

**C0XR - AC0 Auxiliary Control Register**

<b>C0XR - AC0 Auxiliary Control Register</b>								
address: 0x51		Defaults: 0x00						
Bit	7	6	5	4	3	2	1	0
Name	-	C0OE	C0HYSE	C0PS0 C0WKE C0FEN C0FS1 C0FS0				
R / W	-	R / W	R / W	R / W	R / WR / W		R / WR / W	
Bit	Name	description						
7	-	Retention						
6	C0OE	AC0 The comparator output to an external enable control port C0OE = 1, AC0 The comparator output to an external port PD2 C0OE = 0, Prohibit comparator output to an external port						
5	C0HYSE AC0	Output enable control hysteresis function. 1 = Enable output hysteresis 0 = Disable output hysteresis						
4	C0PS0	AC0 The positive terminal of the input source selected low. C0PS0 versus C0BG Joint control AC0 The positive terminal of the input source, refer to C0SR Register Definition						
3	C0WKE AC0	For the wake-up can be controlled. 1 = Enables the comparator output wake-up function						