

Partial influence on the analog circuit. For details, see ADC The relevant sections.

PCINT13: Pin Change Interrupt 13

SDA / ADC4 / PCINT12- port C Pin 4

SDA: TWI Interface data signal. TWCR Register TWEN position 1 After enabling TWI interface, PC4 will be TWI Control, become TWI Data signal interface.

ADC4: ADC Input channel 4 . DIDR Close register number of multiplexed analog I / O The digital functions, to avoid interference with the digital part of the analog circuit. For details, see ADC The relevant sections.

PCINT12: Pin Change Interrupt 12

ADC3 / APN1 / PCINT11- port C Pin 3

ADC3: ADC Input channel 3 . DIDR Close register number of multiplexed analog I / O The digital functions, to avoid interference with the digital part of the analog circuit. For details, see ADC The relevant sections.

APN1: Inverting input of the differential amplifier 1

PCINT11: Pin Change Interrupt 11

ADC2 / APN0 / PCINT10- port C Pin 2

ADC2: ADC Input channel 2 . DIDR Close register number of multiplexed analog I / O The digital functions, to avoid interference with the digital part of the analog circuit. For details, see ADC The relevant sections.

APN0: Inverting input of the differential amplifier 0

PCINT10: Pin Change Interrupt 10

ADC1 / APP1 / PCINT9- port C Pin 1

ADC1: ADC Input channel 1 . DIDR Close register number of multiplexed analog I / O The digital functions, to avoid interference with the digital part of the analog circuit. For details, see ADC The relevant sections.

APP1: The positive input of the differential amplifier 1

PCINT9: Pin Change Interrupt 9

ADC0 / APP0 / PCINT8- port C Pin 0

ADC0: ADC Input channel 0 . DIDR Close register number of multiplexed analog I / O The digital functions, to avoid interference with the digital part of the analog circuit. For details, see ADC The relevant sections.

APP0 : Differential amplifier positive input 0

PCINT8: Pin Change Interrupt 8