3	PC3D	1 = shut down PC3 Digital Input Function
2	PC2D	1 = shut down PC2 Digital Input Function
1	PC1D	1 = shut down PC1 Digital Input Function
0	PC0D	1 = shut down PC0 Digital Input Function

DIDR1 - Digital Input Disable Control Register 1

DIDR1 - Digital Input Disable Control Register 1								
address: 0x7F						Defaults: 0x00		
Bit	7	6	5	4	3	2	1	0
Name	PE7D	PE6D	PE0D	C0PD	PF0D	PC7D	PD7D	PD6D
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W

Bit	Name	description
0	PD6D	1 = shut down PD6 Digital Input Function
1	PD7D	1 = shut down PD7 Digital Input Function
2	PC7D	1 = shut down PC7 Digital Input Function
3	PF0D	1 = shut down PF0 Digital Input Function
4	C0PD	1 = shut down AC0P Digital input function (LQFP48)
5	PE0D	1 = shut down PE0 Digital Input Function
6	PE6D	1 = shut down PE6 Digital Input Function
7	PE7D	1 = shut down PE7 Digital Input Function

ADCSRD - ADC Control register D

ADCSRD - ADC Control register D								
address: 0xAD						Defaults: 0x00		
Bit	7	6	5	4	3	2	1	0
Name	BGEN	REFS2	IVSEL1	IVSEL0	-	VDS2	VDS1	VDS0
R / W	R / W	R / W	R / W	R / W	- R / W		R / W	R / W

Bit	Name	description
7		BGEN Internal Reference Global enable control, 1 = Enable
6		REFS2 versus ADMUX Register REFS For selecting a combination of ADC Conversion reference voltage Please refer to ADMUX Register REFS Definition
5: 4		IVSEL when ADC The reference voltage selected VCC or AVREF , IVSEL For controlling the output of the internal reference Voltage: 00 = 1.024V 01 = 2.048V 1x = 4.096V
3	-	Retention
2: 0 VDS [2: 0] Dividing the input source selection circuit		000/111 = Close dividing circuit module 001 = ADC0 010 = ADC1 011 = ADC4