TMR0 / 1/3 Prescaler

- 3 More 10 Bit prescaler
- Multiplexing mode TC0 , TC1 with TC3 Multiplexing prescaler CPS310
- Stand-alone mode TC0 Alone with prescaler CPS310, TC1 Alone with prescaler CPS1, TC3 Alone with prescaler CPS3
- Support software reset

Outline

Multiplexing mode (PSS1 = 0 And PSS3 = 0), TC0 , TC1 with TC3 A share 10 Bit prescaler CPS310 But they have a different set of frequency division.

Alone mode (PSS1 = 1 And PSS3 = 0), TC1 Use a separate 10 Bit prescaler CPS1 , TC0 with TC3

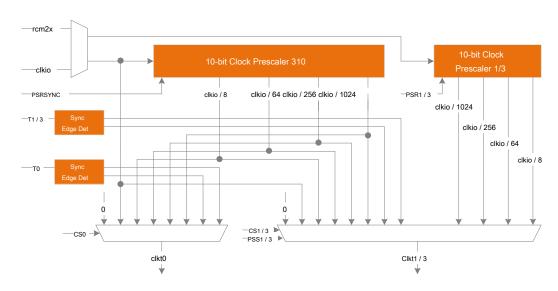
A share 10 Bit prescaler CPS310 But they have a different set of frequency division.

Alone mode (PSS1 = 0 And PSS3 = 1), TC3 Use a separate 10 Bit prescaler CPS3 , TC0 with TC1

A share 10 Bit prescaler CPS310 But they have a different set of frequency division.

Stand-alone mode (PSS1 = 1 And PSS3 = 1), TC0 Use a separate 10 Bit prescaler CPS310 , TC1 Use a separate 10 Bit prescaler CPS1 , TC3 Independently prescaler CPS3 .

The following description uses in TC0 , TC1 with TC3 , among them n representative $\bf 0$, $\bf 1$ or $\bf 3$.



TC0 / TC1 / TC3 Prescaler Structure chart

Internal clock source

When set CSn [2: 0] = 1 When the timer 3 Only by the system clock clkio Driver, timer counter 0 or 1 Directly by the system clock clkio Or high-speed clock rcm2x (internal 32M RC Oscillator output clock 2 Octave) drive. Prescaler can output 4 Different clock frequencies, respectively, clkio / 8, clkio / 64, clkio / 256 with clkio / 1024.