Addr	Name	Bit7	Bit6	Bit5	Bit4	Bit3	Bit2	Bit1	Bit0
$ B9	TWSR	TWI Status bits					-	TWPS	
$ B8	TWBR	TWI Bit Rate register							
$ B6	ASSR	INTCK	-	AS2	TCN2UB	OCR2AUB	OCR2BUB	TCR2AUB	TCR2BUB
$ B4	OCR2B	Timer 2 Output Compare Register B							
$ B3	OCR2A	Timer 2 Output Compare Register A							
$ B2	TCNT2	Timer 2 Counter Register							
$ B1	TCCR2B	FOC2A	FOC2B	-	-	WGM22	CS2		
$ B0	TCCR2A	COM2A1	COM2A0	COM2B1	COM2B0	-	-	WGM21	WGM20
$ AF	DPS2R	-	-	-	-	DPS2E	LPRCE	TOS1	TOS0
$ AE	IOCWK	IOCD7	IOCD6	IOCD5	IOCD4	IOCD3	IOCD2	IOCD1	IOCD0
$ AD	ADCSRD	BGEN	REFS2	IVSEL1	IVSEL0	-	VDS2	VDS1	VDS0
$ AC	ADMSC	AMOF	-	-	-	AMFC3	AMFC2	AMFC1	AMFC0
$ AB	ADT1H	ADC Auto-monitor Overflow threshold high byte							
$ AA	ADT1L	ADC Auto-monitor Overflow threshold low byte							
$ A9	PORTE	Port Output E (for compatible with LGT8FX8D)							
$ A8	DDRE	Data Direction E (for compatible with LGT8FX8D)							
$ A7	PINE	Port Input E (for compatible with LGT8FX8D)							
$ A6	ADT0H	ADC Auto-monitor Underflow threshold high byte							
$ A5	ADT0L	ADC Auto-monitor Underflow threshold low byte							
$ A4	OFR1	ADC positive offset trimming							
$ A3	OFR0	ADC negative offset trimming							
$ A1	DALR	DAC data register							
$ A0	DACON	-	-	-	-	DACEN	DAOE	DAVS1	DAVS0
$ 9F	OCR3CH	Compare output register high byte of Timer3 C channel							
$ 9E	OCR3CL	Compare output register low byte of Timer3 C channel							
$ 9D	DTR3H	Dead-band register high byte of Timer3							
$ 9C	DTR3L	Dead-band register low byte of Timer3							
$ 9B	OCR3BH	Compare output register high byte of Timer3 B channel							
$ 9A	OCR3BL	Compare output register low byte of Timer3 B channel							
$ 99	OCR3AH	Compare output register high byte of Timer3 A channel							
$ 98	OCR3AL	Compare output register low byte of Timer3 A channel							
$ 97	ICR3H	Input capture register high byte of Timer3							
$ 96	ICR3L	Input capture register low byte of Timer3							
$ 95	TCNT3H	Counter register high byte of Timer3							
$ 94	TCNT3L	Counter register low byte of Timer3							
$ 93	TCCR3D	Control register D of Timer3							
$ 92	TCCR3C	Control register C of Timer3							
$ 91	TCCR3B	Control register B of Timer3							
$ 90	TCCR3A	Control register A of Timer3							
$ 8D	DTR1H	Dead-band register high byte of Timer1							
$ 8C	DTR1L	Dead-band register low byte of Timer1							
$ 8B	OCR1BH	Timer 1 Output Compare B High							