

39	27	19	PC4 / ADC4 / SDA PC4: Programmable port C4
			ADC4: ADC Analog input channels 4 SDA: I2C Data line controller
40	28	20	PC5 / ADC5 / SCL PC5: Programmable port C5
			ADC5: ADC Analog input channels 5 SCL: I2C The controller clock line
41	29	1	PC6 / RESETN PC6: Programmable
			port C6 RESETN: External reset input
42	-	-	PC7 / ADC8 / APN2 PC7: Programmable port C7 ADC8:
			ADC Analog input channels 8 APN2: Differential amplifier inverting input channels 2
43	-	-	PF0 / ADC9 / APN3 PF0: Programmable port F0 ADC9:
			ADC Analog input channels 9 APN3: Differential amplifier inverting input channels 3
44	30	-	PD0 / RXD PD0: Programmable port D0
			RXD: USART Receiving input data
45	31	-	PD1 / TXD PD1: Programmable port D1
			TXD: USART Data transmission output
46	31	1	PF1 / OC3A PF1: Programmable port F1 OC3A: Timer 3
			Compare Match Output A
47	32	2	PD2 / INT0 / AC00 PD2: Programmable
			port D2 INT0: External interrupt input 0 AC00: Analog comparator 0 Export
48	32	2	PF2 / OC3B PF2: Programmable port F2 OC3B: Timer 3
			Compare Match Output B