

Figure 27-73. I/O Pin Input Hysteresis vs. V_{CC} .

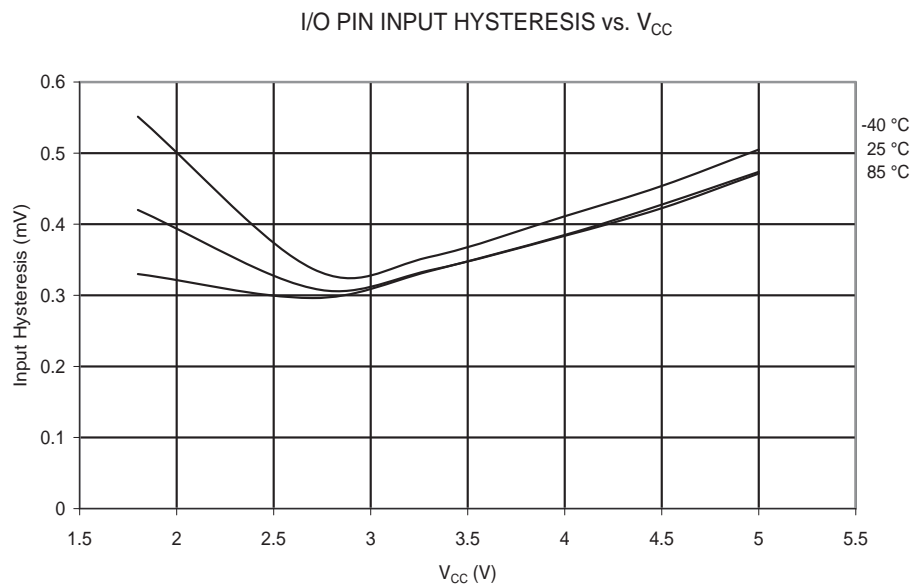


Figure 27-74. Reset Input Threshold Voltage vs. V_{CC} (V_{IH} , I/O Pin read as '1').

