

R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
<i>Bit Definitions</i>								
[7: 0]	PINE	E Group port status register read PINE Get the current port-level state to write PINEn for 1 Flip PORTEn Bit status						

port F Output register - PORTF

PINF - port F Input data register								
PORTF: 0x14 (0x34)					Defaults: 0x00			
Bits	PF7	PF6	PF5	PF4	PF3	PF2	PF1	PF0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
<i>Bit Definitions</i>								
[7: 0]	PORTF	F Port group input port status register mode, the corresponding bit write 1 The open port of the internal pull-up output mode, corresponding to the write bit 1 The driven high						

port F Direction Control Register - DDRF

DDRF - port F Direction Control Register								
DDRF: 0x13 (0x33)					Defaults: 0x00			
Bits	DDF7	DDF6	DDF5	DDF4	DDF3	DDF2	DDF1	DDF0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
<i>Bit Definitions</i>								
[7: 0]	DDRF	F Port Direction control register group						

port F Status Register - PINF

PINF - port F Status Register								
PINF: 0x12 (0x32)					Defaults: 0x00			
Bits	PINF7	PINF6	PINF5	PINF4	PINF3	PINF2	PINF1	PINF0
R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W	R / W
<i>Bit Definitions</i>								
[7: 0]	PINF	F Group port status register read PINF Get port F The current level of the state PINFn write 1 Flip PORTFn State of the corresponding bit						

Drive control register Port - HDR

HDR0 - Port drive control register								
HDR: 0xE0					Defaults: 0x00			
Bit	-	-	HDR5	HDR4	HDR3	HDR2	HDR1	HDR0
R / W	-	-	R / W	R / W	R / W	R / W	R / W	R / W
<i>Bit Definitions</i>								