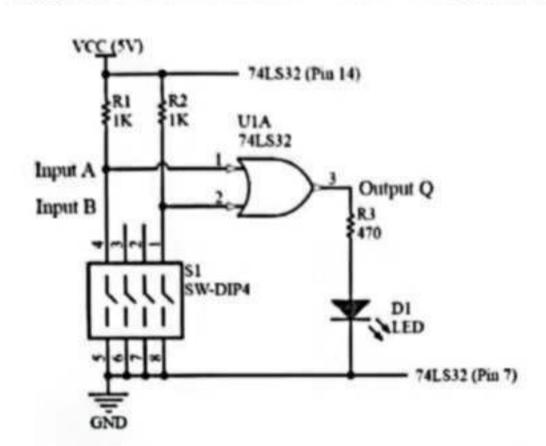
Name:	.SID:	
Name:		

Experiment 1: Familiarization of simple logic circuits

Refer to Pages 3 to 6 and watch the youtube clip for breadboard fundamentals.

74LS32:

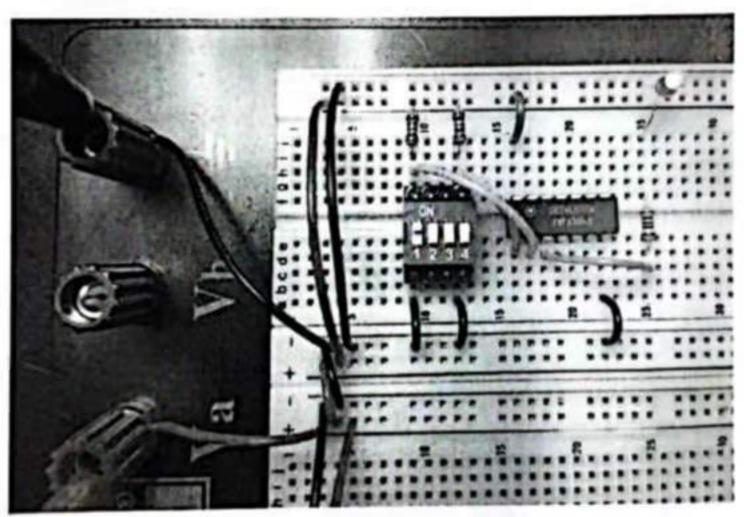
Connect the circuit as shown below schematic diagram and complete the logic table.



74L532

Input		Output
Α	В	Q
0	0	0
0	1	
1	0	
1	1	1

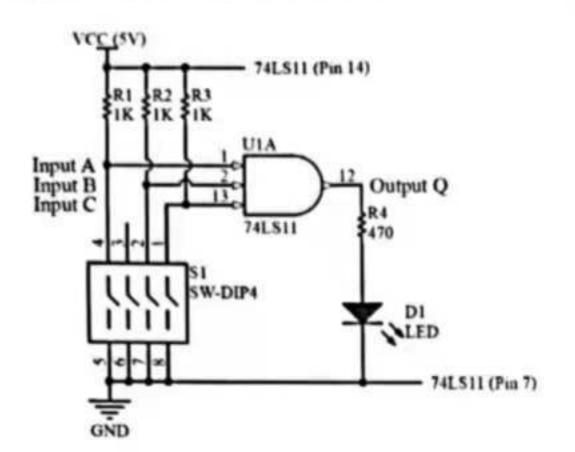
W:



Name:______.SID:_____

74LS11:

Connect the circuit as shown below schematic diagram and complete the logic table.

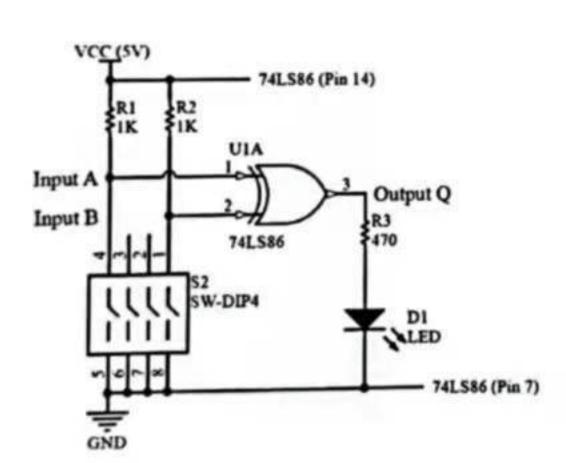


Input			Output
Α	В	С	Q
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

Wor

74LS86:

Connect the circuit as shown below schematic diagram and complete the logic table.



74L586

Input		Output
Α	В	Q
0	0	0
0	1	1
1	0	1
1	1	D

WEL

Show your results to Laboratory Supervisor.

Items	Grade	
Attitude and progress (1 - 10)		
Quality of implementation (1 - 10)		
Completion % (0% – 100%)		
Supervisor Signature		

