

Homework solution

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1 Autoregressive Filter

This section is going to present my solution for exercise 1. The implementation code can be found in the attached folder.

The formula of an autoregressive filter is as follows:

$$y_z = x_z + \sum_{i=0}^{N-1} a_i \times y_{z-i-1}$$

Here two assumptions as following are made:

1. Let a be a bounded sequence of length $N = 5$; i.e., $a \in \{j \rightarrow V\}$ and $a_j = 0$ for $j < 0$ and $j \geq N$.
2. Let h be a bounded sequence of length $z_{end} = 9$; i.e., $y \in \{j \rightarrow V\}$ and $y_j = 0$ for $j < 0$ and $j \geq z_{end}$.

Then we can derive all boundaries from the conditions and assumptions:

$a_i y_{z-i-1} = 0$ for $i < 0, z - i - 1 < 0, i > N - 1$ and $z - i - 1 > z_{end} - 1$.

With these limitations, the dependency graph can be drawn.

1.1 Dependency graph

A basic cell of the dependency graph is showed as below, where $S_{z,i} = S_{z,i-1} + a_i \times y_{z-i-1}$ and $S_{z,-1} = x_z$. The addition and multiplication are the operations performed in the basic cell. They form to a processing element.

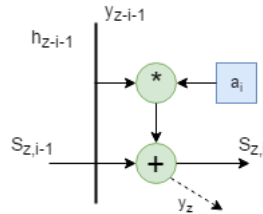


Figure 1: basic cell

Based on the basic cell, we can have a dependency graph as below (figure 2).

1.2 Scheduling vectors range

There are some restrictions on choosing \vec{s} and \vec{d} . The equation $\vec{s} \cdot \vec{d} \neq 0$ should be satisfied which is easy to understand as it is impossible to project equi-time zone operations on a single processing element. Additionally, the scheduling vector expresses the order of the operations will be executed in the PE of SFG. Since the operation will not depend on future's operation, the angle between scheduling vector \vec{s} and edges \vec{e} should be less than 90 degrees which can be expressed as $\vec{s}^T \cdot \vec{e} > 0$. And for the streaming environment, the condition $\vec{s}^T \cdot \vec{d} > 0$ is also

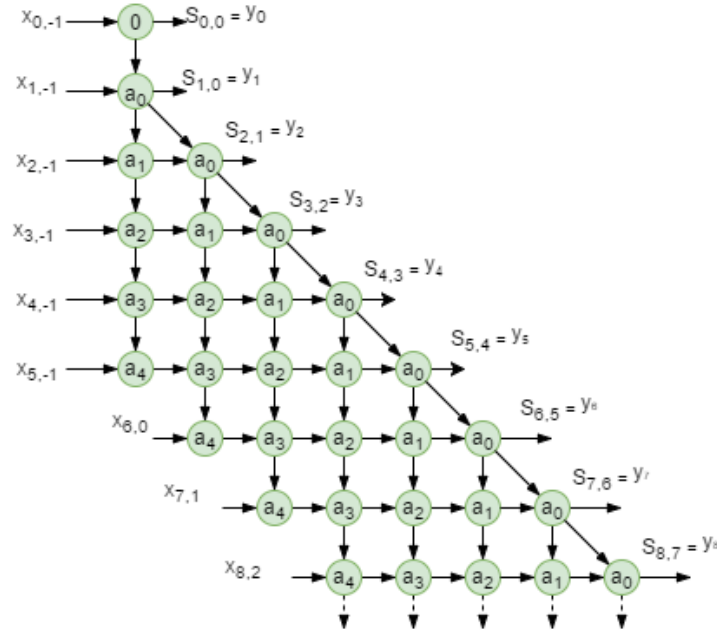


Figure 2: Dependency graph

required. However, with our initial assumption, the number of inputs and outputs is finite. They can be mapped in any order on various inputs in the SFG. Therefore, only $\vec{s}^T \cdot \vec{e} > 0$ is necessary to observe. In this dependency graph, only vector \vec{e}_1 , vector \vec{e}_2 and vector \vec{e}_3 are existed, so the scheduling vectors can be picked up within the range showed in figure 3 where the blue part is the valid range for scheduling vectors.

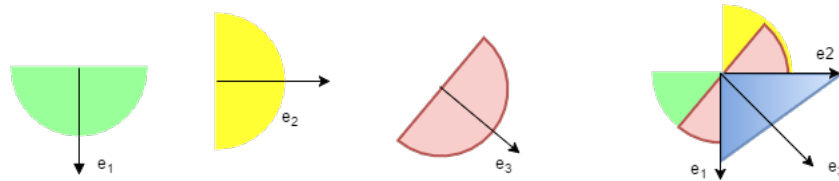


Figure 3: Scheduling vectors range

1.3 Equi-time zones

Theoretically, we can randomly pick up a scheduling vector within the range derived above. While here two vectors $(1, 1)$ and $(1, 0)$ are chosen for the easier calculation. The dependency graph with different scheduling vectors are showed in figure 4. The red dash line separates equi-time zones for scheduling vector $(1, 0)$. While blue dash line separates equi-time zones

for scheduling vector $(1, 1)$.

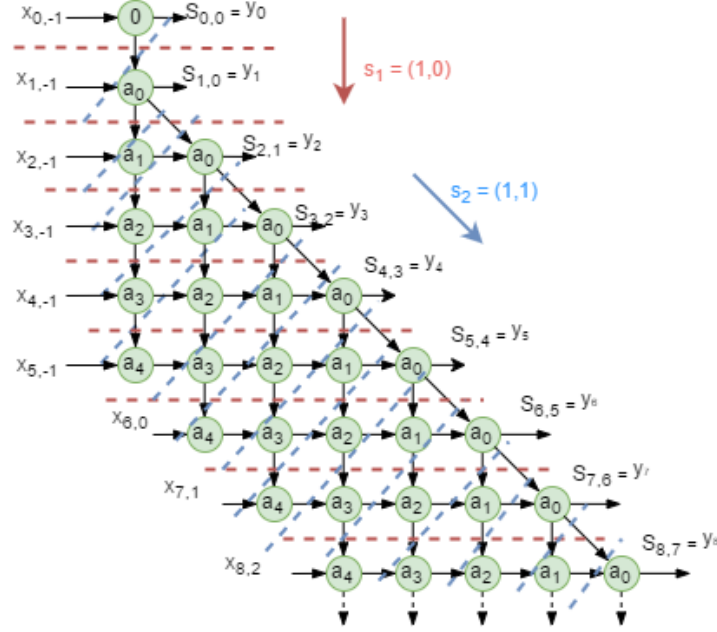


Figure 4: Dependency graph with scheduling vectors

1.4 Signal flow graphs

The projection vector $\vec{d} = (1, 1)$ is adopted. Then we can project all processing elements on the hyper-plane which is perpendicular to \vec{d} . And with different scheduling vectors, different delay units and annotations can be obtained by formulas below:

$$\vec{W}_{source} = P \cdot \vec{v}_{source} \quad n = \vec{s} \cdot \vec{v}_{dest} - \vec{v}_{source} \quad t = \vec{s} \cdot \vec{v}_{source}$$

The right side part of figure 5 shows the signal flow graphs with scheduling vector $\vec{s} = (1, 1)$. While the left side shows the signal flow graphs with scheduling vector $\vec{s} = (1, 0)$.

1.5 Haskell implementation

Figure 6 shows the code of implementation in Haskell for scheduling vector $\vec{s} = (1, 0)$. A **dotProduct** function is defined and a **sim** function is used to simulate the **dotProduct** function. The *ys* is the initial state, the *inp* is the input value, and *coefs* is a list of coefficients for the filter.

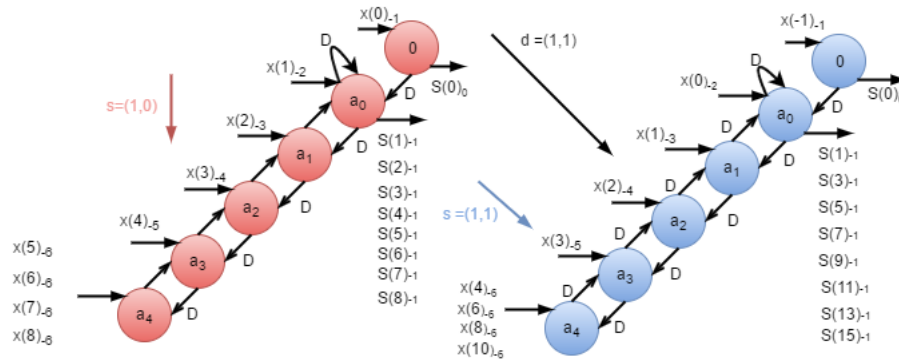


Figure 5: Signal flow graphs with different scheduling vector

```

1 module Autoregressive where
2 import Data.List
3 --dotProduct function
4 --inp: input, coefs: coefficient, ys: output stream
5 --When the length of output is larger or equal than 9, then the list
6 --will be emptied, and restart the calculation from y0.
7
8 dotProduct ys ( inp, coefs) |length ys <= 9 = (z,zs)
9                               |otherwise = dotProduct [] ( inp, coefs)
10                               where
11                                 ws = zipWith (*) coefs (take 5 ys)
12                                 z = foldl (+) inp ws
13                                 zs = z:ys
14
15 --sim recursively calls dotProduct
16
17 sim dotProduct ys (x,[]) = []
18 sim dotProduct ys ([],xs) = []
19 sim dotProduct ys (inp:inps, coefs)
20   = result: sim dotProduct zs (inps,coefs)
21   where
22     (outp,zs) = dotProduct ys (inp,coefs)
23     result = outp
24

```

Figure 6: Haskell code screenshot

1.6 Simulation result

During the simulation, the initial state is assigned an empty list , the *inps* is assigned a input stream $concat[[-4..4] + reverse([-3..3)) | i < -[1..]]$ and the *coefs* is assigned a coefficient list $[-0.4, 0.1, 0.6, 0.1, -0.4]$. Figure 7 shows the simulation results.

the valid data bits are signaled to adder. For example, the *mul0* only signals 31st bit and previous 11 bits. While the left 4 bits are connected to ground which makes the original number $2^4 = 16$ times larger. By doing in this way, four multipliers are reduced. So the RTL-schematic of this variant is simpler than the others.

```

1 module Polynomial where
2 import CLaSH.Prelude
3
4 polynomial :: (Default a, SaturatingNum a)
5   => Vec 5 (Signal a) -> Signal a -> Signal a
6
7 polynomial coeffs x = foldl (+) 0 (zipWith(*) coeffs (x^4:>x^3:>x^2:>x:>1:>Nil))
8
9 topEntity :: Signal (Signed 16) -> Signal (Signed 16)
10 topEntity = polynomial (16:>8:>4:>2:>1:>Nil)

```

Figure 10: Clash code with variant (16, 8, 4, 2, 1)

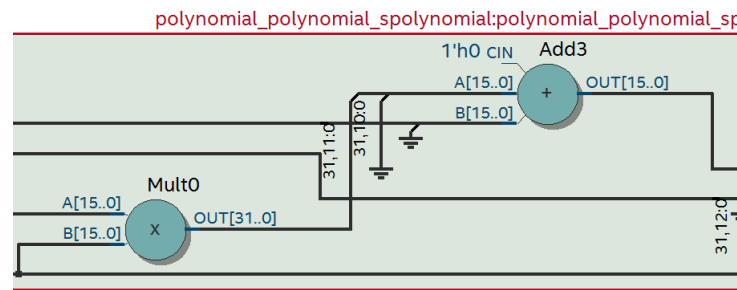


Figure 11: Quartus optimization

2.3 Use of tuple

The co-efficients are defined as a five elements' tuple. Figure 12 shows the definition of *topEntity* variable with concrete instantiation for the co-efficients while figure 13 shows the definition of *topEntity* variable without the concrete instantiation.

```

1 module Polynomial where
2 import CLaSH.Prelude
3
4 polynomial :: (Default a, SaturatingNum a)
5   => (Signal a, Signal a, Signal a, Signal a, Signal a) -> Signal a -> Signal a
6
7 polynomial (a4,a3,a2,a1,a0) x = foldl (+) 0 (a4*x^4:>a3*x^3:>a2*x^2:>a1*x:>a0:>Nil)
8
9 topEntity :: Signal (Signed 16) -> Signal (Signed 16)
10 topEntity = polynomial (31,15,7,3,1)

```

Figure 12: Use of tuple with instantiation

```

1 module Polynomial where
2 import CLaSH.Prelude
3
4 polynomial :: (Default a, SaturatingNum a)
5   => (Signal a, Signal a, Signal a, Signal a, Signal a) -> Signal a -> Signal a
6
7 polynomial (a4,a3,a2,a1,a0) x = foldl1 (+) 0 (a4*x^4:>a3*x^3:>a2*x^2:>a1*x:>a0:>Nil)
8
9 topEntity :: (Signal (Signed 16), Signal (Signed 16), Signal (Signed 16), Signal (Signed 16), Signal (Signed 16))
10   => Signal (Signed 16) -> Signal (Signed 16)
11 topEntity = polynomial
    
```

Figure 13: Use of tuple without instantiation

2.4 Mathematical equivalence

Table 1 shows three other equivalences that are discussed and compared with the original one.

Equivalence	DSP block usage
$a_4x^4 + a_3x^3 + a_2x^2 + a_1x + a_0$	14 blocks
$(a_4x^3 + a_3x^2 + a_2x^1 + a_1)x + a_0$	12 blocks
$(a_4x^2 + a_3x + a_2)x^2 + a_1x + a_0$	10 blocks
$(a_4x + a_3)x^3 + a_2x^2 + a_1x + a_0$	12 blocks

Table 1: equivalences and their resource usage

Figure 1 shows the three equivalence and their FPGA resource usage. The original equivalence make use of 14 blocks while other equivalences use less resource. The third equivalence $(a_4x^3 + a_3x^2 + a_2x^1 + a_1)x + a_0$ only uses 10 DSP blocks. The RTL-schematics shows the reason why these variants are structurally different. The fact is that, by factorizing equivalence, the $16b * 16b$ multiplication is reduced. For instance, the implementation of equivalence $a_4x^4 + a_3x^3 + a_2x^2 + a_1x + a_0$ requires 7 multiplications namely $x * x$, $x^2 * x$, $x^3 * x$, $a_4 * x^4$, $a_3 * x^3$, $a_2 * x^2$ and $a_1 * x$. While that of equivalence $(a_4x^2 + a_3x + a_2)x^2 + a_1x + a_0$ only requires 5 multiplications namely $x * x$, $a_4 * x^2$, $a_3 * x$, $(a_4x^2 + a_3x + a_2) * x^2$ and $a_1 * x$. While the other two equivalences both require 6 multiplications respectively. The fewer multiplication operations occupies fewer DSP blocks. So we can observe that the generated structures can be different even the variants are equivalent mathematically. A more efficient mathematical description of a architecture sometimes leads to a structure with better efficiency.

2.5 Implementation by Higher order function

The *foldl* and *zipWith* are used in this version of code. Figure 14 shows the implementation. The RTL-schematic is in the attached folder.

```

1 module Polynomial where
2 import CLaSH.Prelude
3
4 polynomial :: (Default a, SaturatingNum a)
5   => (Signal a, Signal a, Signal a, Signal a, Signal a) -> Signal a -> Signal a
6
7 polynomial (a4,a3,a2,a1,a0) x = foldl1 (+) 0 ws
8   where ws = zipWith (*) (a4:>a3:>a2:>a1:>a0:>Nil) (x^4:>x^3:>x^2:>x:>1:>Nil)
9
10 topEntity :: (Signal (Signed 16), Signal (Signed 16), Signal (Signed 16), Signal (Signed 16), Signal (Signed 16))
11   => Signal (Signed 16) -> Signal (Signed 16)
12 topEntity = polynomial
    
```

Figure 14: Implementation by Higher order function