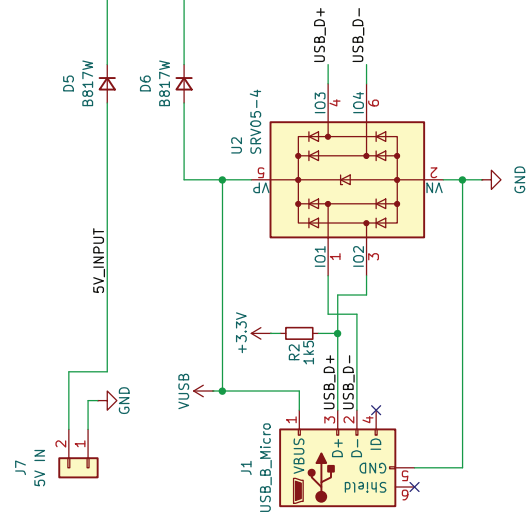
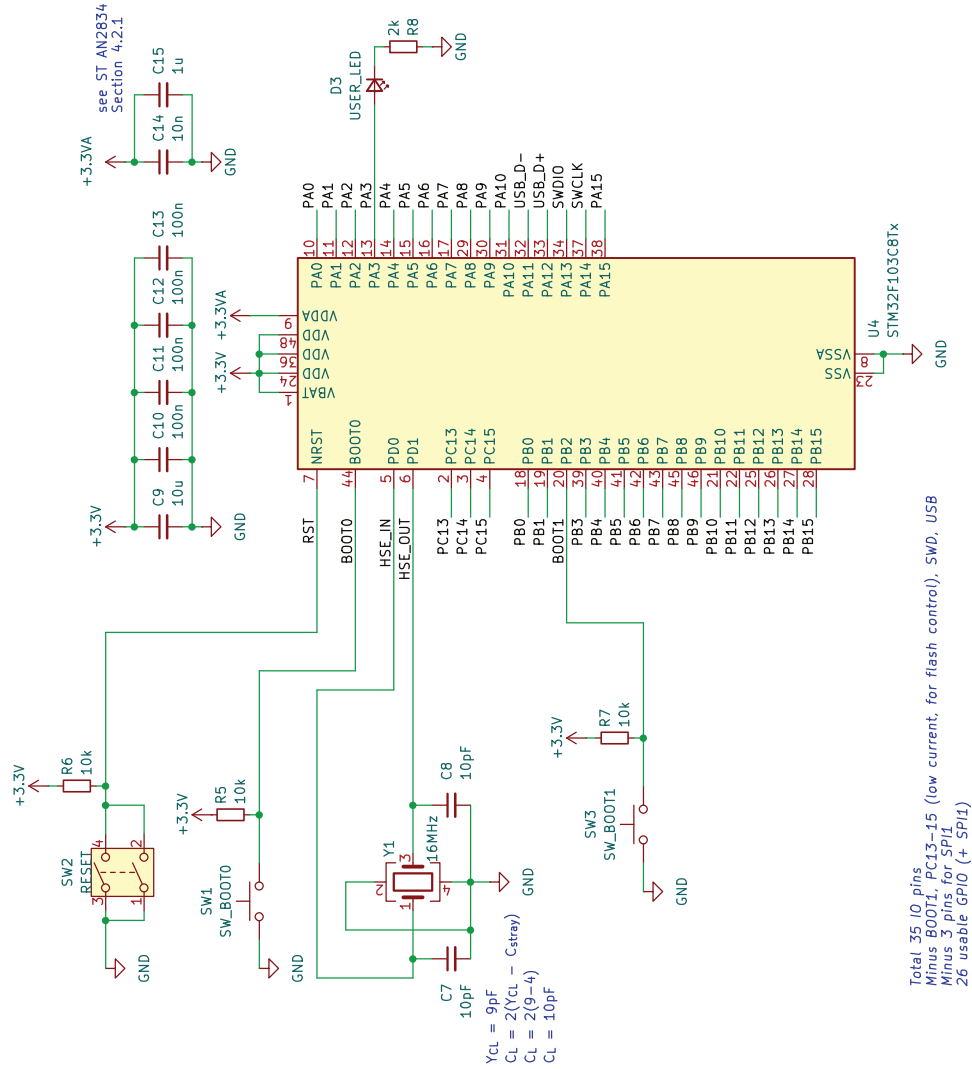


POWER FILTERING AND CONVERSION

With USB and SPI flash

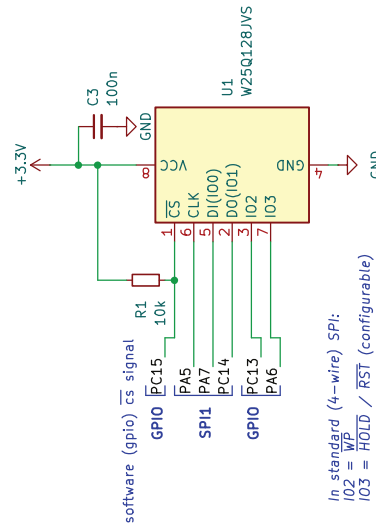


Stackup is 4 layers (JLC04161H-7628)

1. SIG
2. GND
3. GND
4. SIG

Top-side to be assembled by JLC, using only basic parts, other than the USB connector

Controlled Impedance Traces:
90-Ohm Diff: Width=0.286mm, Spacing=0.2032mm
50-Ohm Single-ended: Width=0.3493mm
All controlled traces are on L1, ref. L2, non-coplanar



ALL CAPACITORS MLCC AND 16V+ UNLESS OTHERWISE SPEC.
1.6mm FR4 BOARD WITH FOUR LAYERS (SIG-GND-GND-SIG/PWR) JLC04161H-7628

Sheet: /

File: STMF103C8T6 Dev Board.kicad_sch

Title: STM32F103C8T6 Development Board with SPI Flash

Size: A3

Size: A3	Date: 2024-02-23
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Rev: 1

KICad E.D.A. 8.0.0